AN11357

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

Rev. 1 — 27 May 2013

Application Note

Document information

Info	Content
Keywords	LNA, GNSS, GPS, BGU8009, WLAN, GSM-850, GSM-900
Abstract	This document describes an alternative input matching option for the BGU8009 GNSS LNA. This option provides additional immunity to GSM-850/900 and 2.4 GHZ Wireless LAN jammers.



BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

Revision history

Rev	Date	Description
01	20130527	First publication

Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

1. Introduction

The BGU8009 is a Low Noise Amplifier (LNA) dedicated for Global Navigation Satellite System (GNSS) receiver applications. It is offered in a plastic leadless 6-pin SOT886 package. The BGU8009 uses NXP's eighth generation 180 GHz f_T SiGe:C process. Under small signal conditions it has typical gain of 17.8 dB and typical noise figure of 0.65 dB, and can be operated at supply voltages up to 3.1V. The part contains a single RF stage and is supplied with an enable function allowing it to be controlled using logic signals. It also features temperature-stabilized bias circuitry. A product datasheet as well as an application note detailing the features of the BGU8009 evaluation board are available.

- BGU8009 Datasheet: SiGe:C Low Noise Amplifier MMIC for GPS, GLONASS, Galileo, and Compass
- Application Note BGU8009 GNSS LNA Evaluation Board (AN11288)

In the case of the baseline matching scenario for the BGU8009, only two external components are required: a decoupling capacitor on the collector feed and a low-cost series inductor for RF input matching. The output of the part is internally matched for GNSS frequencies. This application note will outline additional options for modifying the input match to provide increased immunity for the LNA in the presence of GSM-850/900 and 2.4 GHz Wireless LAN (WLAN) signals. For example, a 2410 MHz WLAN signal and an 835 MHz GSM-850 signal will cause a 1575 MHz 2nd order intermodulation product (IM2) to be generated in an active device such as an LNA. Another possible case is a 2465 MHz WLAN signal and an 890 MHz GSM-900 signal. Although this note deals specifically with the BGU8009, the techniques presented here are applicable to the entire family of NXP GNSS LNAs.

The baseline input match provides high gain, low current consumption, high linearity, and lowest noise figure. In the specific case of operating the BGU8009 in the presence of GSM and WLAN band jammers, the input match can be modified to provide additional immunity to these signals. The basic premise is to add additional low cost components to the input match in order to provide gain nulls in the 800 and 2400 MHz bands. This technique can potentially reduce or alleviate the need for relatively high cost filtering in the system.

Figure 1 below shows the broadband gain performance of the BGU8009 with baseline single element input match and a 5-element jammer immunity input match which creates the gain nulls.

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

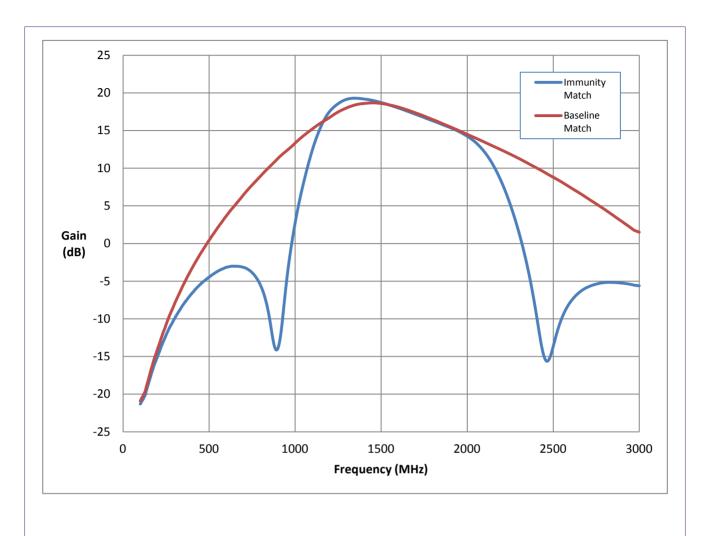


Fig 1. Broadband Gain Response Comparison for Baseline and Jammer Immunity Options BGU8009 1.8V

2. Baseline Single Element Match Performance BGU8009

The standard BGU8009 evaluation board is supplied with a Murata LQW15 series inductor (0402 size) in the input match. This type of high quality factor (Q) inductor is recommended in order to provide best noise performance. Figure 2 and Table 1 below show the schematic and bill of materials for the BGU8009 baseline circuit. The broadband gain and input/output return loss are shown in Figure 3.

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

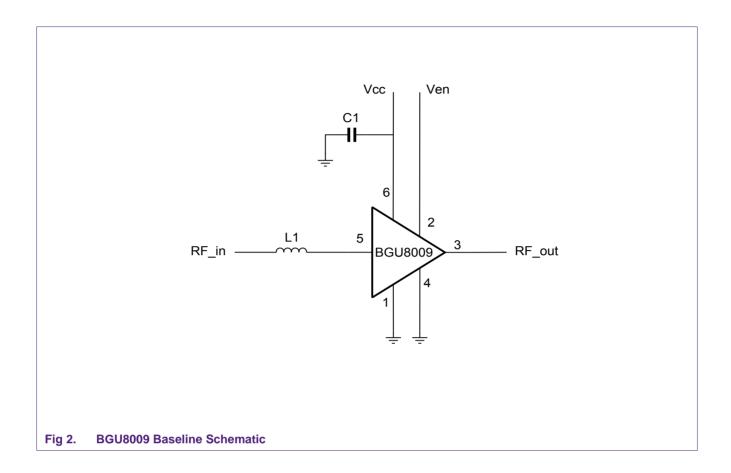
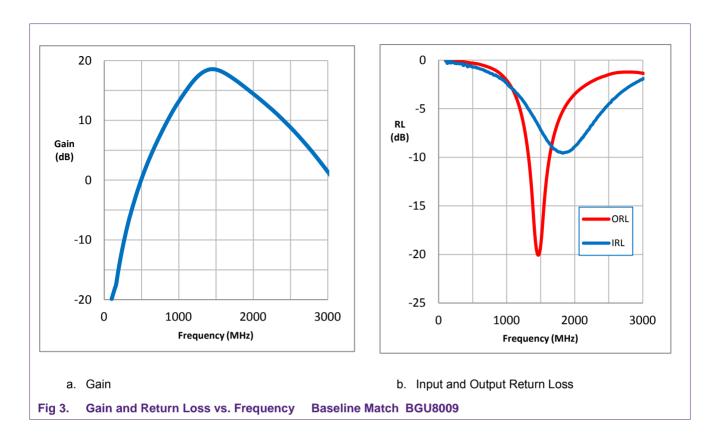


Table 1. List of Components for Baseline Input Match BGU8009

For schematic see Figure 2

Component	Description	Value	Supplier
C1	Decoupling Capacitor	1nF	Various
L1	Input Matching	5.6nH	Murata LQW15
IC	BGU8009	-	NXP

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity



At average power levels received by a GNSS receiver under normal conditions, the system will not have in-band intermodulation problems caused by the GNSS signal itself. Strong out-of-band transmit frequency jammers can cause linearity problems, however. For example, an incident 870 MHz signal along with a 2445 MHz signal can cause a 2nd order spurious product which falls in the GNSS band to be produced in the LNA.

$$f_{snur} = f_1 - f_2 \sim GNSS$$
 band

Specific to this application note, two input signals of equal amplitude at 2445 MHz and 870 MHz are applied to the input of the LNA, producing a 2nd order spurious in the GNSS band.

Figure 4 below shows the measured results of this two-tone test for the baseline BGU8009 input match. The level of the 2^{nd} order spurious product and the output level of the f_1 and f_2 fundamental products are plotted as a function of single tone input power (note that Pin1 = Pin2 for the plot).

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

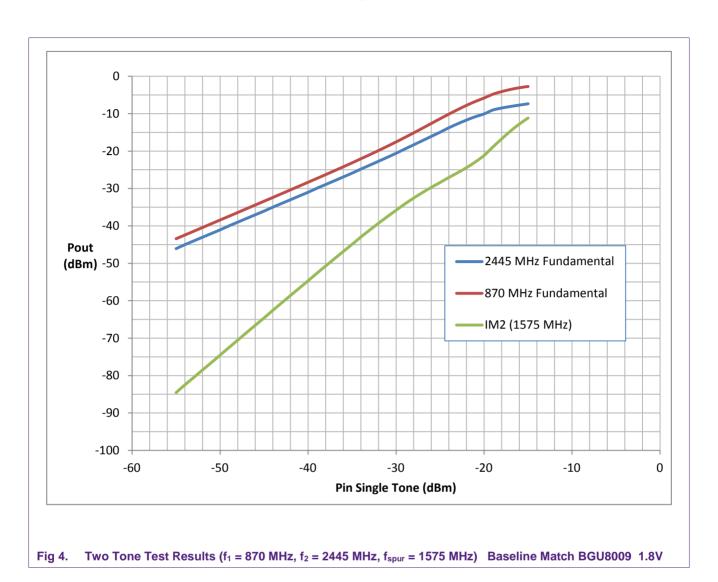


Figure 5 below shows the current draw of the BGU8009, again as a function of single tone input power (Pin1 = Pin2).

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

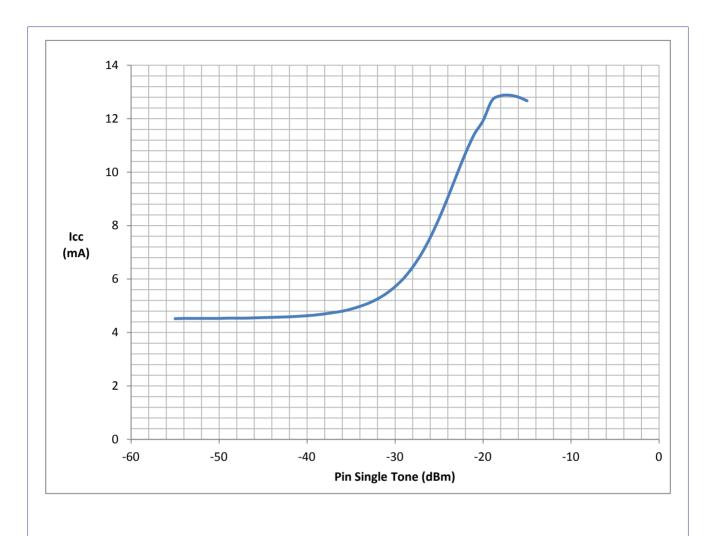
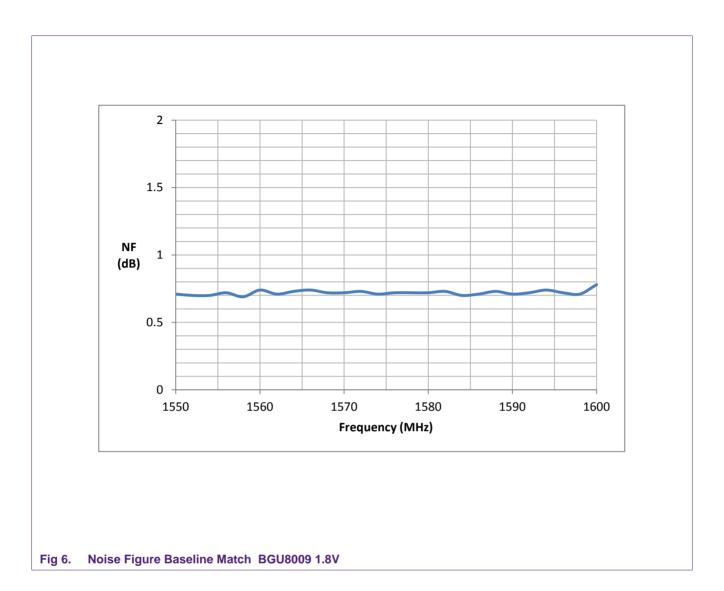


Fig 5. Two Tone Test Results (f₁ = 870 MHz, f₂ = 2445 MHz) Baseline Match BGU8009 1.8V

Figure 6 shows the GNSS-band noise figure for a typical BGU8009 sample. Note that these data are with no jammer signals present, and also include printed circuit board and SMA connector losses. De-embedding the PCB and connector will result in a NF which is 0.05 dB lower.

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity



3. Input Match for 800 MHz / 2400 MHz Jammer Immunity

To increase immunity to GSM-800 and 2.4 GHz WLAN signals, the input match can be modified to include a dual notch topology, providing gain nulls in the broadband response. The additional elements are low cost chip capacitors and low cost chip inductors. See Figure 7 and Table 2 for the schematic and bill of materials for the BGU8009 jammer immunity configuration.

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

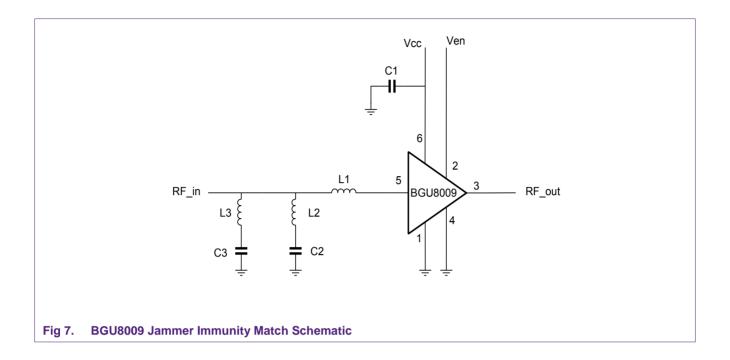


Table 2. List of Components for Jammer Immunity Input Match BGU8009

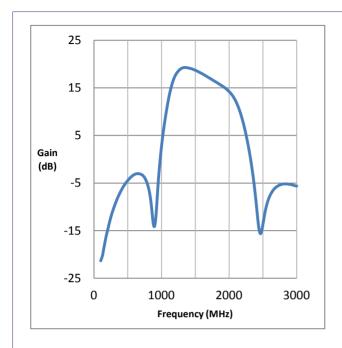
For schematic see Figure 7

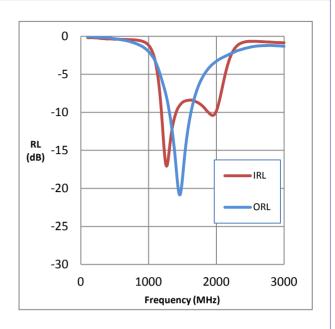
Component	Description	Value	Supplier
C1	Decoupling Capacitor	1nF	Various
L1	Input Matching	5.6 nH	Murata LQW15
L2	Notch	6.2 nH	Murata LQG15
L3	Notch	3.0 nH	Murata LQG15
C2	Notch	4.7 pF	Murata GRM15
C3	Notch	1.0 pF	Murata GRM15
IC	BGU8009	-	NXP

As can be seen in Figure 8, the jammer immunity matching topology creates gain nulls in the 870 MHz and 2445 MHz frequency regions. The gain nulls serve to reduce the level of 2^{nd} order intermodulation distortion in the GNSS band.

AN11357 **NXP Semiconductors**

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity





a. Gain vs. Frequency

b. Input and Output Return Loss vs. Frequency

Gain and Return Loss vs. Frequency Jammer Immunity Match BGU8009 1.8V

Figures 9 and 10 below show the two-tone test results with the jammer immunity match. The fundamental tones are again 870 MHz and 2445 MHz, and the results are plotted as a function of single tone input power (Pin1 = Pin2).

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

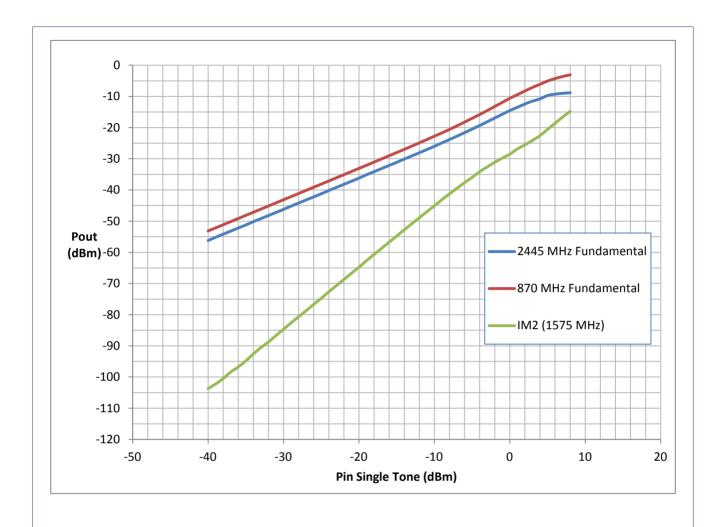


Fig 9. Two-Tone Test Results ($f_1 = 870 \text{ MHz}$, $f_2 = 2445 \text{ MHz}$, $f_{spur} = 1575 \text{ MHz}$) Jammer Immunity Match BGU8009 1.8V

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

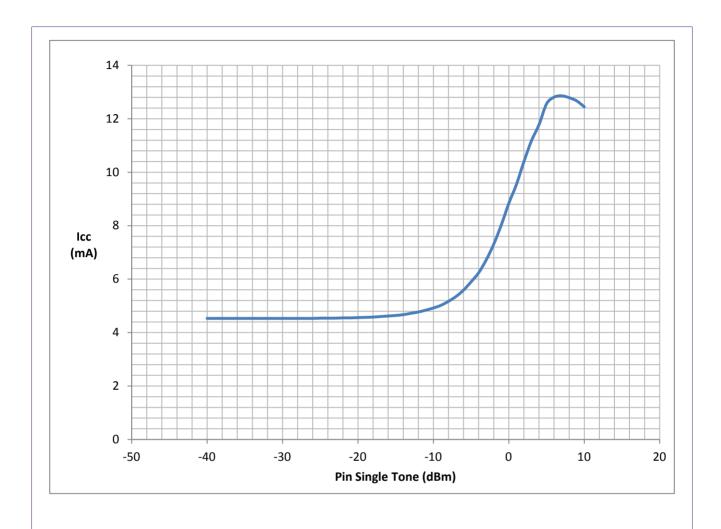


Fig 10. Two Tone Test Results (f₁ = 870 MHz, f₂ = 2445 MHz) Jammer Immunity Match BGU8009 1.8V

Figure 11 below shows a direct comparison of 1575 MHz IM2 distortion level at the output of the LNA as a function of single tone input power. The jammer immunity match results in a suppression of approximately 49 dB for the 1575 MHz IM2 product.

In terms of input 2^{nd} order intercept point, or IIP2, the 49 dB suppression of the IM2 product corresponds to a 49 dB increase in IIP2 level:

$$IIP2 = P1in + P2in - IIM2$$

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

where IIM2 is the input-referred IM2 level, and all power levels are in dBm. So, for the baseline case for -30 dBm input power level per tone,

$$IIP2 = (-30) + (-30) - (-36 - Gain[1575MHz]) = -6 dBm$$

And for the jammer immunity case,

$$IIP2 = (-30) + (-30) - (-85 - Gain[1575MHz]) = 43 dBm$$

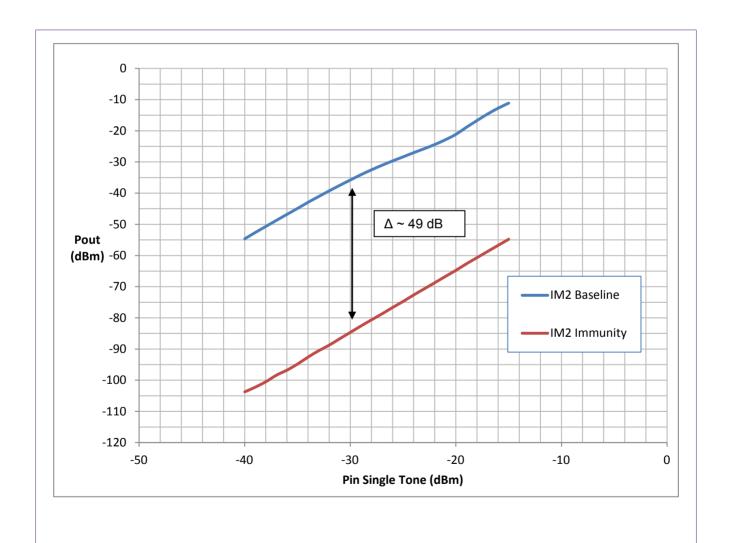


Fig 11. IM2 Comparison Jammer Immunity vs. Baseline BGU8009 1.8V

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

Figure 12 shows a comparison of the current draw as a function of the input power. As can be seen, with the jammer immunity match there is an approximately 24 dB difference of input power levels needed to reach a given current draw, once the quiescent current of the part is exceeded.

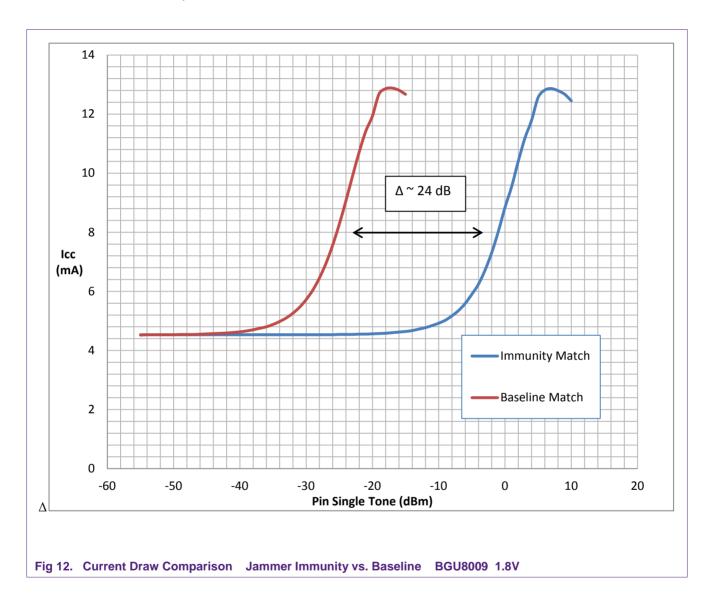
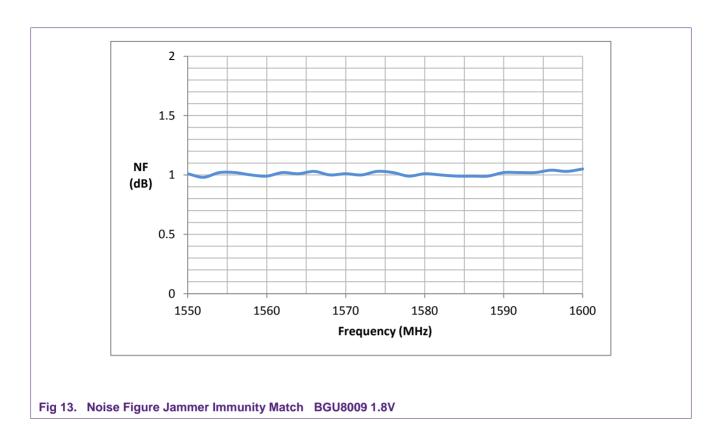


Figure 13 shows the NF with the jammer immunity match. The noise figure is degraded by 0.35 dB due to the additional elements at the input of the LNA.

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity



Also note that an LQG15 ceramic inductor can be used as the series inductor in the input match (L1). A lower cost inductor such as this can be used if it is not required to reach absolute best noise figure. Figure 14 below shows a comparison of measured NF with a 5.6 nH LQW15 inductor as L1 and a 5.6 nH LQG15 inductor as L1.

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

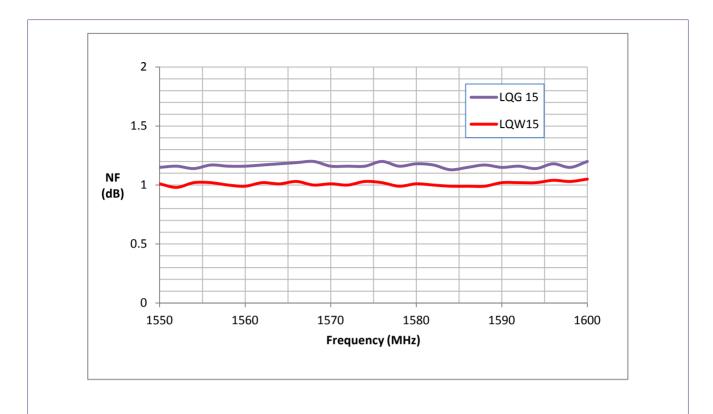


Fig 14. Noise Figure Jammer Immunity Match BGU8009 1.8V LQW15 vs. LQG15 Input Matching Inductor (L1)

4. Conclusion

By changing the input matching topology of NXP's GNSS LNAs, the gain of the circuit in the 800 MHz and 2400 MHz bands can be significantly reduced while leaving the in-band gain essentially unaltered. This can be accomplished with the addition of low cost, readily available lumped element components. This has the effect of increasing the immunity to jamming signals in these bands at the expense of noise figure, which increases slightly due to having additional components at the input of the device. To further quantify, Table 3 below show results for the BGU8009 for the case of 870 MHz and 2445 MHz jamming signals at a level of -30 dBm at the LNA input.

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

Table 3. LTE Band 2nd Harmonic BGU8009 Vcc = 1.8V IM2 Level for Pin @ -30 dBm / Tone

Matching	Gain	Gain	Gain	Input	Noise Figure*
Option	870 MHz	2445 MHz	1575 MHz	Referred IM2 Level	1575 MHz
Baseline	10.7 dB	9.5 dB	17.8 dB	-54 dBm	0.70 dB
Jammer Immunity	-12.8 dB	-14.9 dB	17.8 dB	-103 dBm	1.05 dB**

^{*} Includes board and connector losses (0.05 dB)

^{**} Noise Figure degrades by a further 0.15 dB when using an LQG15 series inductor as L1

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

5. Legal information

5.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

5.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or

customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Evaluation products — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

5.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are property of their respective owners.

AN11357 **NXP Semiconductors**

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

List of figures

Fig 1.	Broadband Gain Response Comparison for Baseline and Jammer Immunity Options4
BGU8009	1.8V4
Fig 2.	BGU8009 Baseline Schematic5
Fig 3.	Gain and Return Loss vs. Frequency Baseline Match BGU80096
Fig 4.	Two Tone Test Results (f_1 = 870 MHz, f_2 = 2445 MHz, f_{spur} = 1575 MHz) Baseline Match BGU8009 1.8V7
Fig 5.	Two Tone Test Results (f_1 = 870 MHz, f_2 = 2445 MHz) Baseline Match BGU8009 1.8V8
Fig 6.	Noise Figure Baseline Match BGU8009 1.8V9
Fig 7.	BGU8009 Jammer Immunity Match Schematic10
Fig 8.	Gain and Return Loss vs. Frequency Jammer Immunity Match BGU8009 1.8V11
Fig 9.	Two-Tone Test Results (f_1 = 870 MHz, f_2 = 2445 MHz, f_{spur} = 1575 MHz) Jammer Immunity Match BGU8009 1.8V12
Fig 10.	Two Tone Test Results (f_1 = 870 MHz, f_2 = 2445 MHz) Jammer Immunity Match BGU8009 1.8V13
Fig 11.	IM2 Comparison Jammer Immunity vs. Baseline BGU8009 1.8V14
Fig 12.	Current Draw Comparison Jammer Immunity vs. Baseline BGU8009 1.8V15
Fig 13.	Noise Figure Jammer Immunity Match BGU8009 1.8V16
Fig 14.	Noise Figure Jammer Immunity Match BGU8009 1.8V LQW15 vs. LQG15 Input Matching Inductor (L1)17

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

7. List of tables

Table 1.	List of Components for Baseline Input Match	
	BGU8009	5
Table 2.	List of Components for Jammer Immunity Inpu	ıt
	Match BGU8009	10
Table 3	LTF Band 2 nd Harmonic BGU8009	18

BGU8009 Matching Options for 850 MHz / 2400 MHz Jammer Immunity

8. Contents

Introduction	3
Baseline Single Element Match Performance BGU8009	4
Input Match for 800 MHz / 2400 MHz Jammer Immunity	9
Conclusion	.17
Legal information	.19
Definitions	.19
Disclaimers	.19
Trademarks	.19
List of figures	.20
List of tables	.21
Contents	.22
	Baseline Single Element Match Performance BGU8009 Input Match for 800 MHz / 2400 MHz Jammer Immunity Conclusion Legal information Definitions Disclaimers Trademarks List of figures List of tables

Please be aware that important notices concerning this document and the product(s) described herein, have been included in the section 'Legal information'.

© NXP B.V. 2013.

All rights reserved.

For more information, visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

> Date of release: 27 May 2013 Document identifier: AN11357