

# Programming the Channel Control Registers on the Time Processor Unit

By Sharon Darley



# Motorola Semiconductor Engineering Bulletin

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**EB280**

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### Introduction

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The time processor unit (TPU) has several control registers that are shared by all 16 channels. Some of these registers, such as the channel interrupt enable register and channel interrupt status register, are not always used.

However, four registers must always be initialized:

- Channel function select registers
- Host sequence registers
- Host service request registers
- Channel priority registers

