

# OL-NX3P191UK

wafer level chip-size package; 4 bumps

10 February 2016

Package information

## 1. Package summary

<b>Terminal position code</b>	B (bottom)
<b>Package type descriptive code</b>	WLCSP
<b>Package type industry code</b>	WLCSP
<b>Package style descriptive code</b>	UC (uncased chip)
<b>Package style suffix code</b>	NA (not applicable)
<b>Package body material type</b>	X (other)
<b>IEC package outline code</b>	---
<b>JEDEC package outline code</b>	---
<b>JEITA package outline code</b>	---
<b>Mounting method type</b>	S (surface mount)
<b>Issue date</b>	2-7-2012

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
n <sub>2</sub>	actual quantity of termination	-	-	4	-	



## 2. Legal information

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