

SOT1092-2(DD)

plastic thermal enhanced very thin quad flat package; no leads; dimple wettable flank; 36 terminals; 0.5 mm pitch, 6 mm x 6 mm x 0.85 mm body

14 March 2018

Package information

1. Package summary

Terminal position code Q (quad)

Package type descriptive code HVQFN36

Package type industry code HVQFN36

Package style descriptive code HVQFN (thermal enhanced very thin quad

flatpack; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 9-1-2018

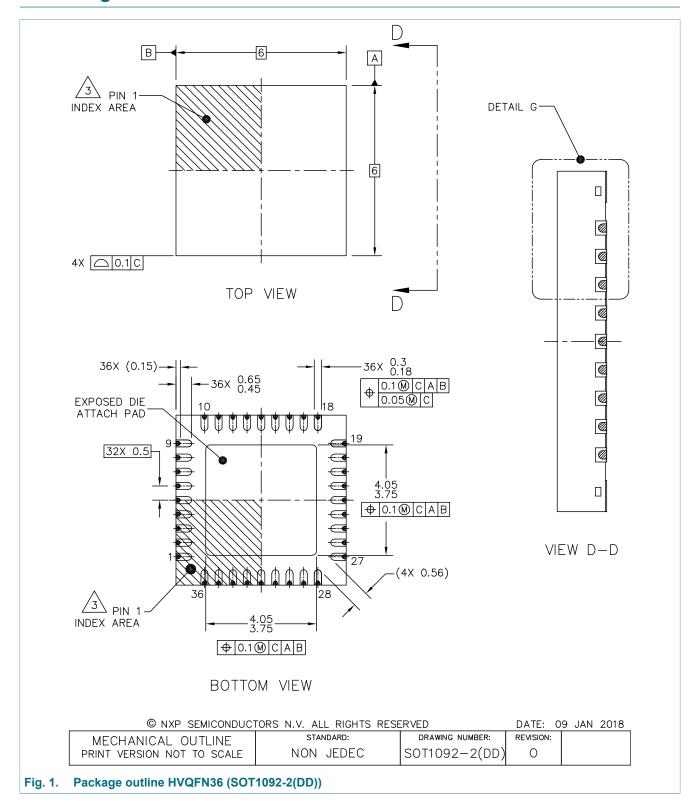
Manufacturer package code 98ASA01179D

Table 1. Package summary

Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	-	-	6	-	mm
E	package width	-	-	6	-	mm
Α	seated height	-	-	0.85	-	mm
е	nominal pitch	-	-	0.5	-	mm
n ₂	actual quantity of termination	-	_	36	-	A/A



2. Package outline



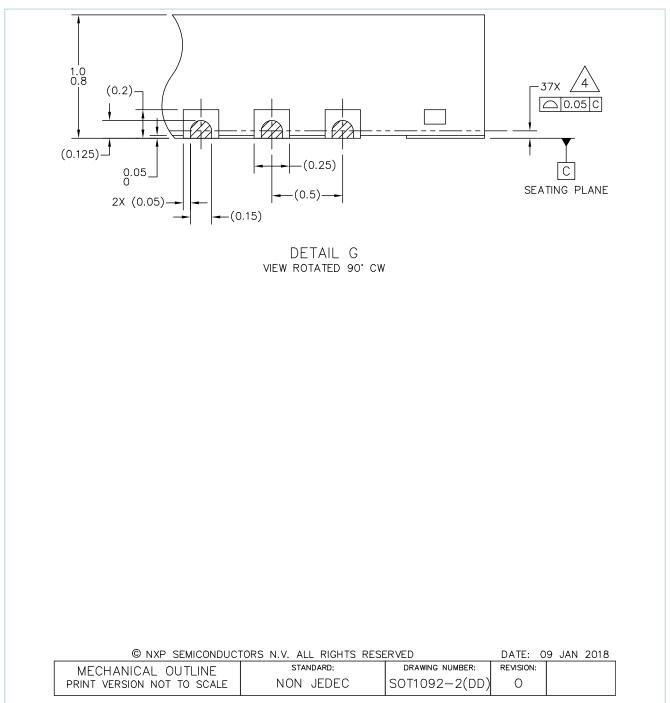


Fig. 2. Package outline dt HVQFN36 (SOT1092-2(DD))

NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

3. PIN ONE FEATURE SHAPE, SIZE AND LOCATION MAY VARY.

4. COPLANARITY APPLIES TO LEADS, DIE ATTACH FLAG.

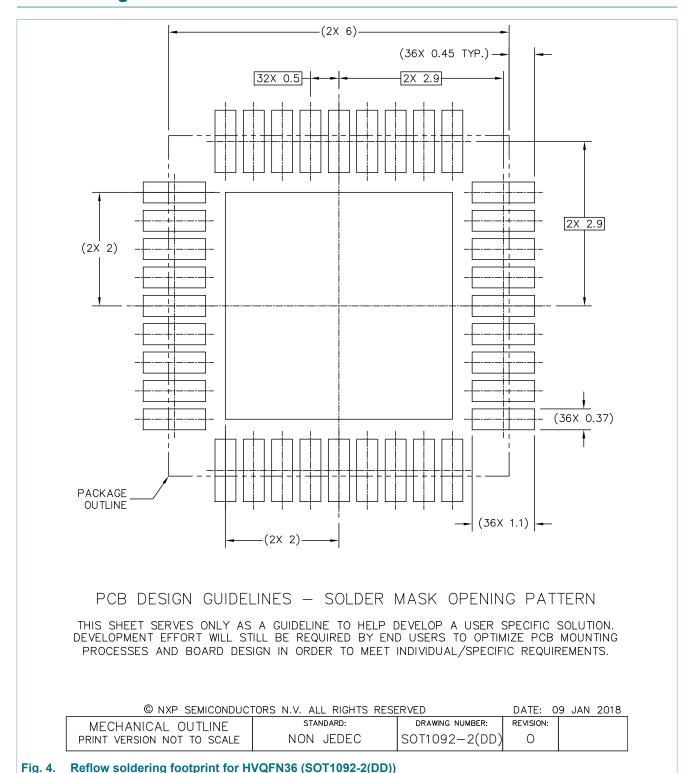
5. MIN. METAL GAP SHOULD BE 0.25 MM.

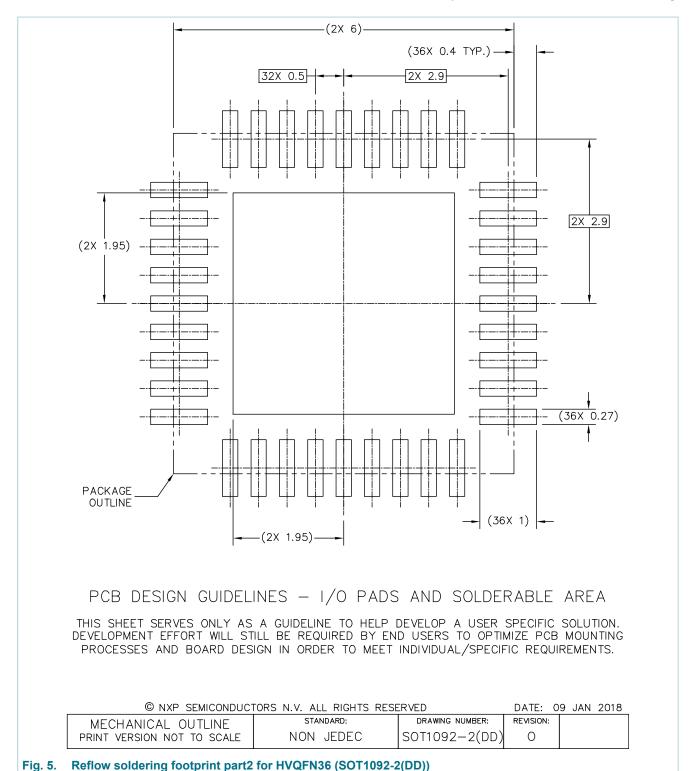
© NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED DATE: 09 JAN 2018

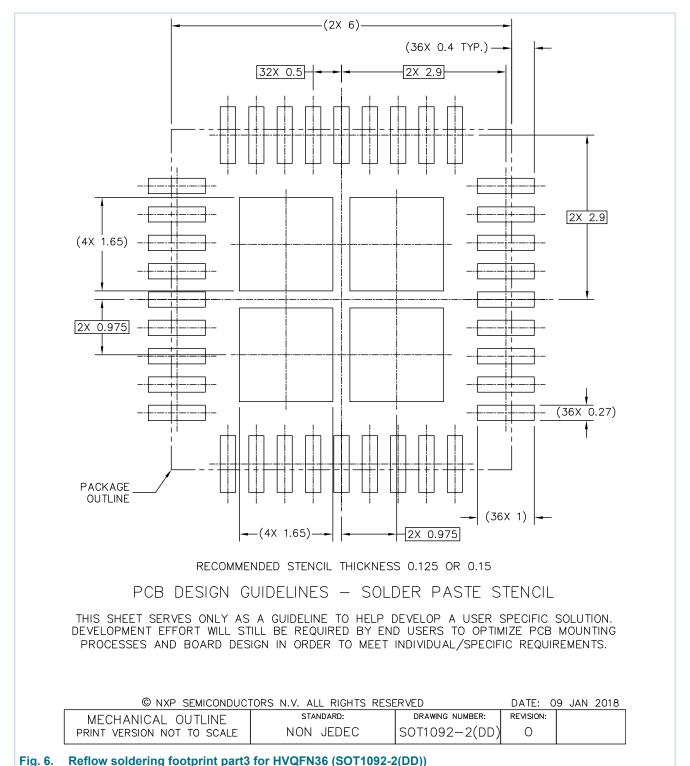
MECHANICAL OUTLINE STANDARD: DRAWING NUMBER: REVISION:
PRINT VERSION NOT TO SCALE NON JEDEC SOT1092-2(DD) O

Fig. 3. Package outline note HVQFN36 (SOT1092-2(DD))

3. Soldering







rig. 6. Renow soldering tootprint parts for HVQFN36 (501 1092-2(DD))

4. Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

NXP Semiconductors SOT1092-2(DD)

plastic thermal enhanced very thin quad flat package; no leads; dimple wettable flank; 36 terminals; 0.5 mm pitch, 6 mm x 6 mm x 0.85 mm body

5. Contents

1.	Package summary	1
2.	Package outline	2
3.	Soldering	5
4.	Legal information	8

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 14 March 2018

[©] NXP B.V. 2018. All rights reserved