

# SOT1375-5 WLCSP4, wafer level chip-scale package, 4 terminals, 1.31 mm x 0.94 mm x 0.5 mm body 17 December 2018 Package information

Package information

# **Package summary**

Terminal position code B (bottom) WLCSP4 Package type descriptive code

Package style descriptive code WLCSP (wafer level chip-size package)

Mounting method type S (surface mount)

Issue date 30-11-2018 Manufacturer package code 98ASA01358D

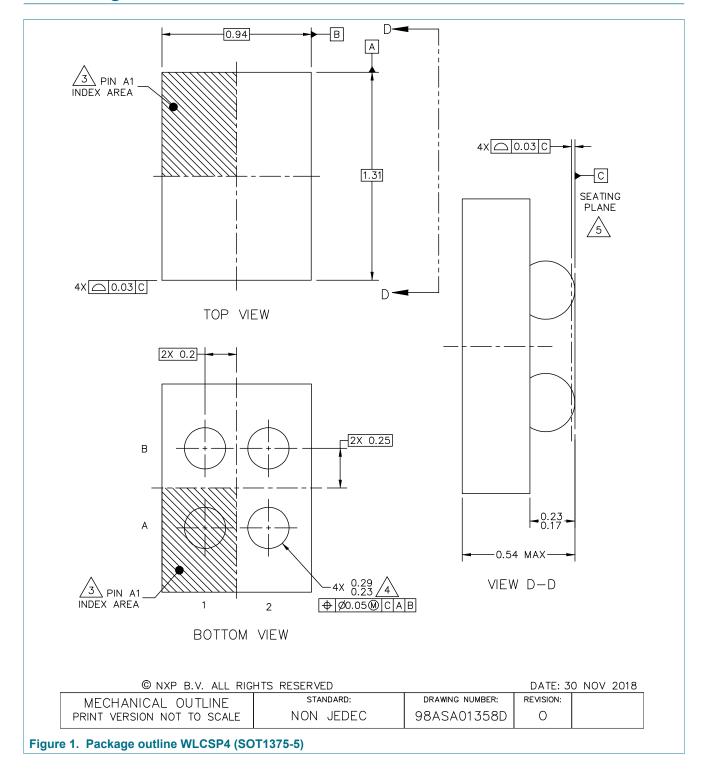
**Table 1. Package summary** 

Parameter	Min	Nom	Max	Unit
package length	-	1.31	-	mm
package width	-	0.94	-	mm
package height	-	0.5	-	mm
nominal pitch	-	0.4	-	mm
actual quantity of termination	-	4	-	



WLCSP4, wafer level chip-scale package, 4 terminals, 1.31 mm x 0.94 mm x 0.5 mm body

# 2 Package outline



WLCSP4, wafer level chip-scale package, 4 terminals, 1.31 mm x 0.94 mm x 0.5 mm body

# 3 Soldering

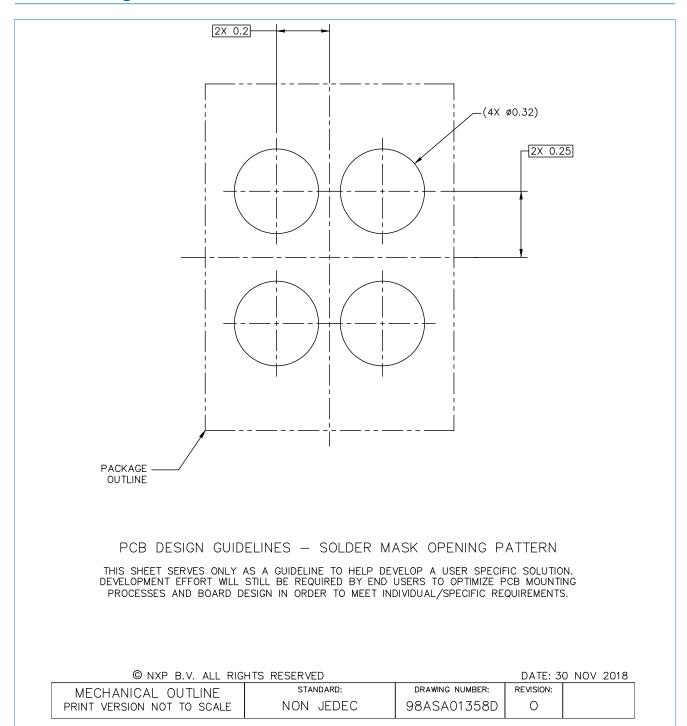


Figure 2. Reflow soldering footprint part1 for WLCSP4 (SOT1375-5)

### WLCSP4, wafer level chip-scale package, 4 terminals, 1.31 mm x 0.94 mm x 0.5 mm body

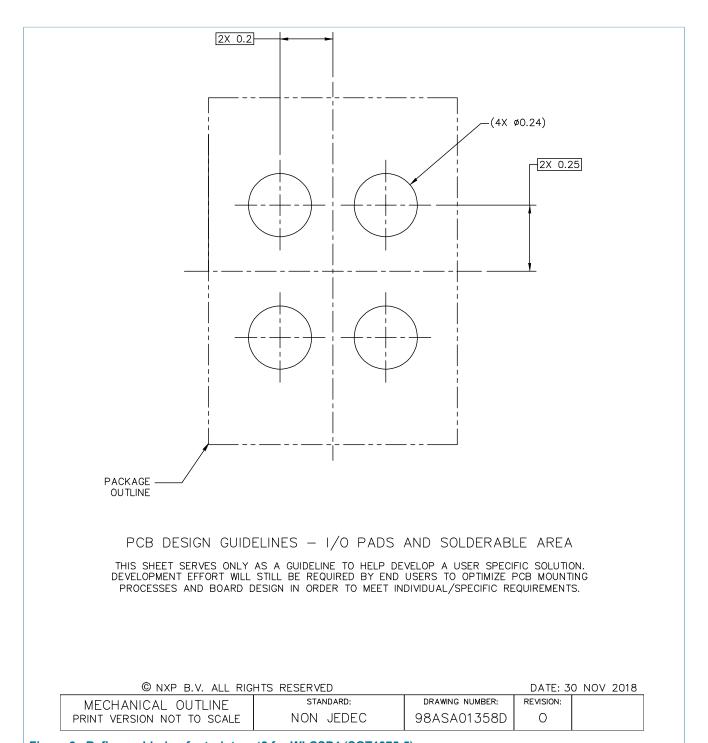


Figure 3. Reflow soldering footprint part2 for WLCSP4 (SOT1375-5)

### WLCSP4, wafer level chip-scale package, 4 terminals, 1.31 mm x 0.94 mm x 0.5 mm body

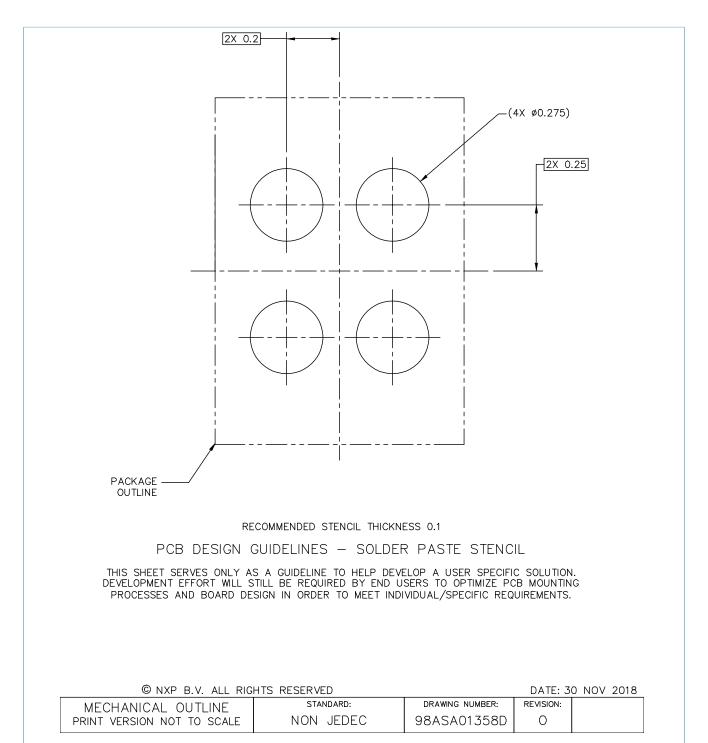


Figure 4. Reflow soldering footprint part3 for WLCSP4 (SOT1375-5)

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#### NOTES:

- 1. ALL DIMENSIONS IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

3. PIN A1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY.

 $\stackrel{\textstyle \checkmark}{4}$  maximum solder ball diameter measured parallel to datum c.

 $\sqrt{5}$ \ DATUM C, THE SEATING PLANE, IS DETERMINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

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DATE: 30 NOV 2018

MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON JEDEC	98ASA01358D	0	

Figure 5. Package outline note WLCSP4 (SOT1375-5)

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# 4 Legal information

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