

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

19 February 2019

Package information

1 Package summary

Terminal position code	Q (quad)
Package type descriptive code	HQFN16
Package style descriptive code	HQFN (thermal enhanced quad flatpack; no leads)
Package body material type	P (plastic)
Mounting method type	S (surface mount)
Issue date	22-05-2018
Manufacturer package code	98ASA00815D

Table 1. Package summary

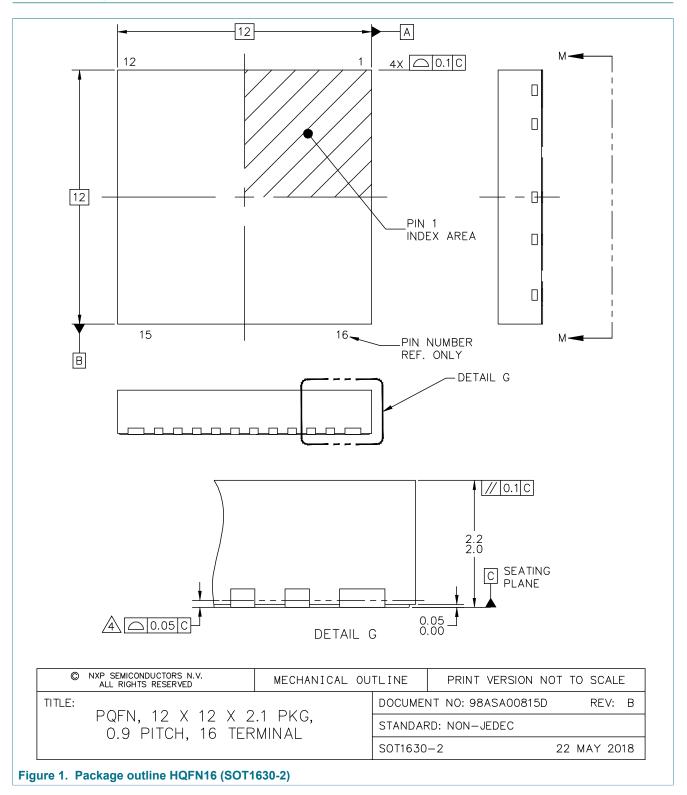
Parameter	Min	Nom	Мах	Unit
package length	-	12	-	mm
package width	-	12	-	mm
package height	-	2.1	-	mm
nominal pitch	-	0.9	-	mm
actual quantity of termination	-	16	-	



SOT1630-2

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

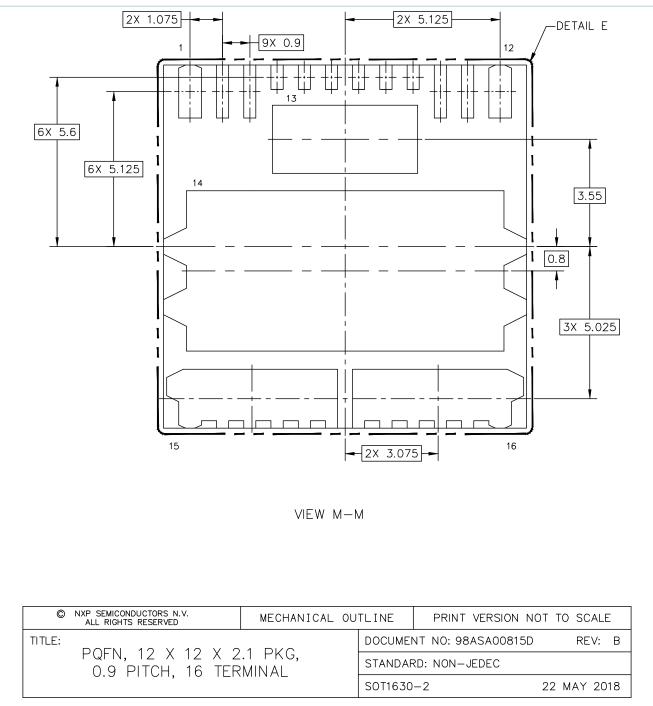
2 Package outline



SOT1630-2
Package information

SOT1630-2

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

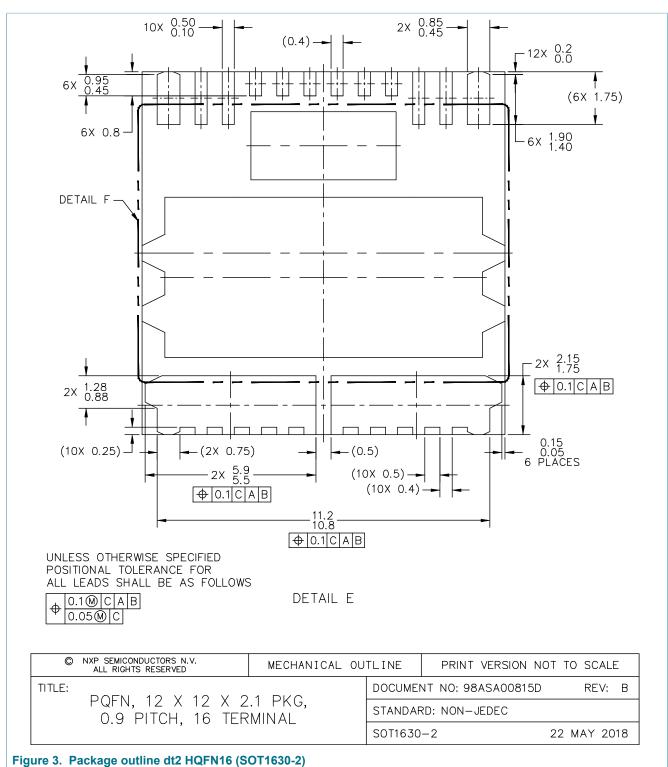




SOT1630-2
Package information

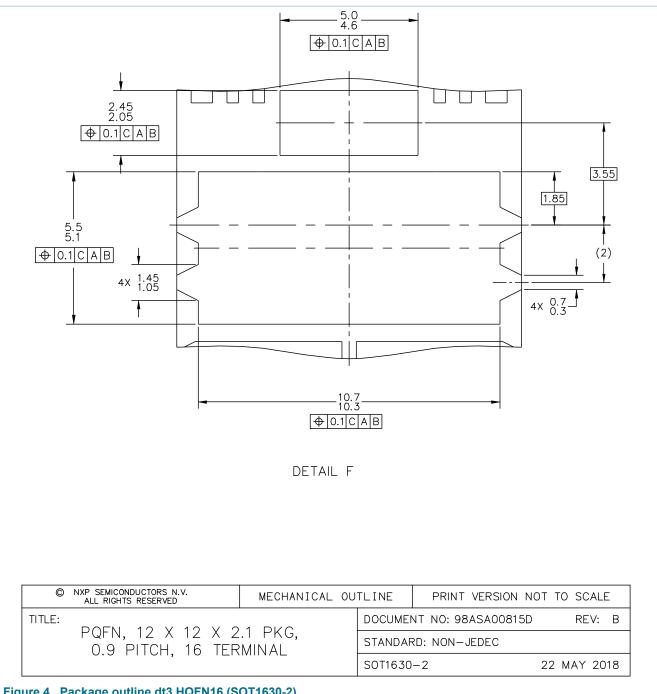
NXP Semiconductors

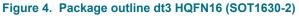
HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body



NXP Semiconductors

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

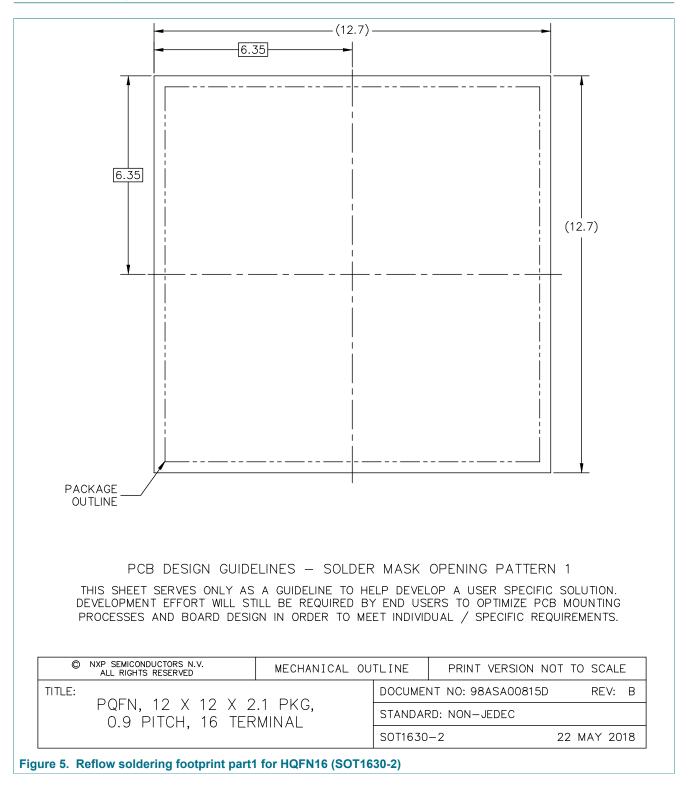




SOT1630-2

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

3 Soldering



SOT1630-2
Package information

NXP Semiconductors

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

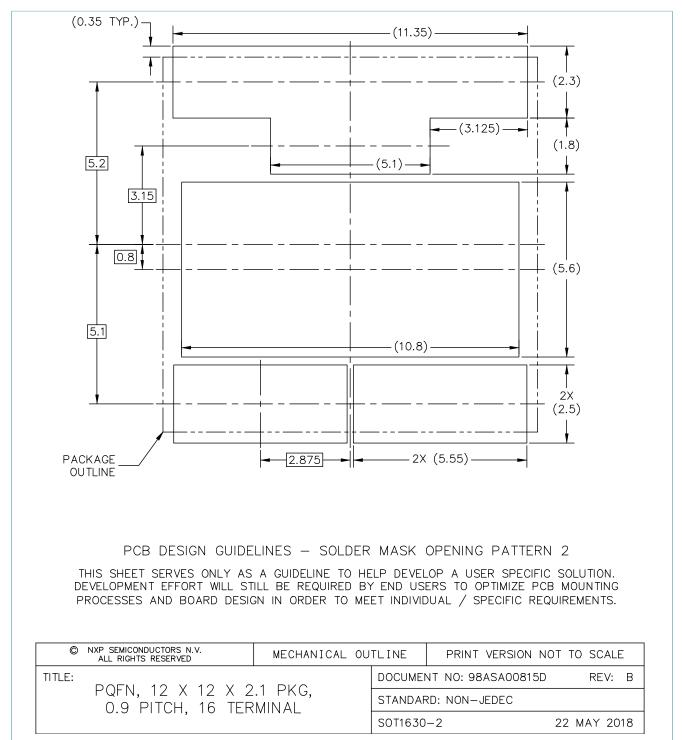
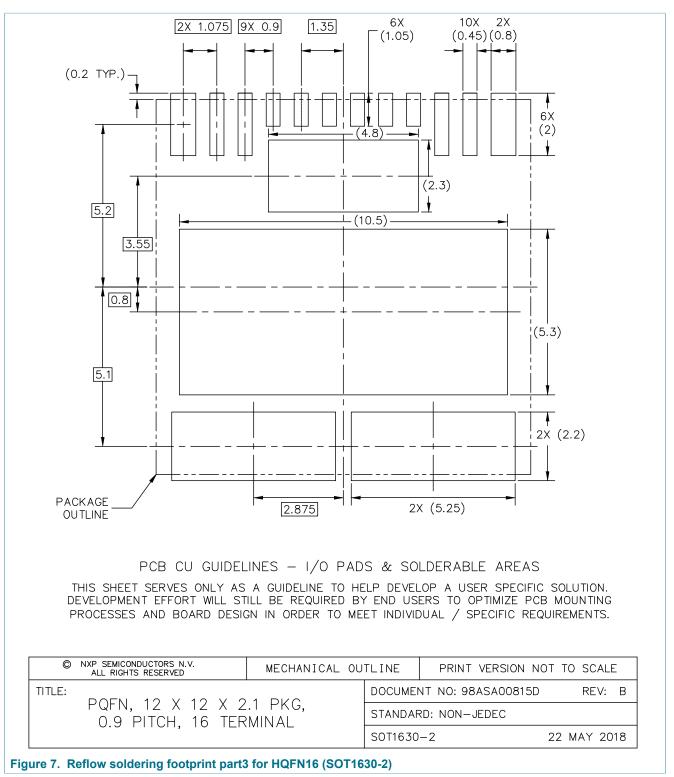


Figure 6. Reflow soldering footprint part2 for HQFN16 (SOT1630-2)

NXP Semiconductors

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body



NXP Semiconductors

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

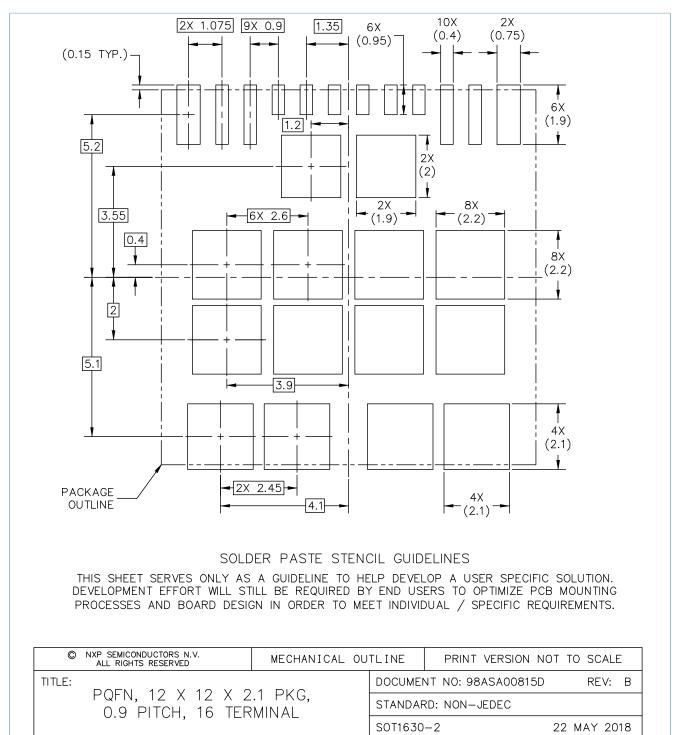


Figure 8. Reflow soldering footprint part4 for HQFN16 (SOT1630-2)

SOT1630-2 Package information

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. THIS IS NON-JEDEC REGISTERED PACKAGE.

4. COPLANARITY APPLIES TO LEADS AND CORNER LEADS.

5. MINIMUM METAL GAP IS GUARANTEED TO BE 0.25 MM.

NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OUTL	LINE	PRINT VERSION NO	DT TO SCALE	
TITLE: PQFN, 12 X 12 X 2.1 PKG, 0.9 PITCH, 16 TERMINAL		DOCUMENT	NO: 98ASA00815D	REV: I	В
		STANDARD:	NON-JEDEC		
		SOT1630-2	<u>-</u>	22 MAY 2018	8



SOT1630-2

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

SOT1630-2

HQFN16, plastic, thermal enhanced quad flat package, no leads; 16 terminals; 0.9 mm pitch; 12 mm x 12 mm x 2.1 mm body

Contents

1	Package summary	1
2	Package outline	2
3	Soldering	
4	Legal information	11

© NXP B.V. 2019.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 19 February 2019