

WLCSP81, wafer level chip-scale package; 81 bumps; 0.35 mm pitch, 3.55 mm x 3.36 mm x 0.365 mm body (backside coating included)

6 April 2018

Package information

1. Package summary

Terminal position code B (bottom)

Package type descriptive code WLCSP81

Package style descriptive code WLCSP (wafer level chip-size package)

Mounting method type S (surface mount)

Issue date 14-3-2018

Manufacturer package code 98ASA01227D

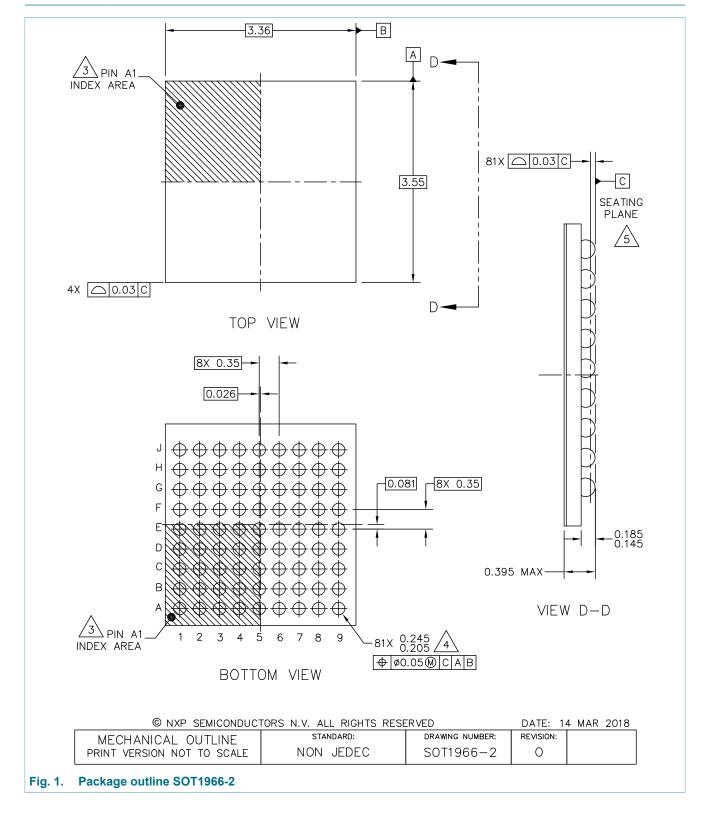
Table 1. Package summary

Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	3.52	-	3.55	3.58	mm
Е	package width	3.33	-	3.36	3.39	mm
A	seated height	0.335	-	0.365	0.395	mm
е	nominal pitch	_	-	0.35	-	mm
n ₂	actual quantity of termination	_	-	81	-	A/A



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2. Package outline



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NOTES:

- 1. ALL DIMENSIONS IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

23. PIN A1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY.

4. MAXIMUM SOLDER BALL DIAMETER MEASURED PARALLEL TO DATUM C.

 $\sqrt{5.}$ datum c, the seating plane, is determined by the spherical crowns of the solder balls.

6. THIS PACKAGE HAS A BACK SIDE COATING THICKNESS OF 0.025.

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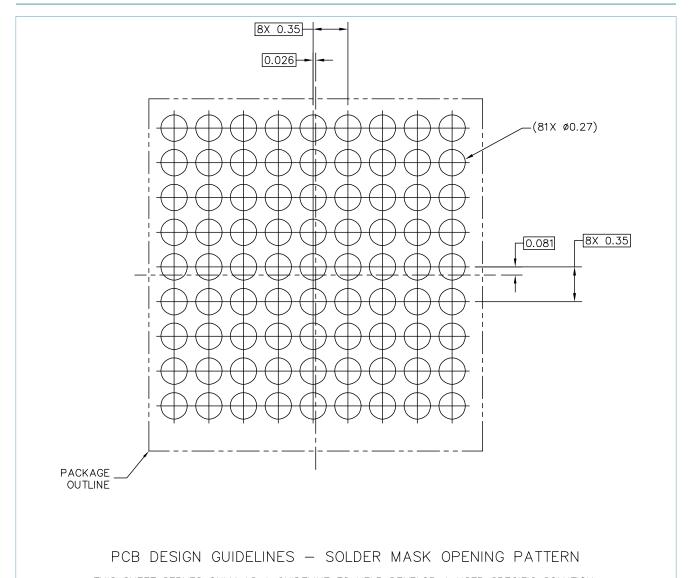
DATE: 14 MAR 2018

MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON JEDEC	SOT1966-2	0	

Fig. 2. Package outline note WLCSP81 (SOT1966-2)

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3. Soldering



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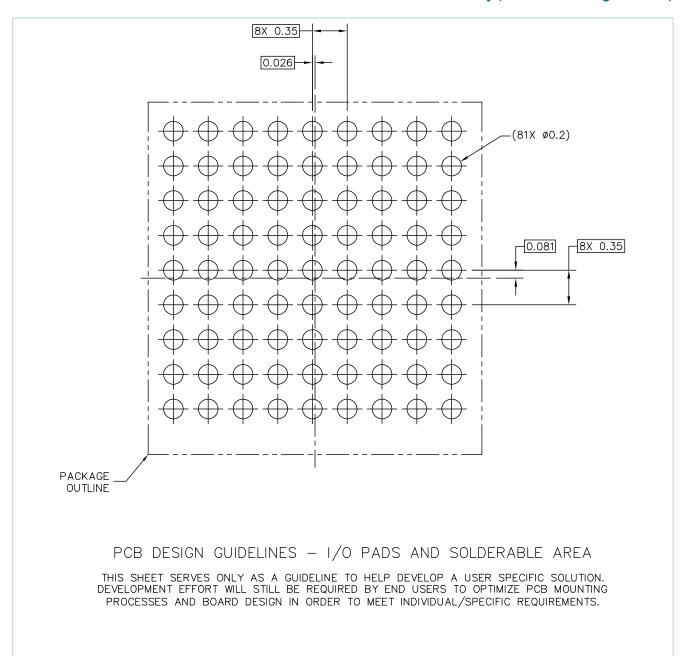
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Reflow soldering footprint for SOT1966-2 Fig. 3.

SOT1966-2

Package information 6 April 2018

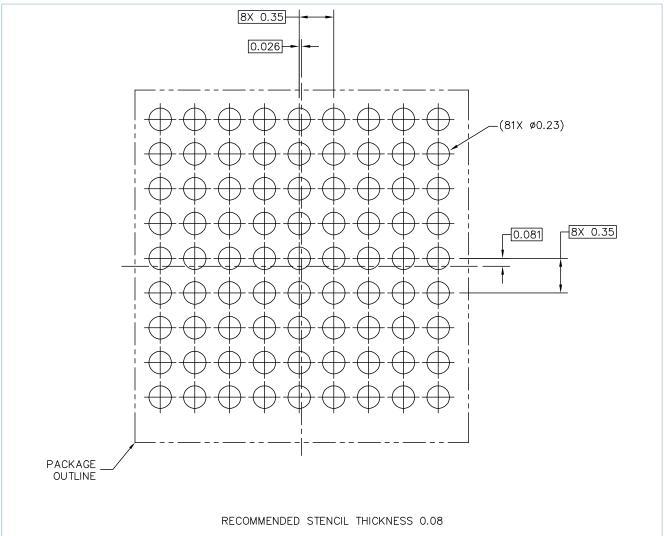
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Fig. 4. Reflow soldering footprint part2 for WLCSP81 (SOT1966-2)

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PCB DESIGN GUIDELINES - SOLDER PASTE STENCIL

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Fig. 5. Reflow soldering footprint part3 for WLCSP81 (SOT1966-2)

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