

SOT2113-1

VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87 mm body

11 December 2020

Package information

1 Package summary

Terminal position code B (bottom)

Package type descriptive code VFBGA120

Package style descriptive code VFBGA (very thin fine-pitch ball grid array)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 12-10-2020

Manufacturer package code 98ASA01719D

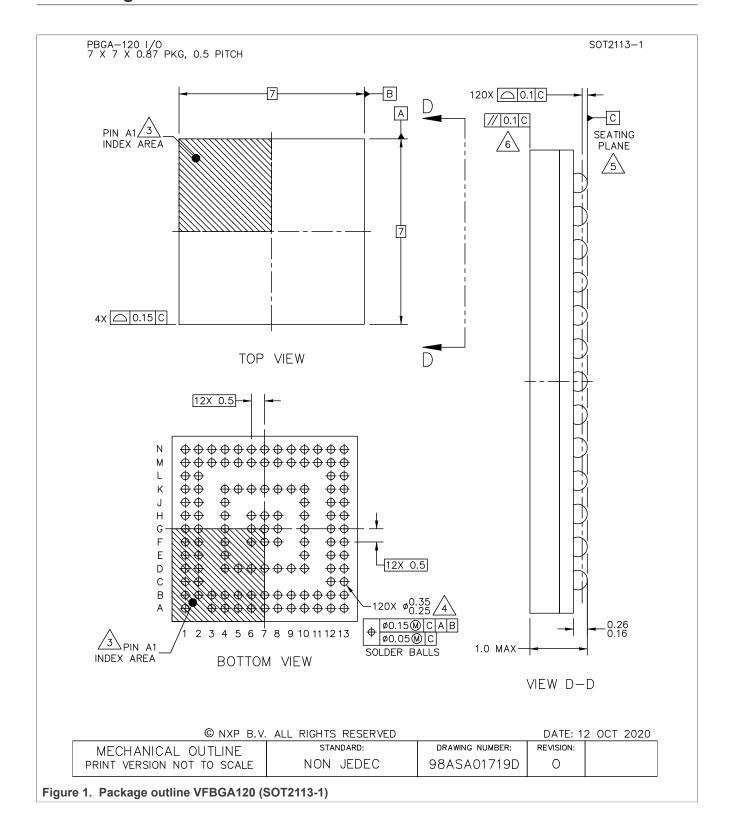
Table 1. Package summary

- and the analysis of the anal					
Parameter	Min	Nom	Max	Unit	
package length	6.9	7	7.1	mm	
package width	6.9	7	7.1	mm	
package height	-	0.87	1	mm	
nominal pitch	-	0.5	-	mm	
actual quantity of termination	-	120	-		



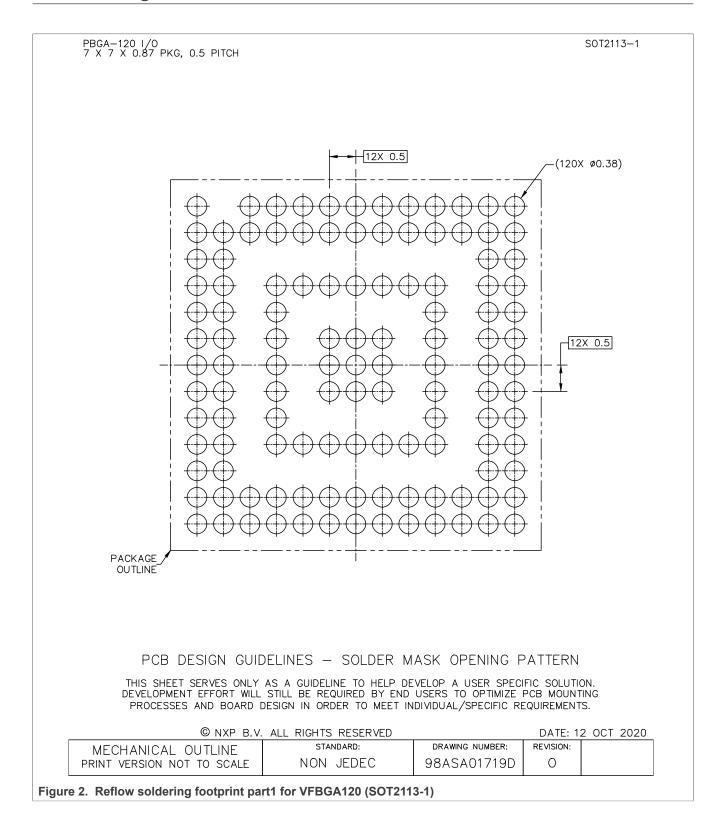
VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87 mm body

2 Package outline



VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87 mm body

3 Soldering



VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87 mm body

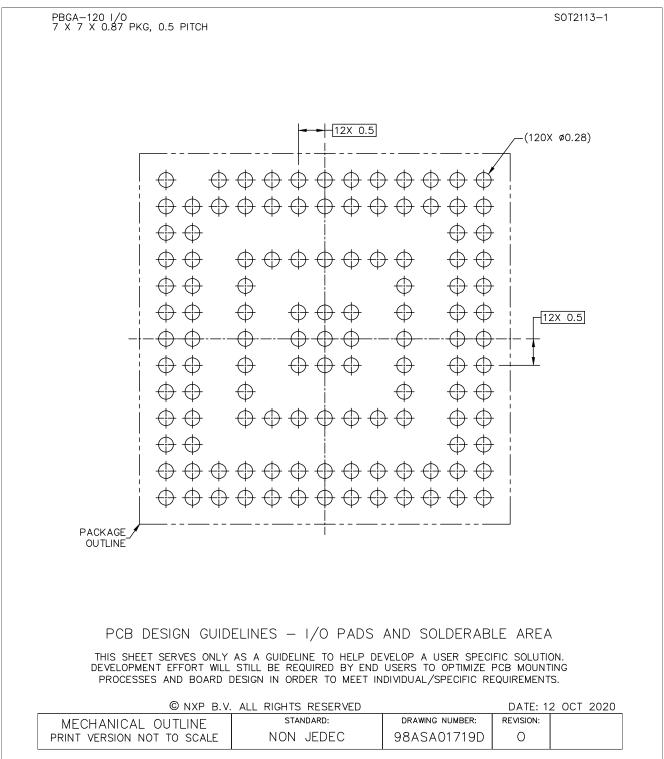
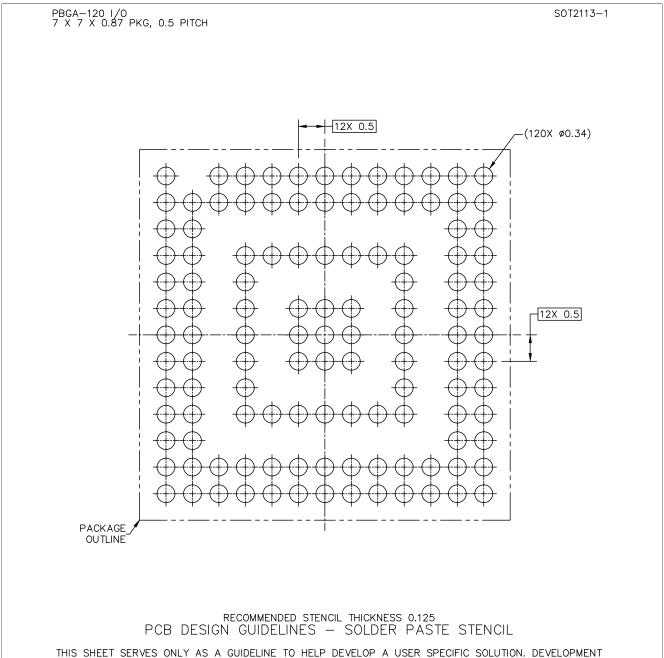


Figure 3. Reflow soldering footprint part2 for VFBGA120 (SOT2113-1)

VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87 mm body



THIS SHEET SERVES ONLY AS A GUIDELINE TO HELP DEVELOP A USER SPECIFIC SOLUTION. DEVELOPMENT EFFORT WILL STILL BE REQUIRED BY END USERS TO OPTIMIZE PCB MOUNTING PROCESSES AND BOARD DESIGN IN ORDER TO MEET INDIVIDUAL/SPECIFIC REQUIREMENTS.

© NXP B.V.	ALL RIGHTS RESERVED		DATE: 1:	2 OCT 2020
MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON JEDEC	98ASA01719D	0	

Figure 4. Reflow soldering footprint part3 for VFBGA120 (SOT2113-1)

VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87

PBGA-120 I/O 7 X 7 X 0.87 PKG, 0.5 PITCH

SOT2113-1

NOTES:

- 1. ALL DIMENSIONS IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

MAXIMUM SOLDER BALL DIAMETER MEASURED PARALLEL TO DATUM C.

PIN A1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY.

DATUM C, THE SEATING PLANE, IS DETERMINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

PARALLELISM MEASUREMENT SHALL EXCLUDE ANY EFFECT OF MARK ON TOP SURFACE OF PACKAGE.

© NXP B.V. ALL RIGHTS RESERVED

DATE: 12 OCT 2020

MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON JEDEC	98ASA01719D	0	

Figure 5. Package outline note VFBGA120 (SOT2113-1)

VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

VFBGA120, very thin fine pitch ball grid array package, 120 terminals, 0.5 mm pitch, 7 mm x 7 mm x 0.87 mm body

Contents

1	Package summary	1
2	Package outline	2
3	Soldering	3
4	Legal information	