SOT617-26(D)

HVQFN32, thermal enhanced very thin quad flat package, no leads, wettable flanks, 32 terminals, 0.5 mm pitch, 5 mm x 5 mm x 0.85 mm body

13 February 2024 Package information



1 Package summary

Terminal position code Q (quad)
Package type descriptive code HVQFN32

Package style descriptive code HVQFN (thermal enhanced very thin quad flatpack; no

leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 02-01-2024

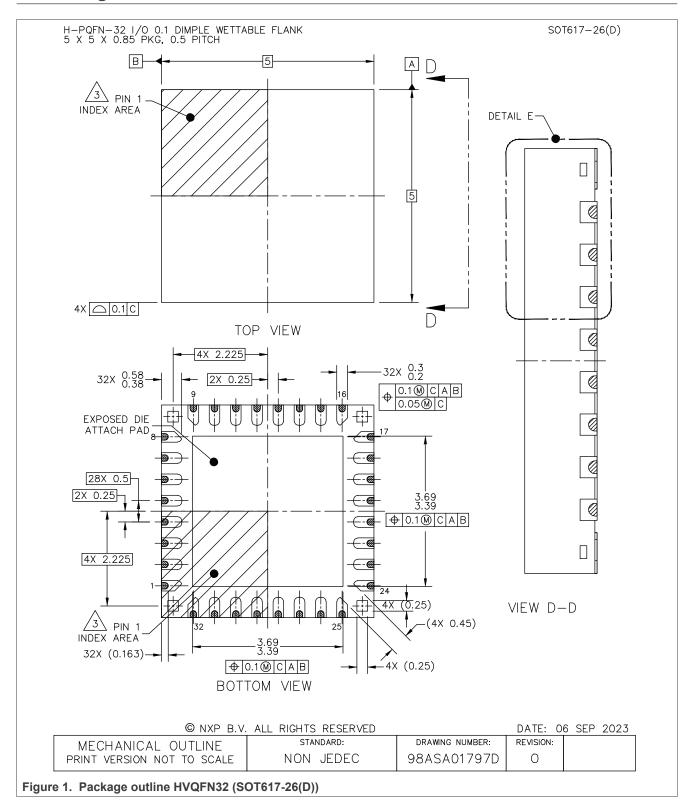
Manufacturer package code 98ASA01797D

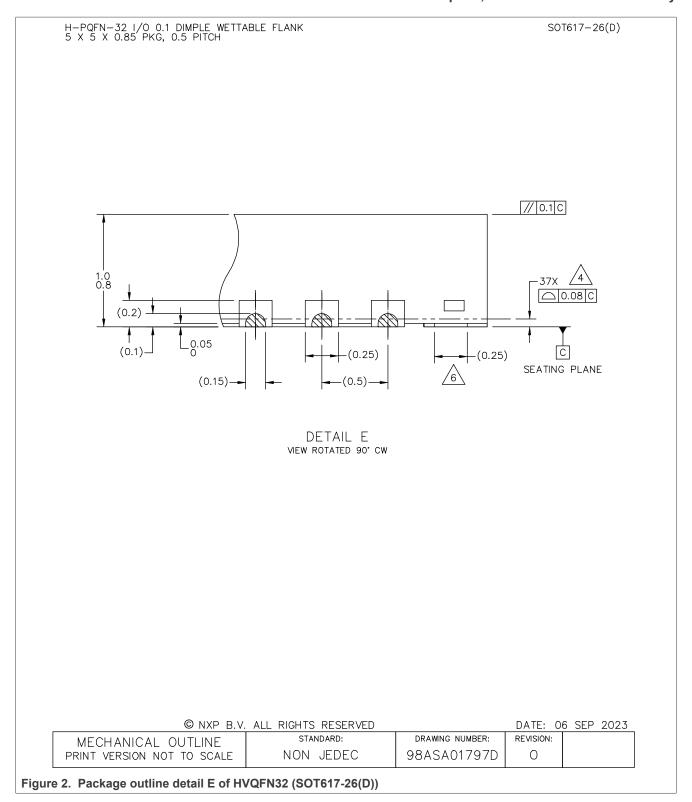
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	5	-	mm
package width	-	5	-	mm
seated height	0.8	0.85	1	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	32	-	

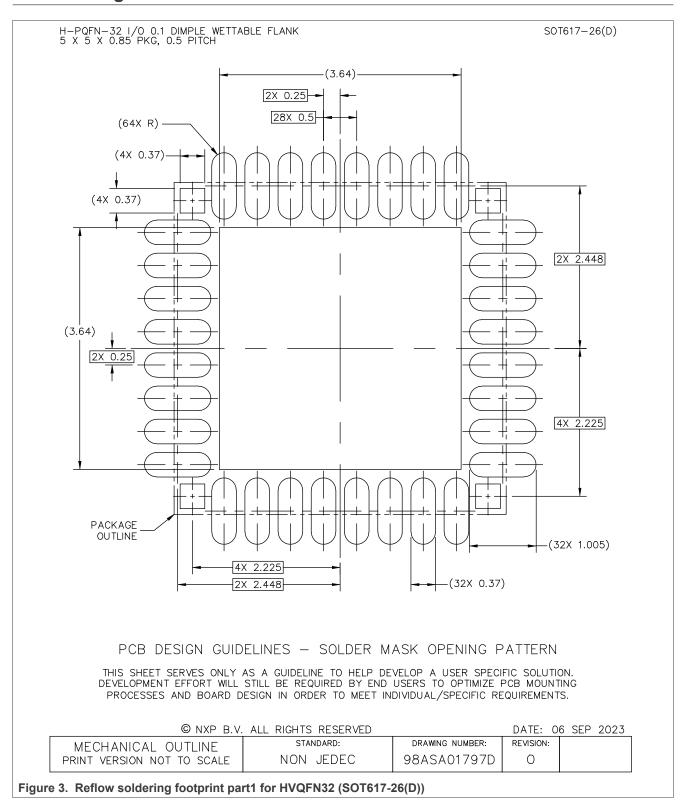


2 Package outline

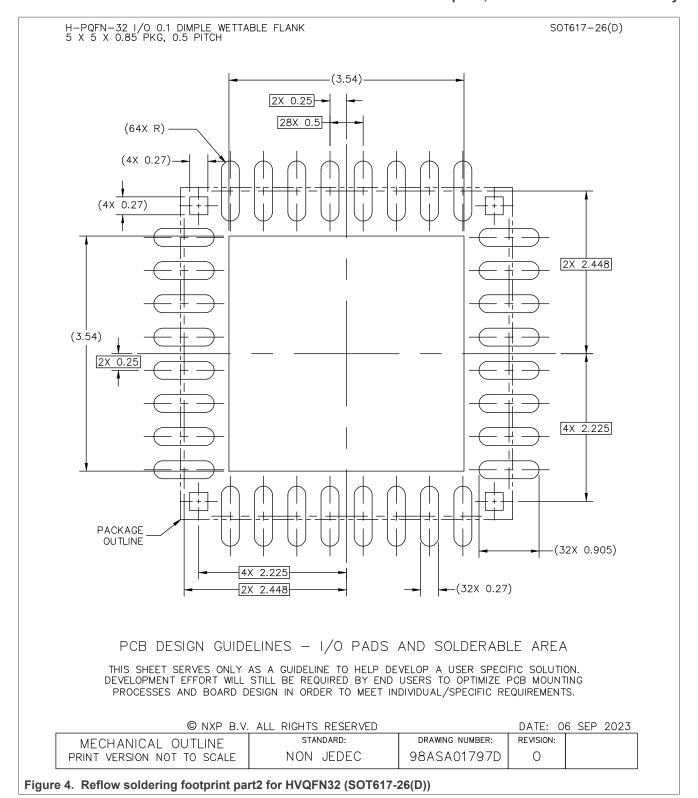


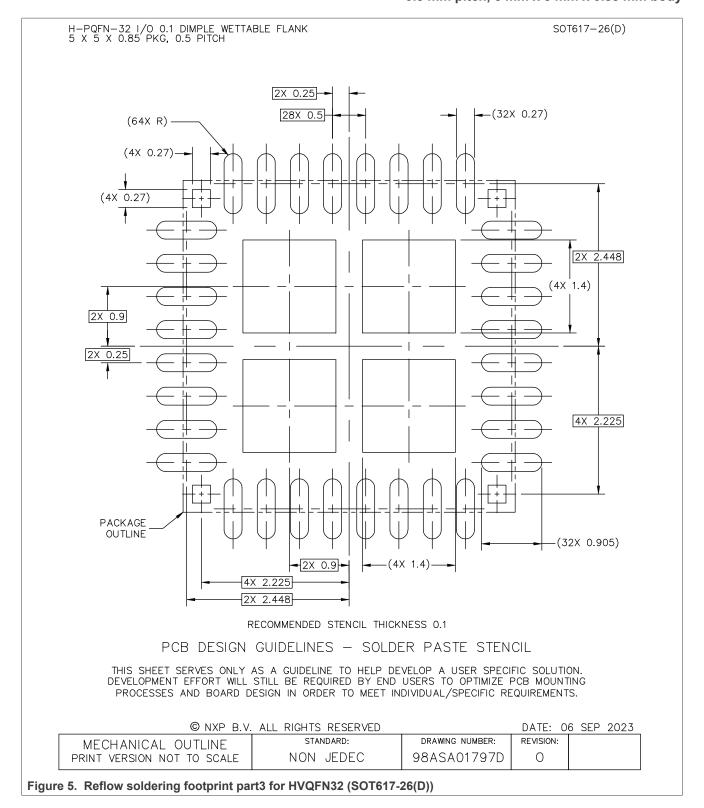


3 Soldering



SOT617-26(D)





SOT617-26(D)

H-PQFN-32 I/O 0.1 DIMPLE WETTABLE FLANK 5 X 5 X 0.85 PKG, 0.5 PITCH SOT617-26(D) NOTES: 1. ALL DIMENSIONS ARE IN MILLIMETERS. 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994. 3. PIN 1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY. $\sqrt{4}$. Coplanarity applies to leads and die attach flag. 5. MIN. METAL GAP FOR LEAD TO EXPOSED PAD SHALL BE 0.2 MM. 6. ANCHORING PADS. © NXP B.V. ALL RIGHTS RESERVED DATE: 06 SEP 2023 DRAWING NUMBER: STANDARD: REVISION: MECHANICAL OUTLINE PRINT VERSION NOT TO SCALE NON JEDEC 98ASA01797D 0

SOT617-26(D)

Figure 6. Package outline note HVQFN32 (SOT617-26(D))

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

NXP Semiconductors

SOT617-26(D)

HVQFN32, thermal enhanced very thin quad flat package, no leads, wettable flanks, 32 terminals, 0.5 mm pitch, 5 mm x 5 mm x 0.85 mm body

Contents

1	Package summary1
2	Package outline2
3	Soldering4
4	<u> </u>