

SOT619-16

HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

22 January 2019

Package information

1 Package summary

Terminal position code Q (quad)

Package type descriptive code HVQFN48

Package style descriptive code HVQFN (thermal enhanced very thin quad

flatpack; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 07-01-2016

Manufacturer package code 98ASA00343D

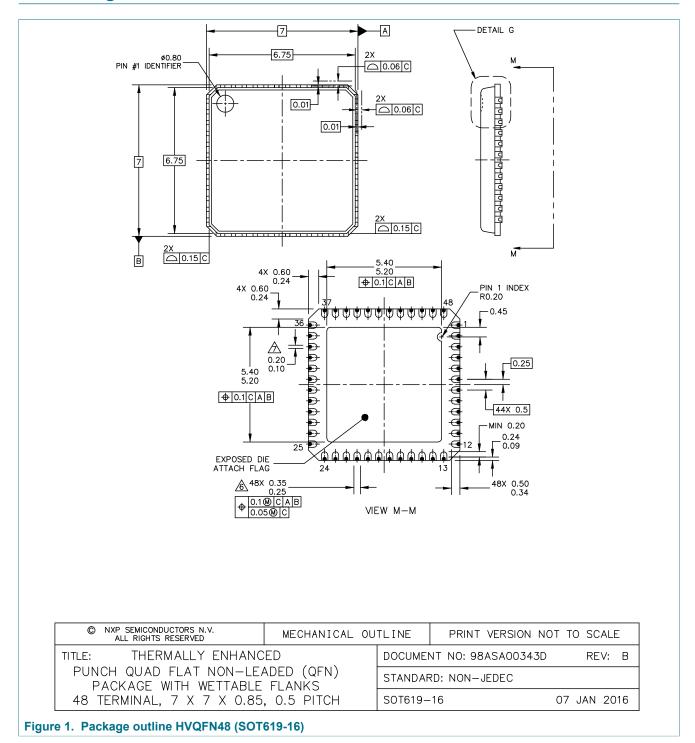
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	7	-	mm
package width	-	7	-	mm
package height	-	0.85	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	48	-	

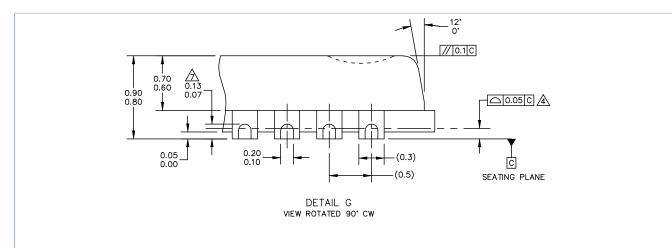


HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

2 Package outline



HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

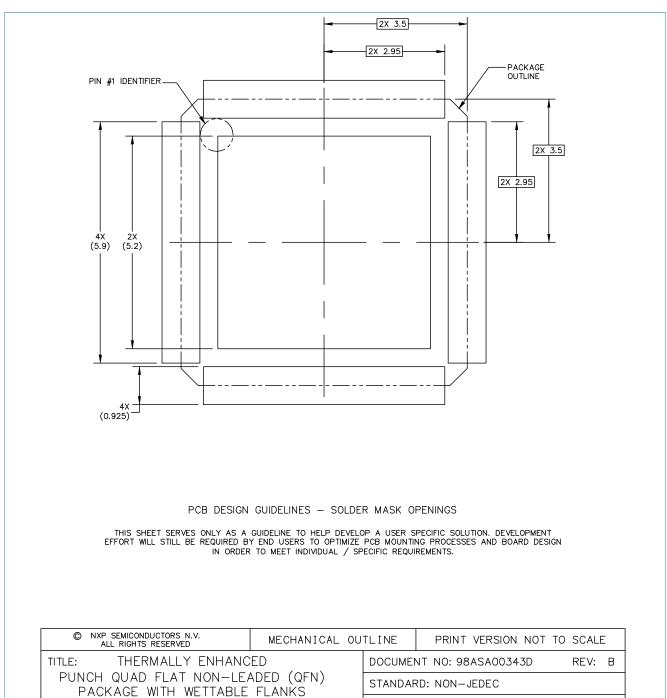


© NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OU	TLINE	PRINT VERSION NOT TO) SCALE
	111211111111111111111111111111111111111		NT NO: 98ASA00343D	REV: B
PUNCH QUAD FLAT NON-LEADED (QFN) PACKAGE WITH WETTABLE FLANKS 48 TERMINAL, 7 X 7 X 0.85, 0.5 PITCH		STANDARD: NON-JEDEC		
		SOT619-	16 07 .	JAN 2016

Figure 2. Package outline dt1 HVQFN48 (SOT619-16)

HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

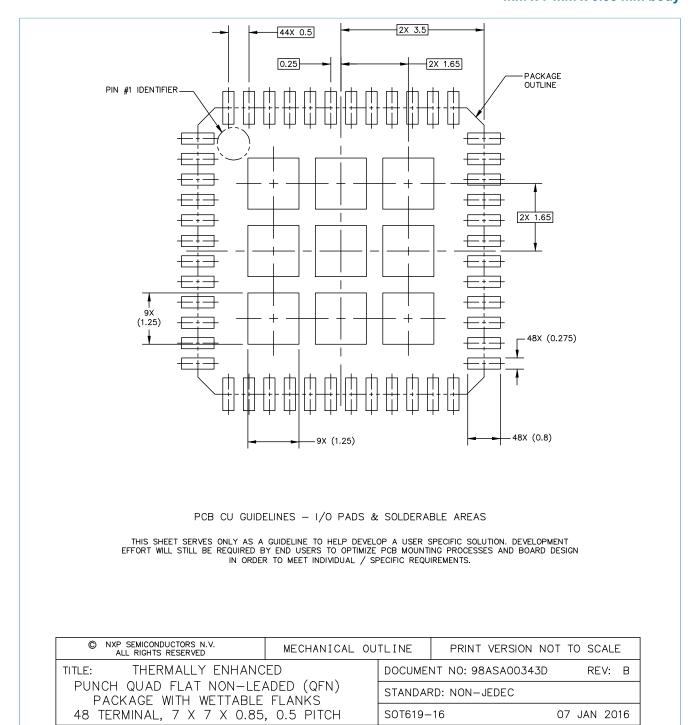
Soldering



	NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OU	TLINE	PRINT VERSION NO	OT TO SCALE
	PUNCH QUAD FLAT NON-LEADED (QFN) PACKAGE WITH WETTABLE FLANKS		DOCUMEN	NT NO: 98ASA00343D	REV: B
			STANDARD: NON-JEDEC		
			SOT619-	-16	07 JAN 2016

Figure 3. Reflow soldering footprint part1 for HVQFN48 (SOT619-16)

HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body



SOT619-16

Figure 4. Reflow soldering footprint part2 for HVQFN48 (SOT619-16)

HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

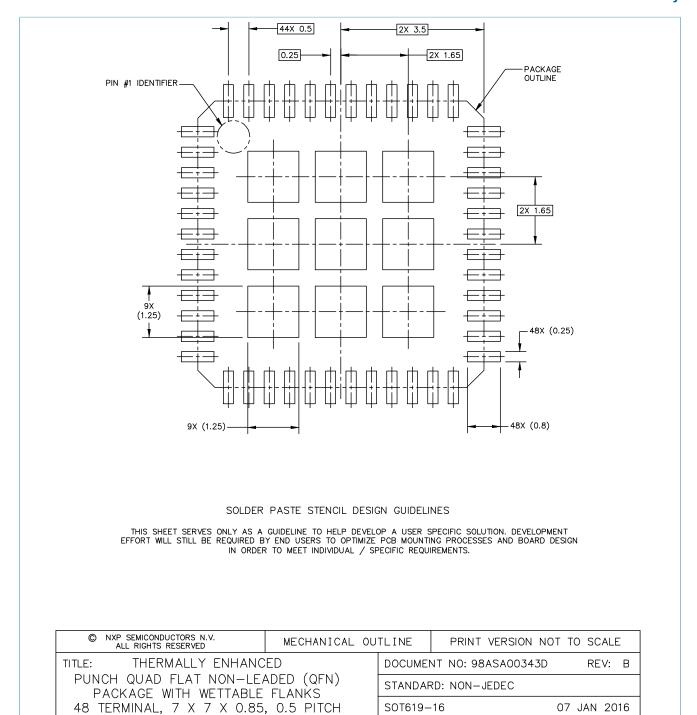


Figure 5. Reflow soldering footprint part3 for HVQFN48 (SOT619-16)

HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. THIS IS NON JEDEC REGISTERED PACKAGE.
- A COPLANARITY APPLIES TO LEADS, CORNER LEADS AND DIE ATTACH FLAG.
- 5. MIN METAL GAP BETWEEN TERMINAL AND DIE PADDLE SHALL BE 0.25MM.
- $\stackrel{\textstyle \frown}{\triangle}$ This dimension applies to plated terminal and is measured between 0.02mm and package cutting line.

NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OU	TLINE	PRINT VERSION NO	T TO SCALE
PUNCH QUAD FLAT NON-LEADED (QFN) PACKAGE WITH WETTABLE FLANKS		DOCUMEN	NT NO: 98ASA00343D	REV: B
		STANDARD: NON-JEDEC		
		SOT619-	16	07 JAN 2016

Figure 6. Package outline note HVQFN48 (SOT619-16)

HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

HVQFN48, plastic, thermal enhanced very thin quad flatpack; no leads; 48 terminals; 0.5 mm pitch; 7 mm x 7 mm x 0.85 mm body

Contents

1	Package summary	1
2	Package outline	2
3	Soldering	4
4	Legal information	