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Revisions			
Rev	Description	Date	Approved
A	Prototype Release	21-SEP-15	Ewan Harwood
AX1	Added CAN FD Phy, fixed ethernet phy clocks and various other changes.	12-NOV-15	Ewan Harwood
AX2	Changed CAN FD Phy, changed FTDI chip to 3v3 I/O, Added AC coupling caps to SMA clock input.	17-NOV-15	Ewan Harwood
AX3	Changed CAN FD Phy to match Halo board.	20-NOV-15	Ewan Harwood
B	2nd Prototype Release	14-DEC-15	Ewan Harwood
C	Updated reset circuit to check for 1.25v supply.	02-MAR-16	Ewan Harwood
CX1	RESET circuit modification to separate RESET_B	17-MAY-16	Ewan Harwood
D	3rd Prototype Release	26-MAY-16	Ewan Harwood

S32R274/S32R372RRSEVB

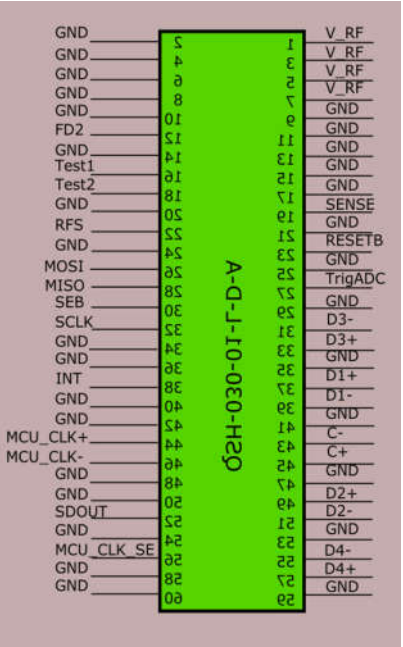
RaceRunner Ultra (S32R274)

& RaceRunner Skinny (S32R372)

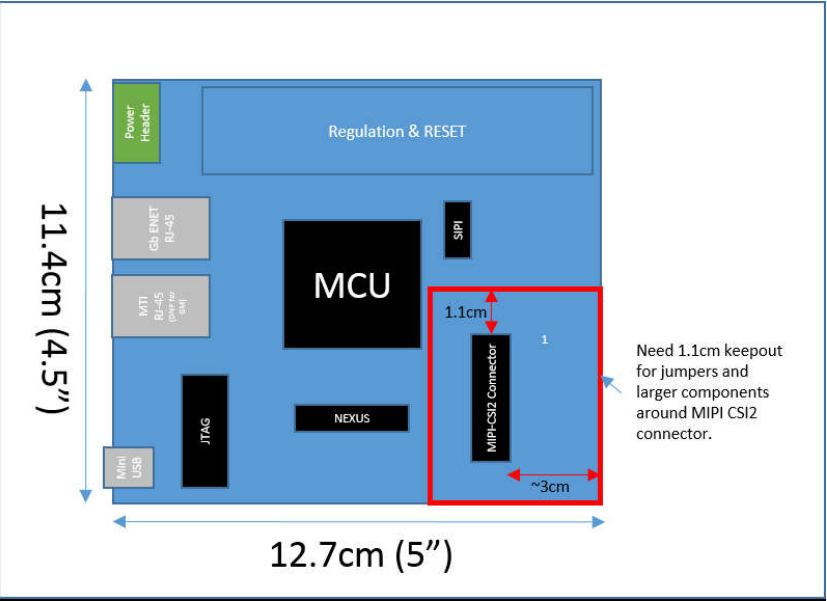
Evaluation Board

		Automotive Product Group 6501 William Cannon Drive West Austin, TX 78755-8590	
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Drawn by: Ewan Harwood		Page Title: TITLE, TOC & REV	
Approved: Ewan Harwood		Size C	Document Number SCH-28921 PDF: SPF-28921
		Date: Wednesday, August 22, 2018	Rev D
		Sheet 1	of 8

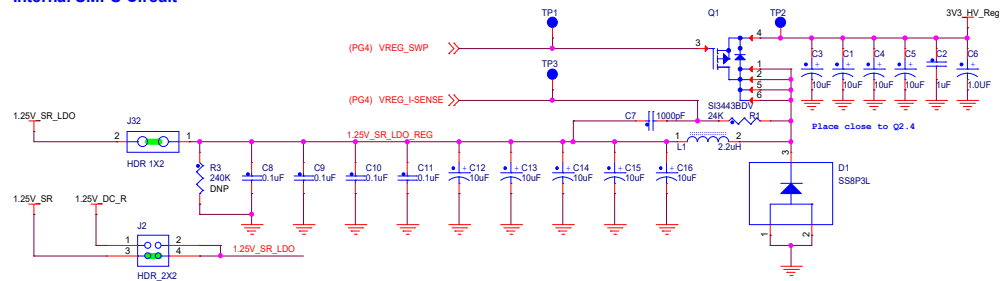
Eagle Connector Pinout



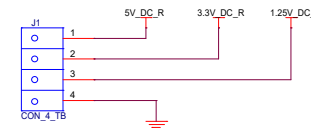
High level board placement



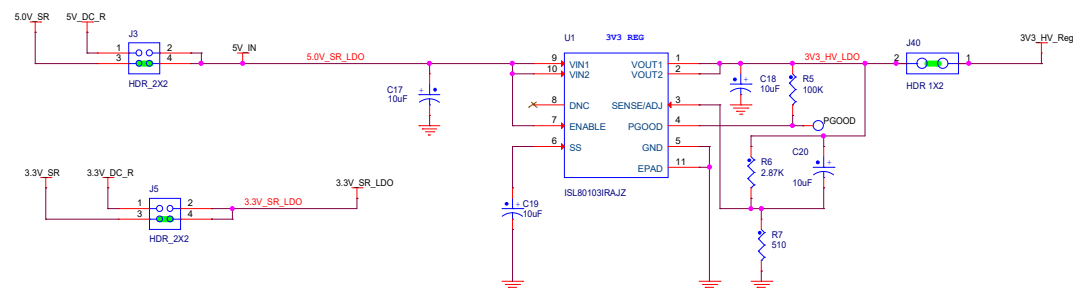
Internal SMPS Circuit



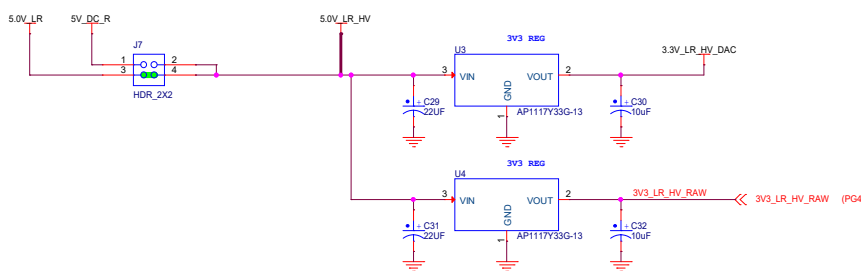
External Supply Input Block



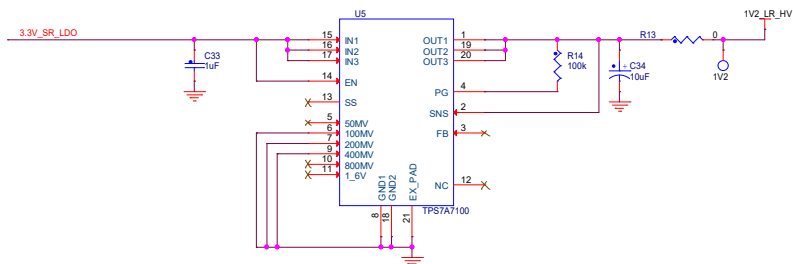
3.3v Regulation



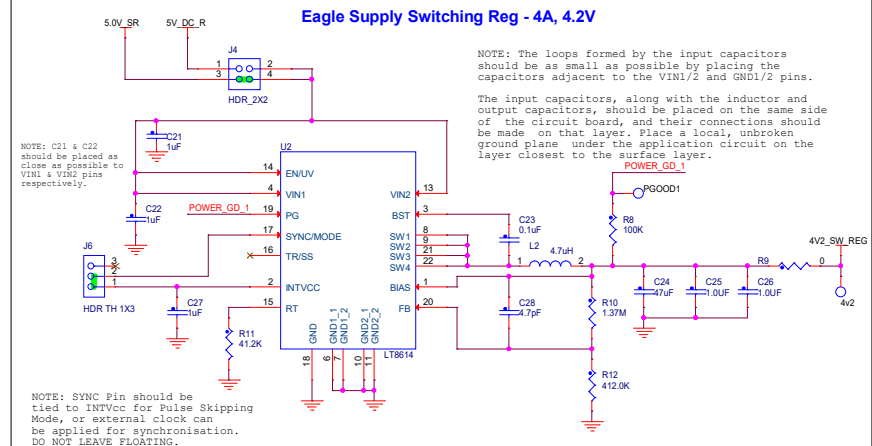
3.3v Linear Regulation for DAC



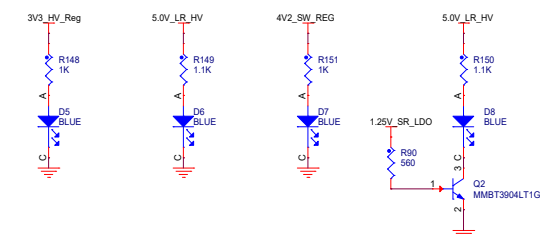
1.2v Ethernet PHY Regulator



Eagle Supply Switching Reg - 4A, 4.2V



Supply Status LED's

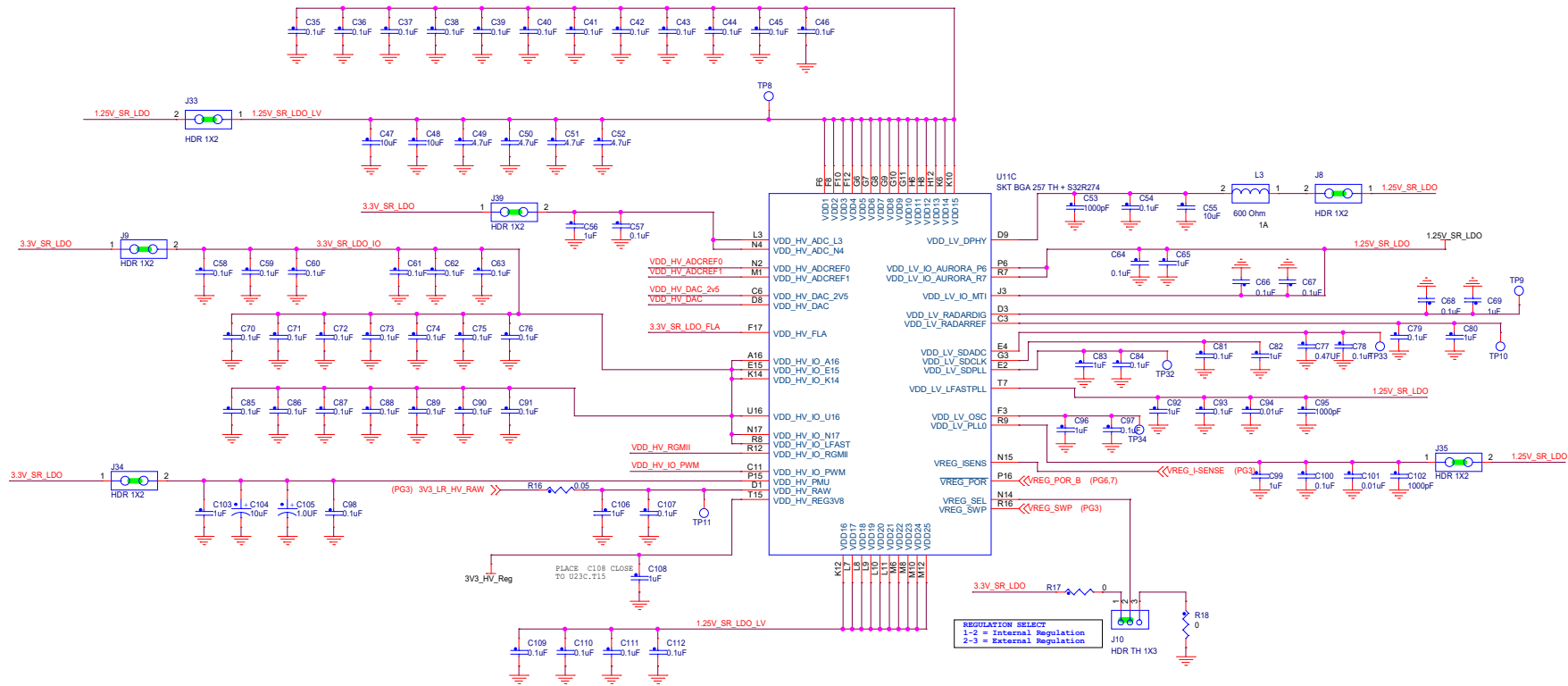


GND Test Points

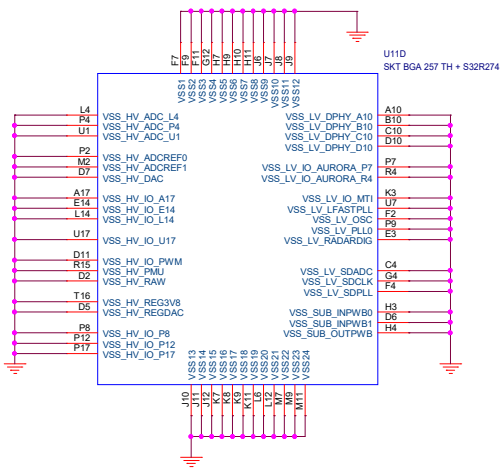


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Drawing Title: S32R274RRUEVB	
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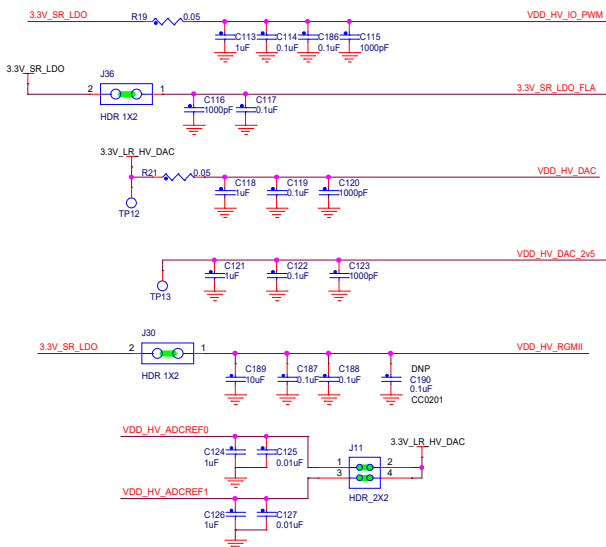
MCU Power Connections and Decoupling



MCU Grounds

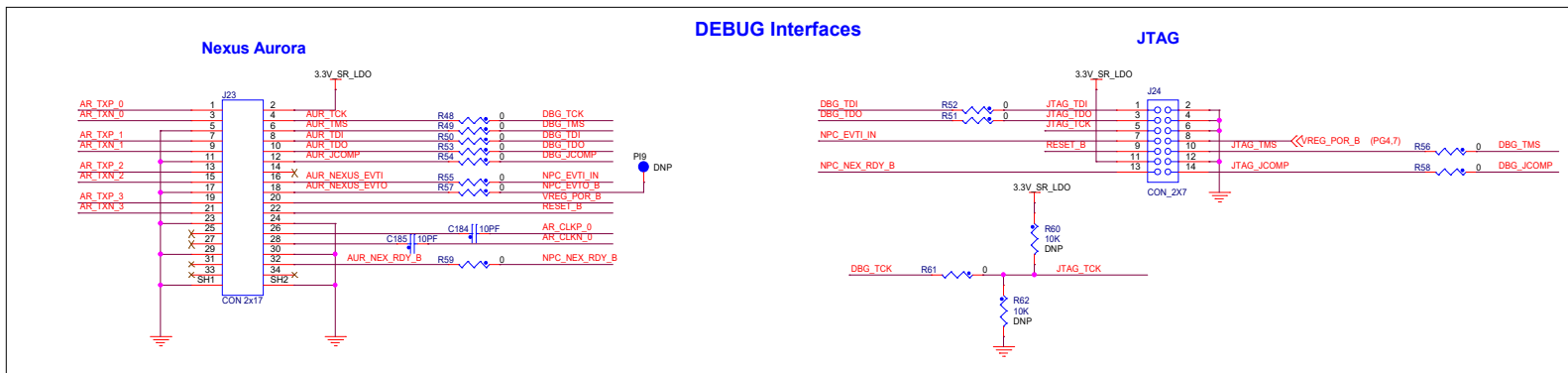
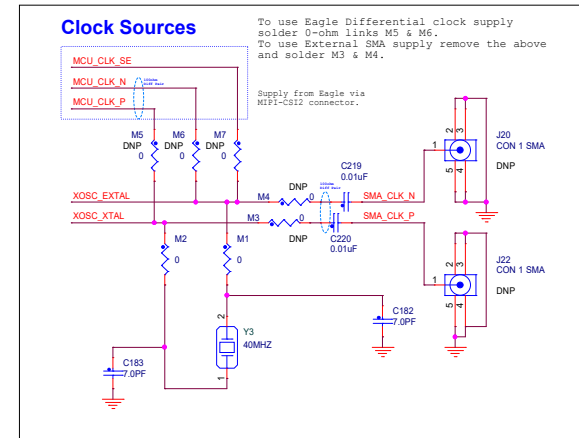
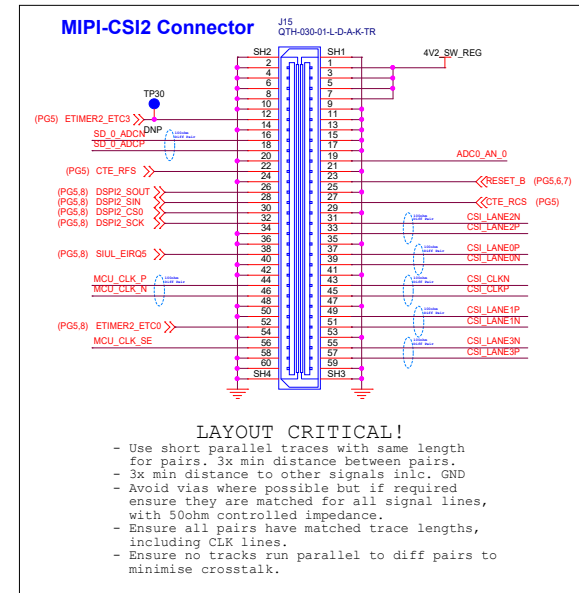
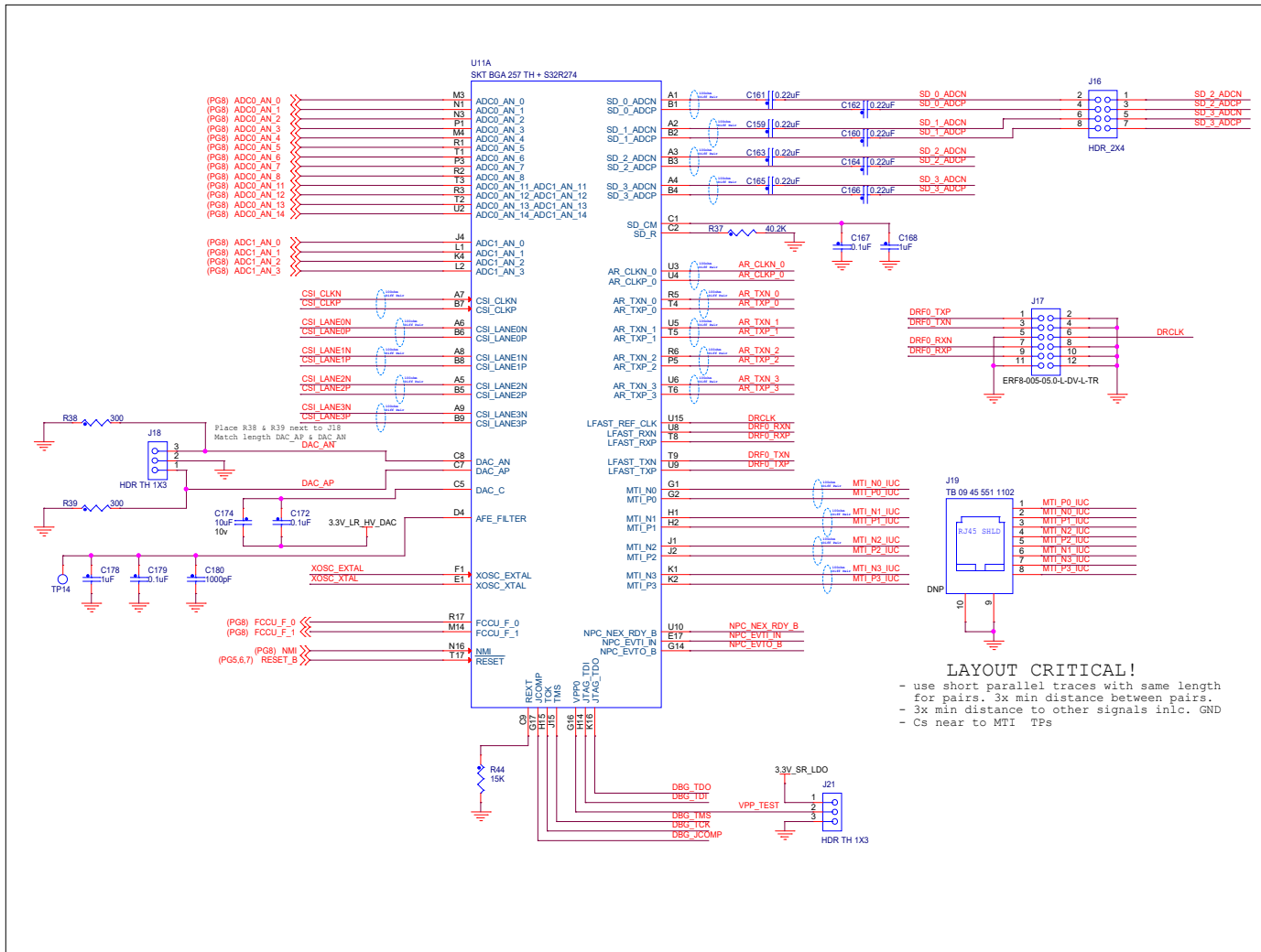


Supply Decoupling

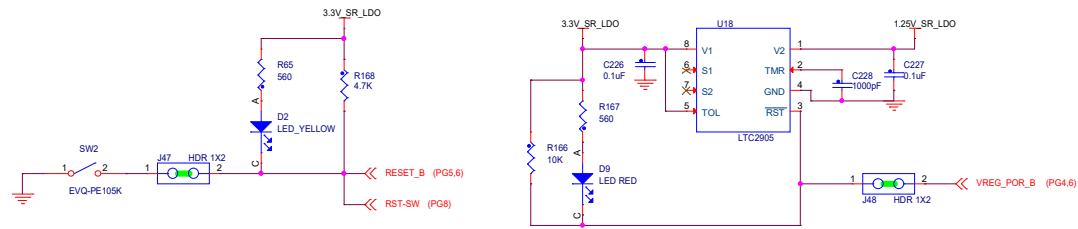


- LAYOUT NOTES:
- Decoupling caps should be placed as close to the DUT pins as possible, smallest 0201 caps should be directly on DUT pins where possible.
 - 3.3V SR LDO IO should have a wide trace length
 - VDD HV RGMII trace should have a wide (at least 200 mil) trace from 3.3V SR LDO IO.

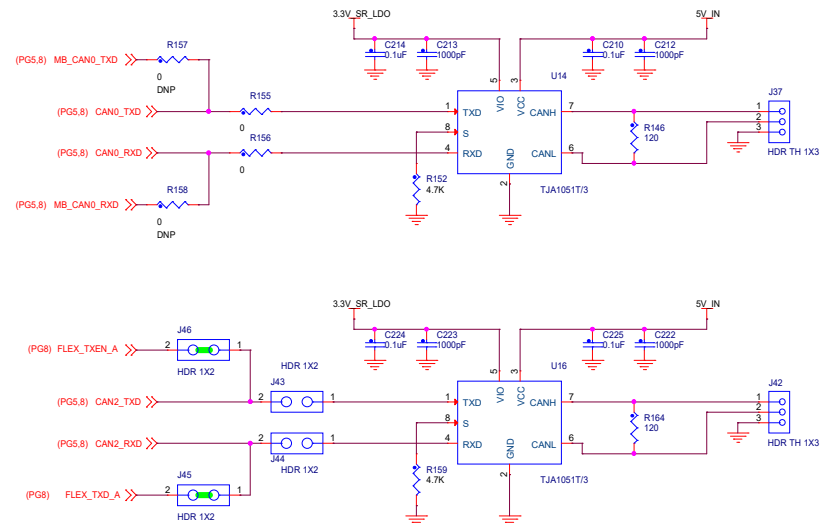
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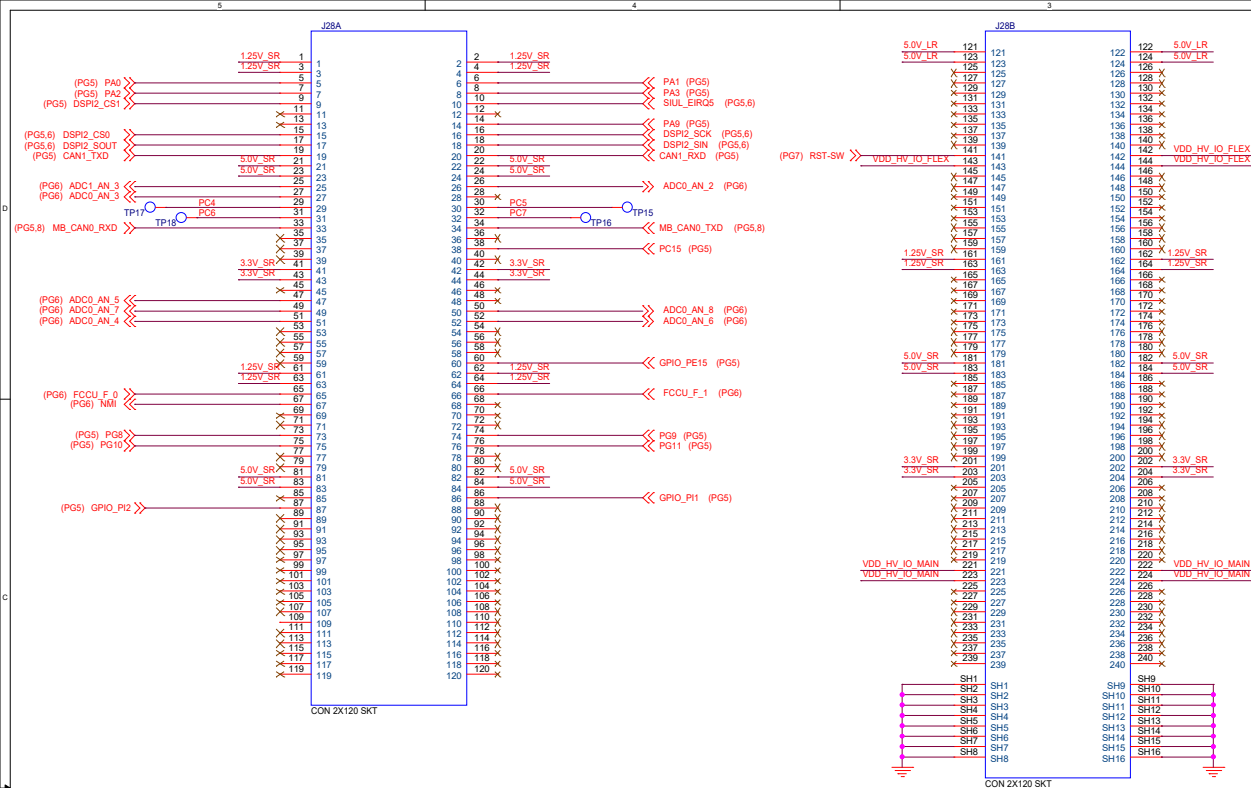


Reset Circuit



CAN FD Phy





DAUGHTER CARD TO MOTHERBOARD CONNECTOR

NOTE - Due to pin multiplexing differences, not all RRU port pins will go to the exact port number pin on the motherboard. Please check individual routings against SCH-27237.

