



NM= NOT MOUNTED

Evaluation products: This product is provided "as is" for evaluation purposes only. NXP, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. | Limitation of Liability: Use of this product is at customer's own risk. The entire liability of NXP, its affiliates and their suppliers arising out of this product shall be limited to any direct damages incurred by customer up to U.S. \$5.00. NXP, its affiliates and their suppliers shall not be liable for any other damages arising out of this product (including but not limited to special, indirect, consequential, punitive or incidental damages) even if advised of the possibility of such damages. Customer shall fully indemnify and hold harmless NXP from any liability arising out of this product.

		Add-On Board TEA1993T			0000-000-3552		REVISION 2.01		
					VARIANT <Default>		PAGE MODIFY DATE Thursday, November 12, 2015		
NAME Gerwin Folmer / PvdH		SUPERS		SH.TOT. 1		SH.NO. 130 - 01		10 A4	
DATA FMT Capture Design		PROG Allegro Design Entry		PLATF MS Windows		OUTPUT ADD. BZ 0.045			
BOARD ID : TEA1993DB1357		DAT. Friday, May 22, 2015		Property of © NXP SEMICONDUCTORS		<small>All rights are reserved. Reproduction in whole or in parts is prohibited without written consent of the copyright owner.</small>			