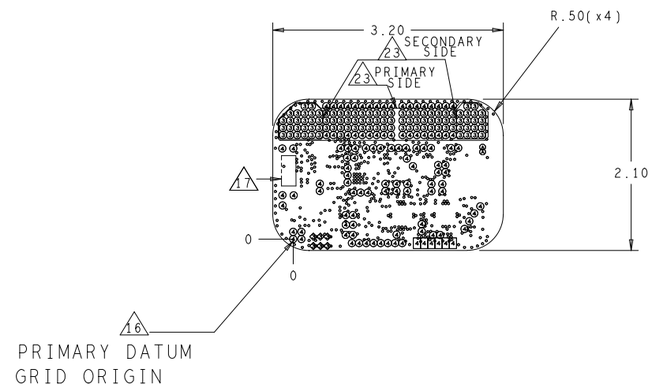
REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	A	ORIGINAL RELEASE	02-09-15	R.G
	A1	ADDITION OF SILK LABELS FOR TP	04-15-15	R.G

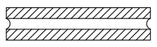
NOTES (UNLESS OTHERWISE SPECIFIED):

1. THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).
2. THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 260 DEGREES CELSIUS FOR 10 SECONDS.
3. BASE MATERIAL - LAMINATE AND PREPREG SHALL MEET IPC-4101B-26, 83 or 98
   Tg - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.
   Td - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.
4. COPPER FOIL WEIGHT - SEE STACKUP DETAIL 'A'
5. CHARACTERISTIC IMPEDANCE - NONE
6. MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .005"/.004"
7. PLATING FINISH - BOTH SIDES LEAD FREE HASL OR IMMERSION TIN. NO NICKEL
8. ALL THROUGH HOLE VIAS MAY BE PLATED SHUT.
9. SOLDERMASK - BLUE COLOR LDI SOLDERMASK BOTH SIDES.
   MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM FREESCALE.
10. SILKSCREEN - WHITE EPOXY INK, BOTH SIDES. NO SILK ON PADS.
11. ELECTRICAL TEST - 100% IPCD356.
12. PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED.
13. DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS, UNLESS PRIOR APPROVAL IS GIVEN IN WRITING BY FREESCALE.
14. TEARDROPS MAYBE ADDED AT THE FAB HOUSE TO ALL SIGNAL LAYERS.
15. TWO SOLDER SAMPLES TO BE PROVIDED.
16. BASIC GRID INCREMENT AT 1:1 IS .0001.
17. SUPPLIER MARKINGS - ON SOLDER SIDE ONLY, WHERE SHOWN.
    - MUST BE UL RECOGNIZED AND MUST HAVE AN ID THAT CONFORMS TO UL94V-0
18. THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP (P6)
19. THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C)
20. ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP. ALL HOLE LOCATION TOLERANCES ARE TO BE +/-.002 IN REFERENCE TO THE PRIMARY DATUM.
21. FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURERS REQUIREMENTS. THE ADDITION OF RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER.PANELIZATION MUST BE APPROVED BY CONTRACT MANUFACTURER.
22. INTENTIONAL SHORT ON J8 BETWEEN NETS GND AND SA_GYRO
    INTENTIONAL SHORT ON J12 BETWEEN NETS P3V3 AND P3V3_MAG
    INTENTIONAL SHORT ON J13 BETWEEN NETS P3V3 AND P3V3_ACCEL
    INTENTIONAL SHORT ON J14 BETWEEN NETS P3V3 AND P3V3_PRESSURE
    INTENTIONAL SHORT ON J15 BETWEEN NETS P3V3 AND P3V3_GYRO
    INTENTIONAL SHORT ON J16 BETWEEN NETS P3V3 AND P3V3_COMBO
    INTENTIONAL SHORT ON J17 BETWEEN NETS P3V3 AND P3V3_SPI_ACC
    INTENTIONAL SHORT ON J18 BETWEEN NETS P3V3_PEDOM AND P3V3
    INTENTIONAL SHORT ON J19 BETWEEN NETS P4V2_BAT_OUT AND P4V2_BAT
    INTENTIONAL SHORT ON J20 BETWEEN NETS P1V8 AND P1V8_PED
    INTENTIONAL SHORT ON J21 BETWEEN NETS GND AND SA1_FXOS8700CQ
    INTENTIONAL SHORT ON J23 BETWEEN NETS GND AND SA0_FXOS8700CQ
    INTENTIONAL SHORT ON J25 BETWEEN NETS BT_RX0 AND BT_TX
    INTENTIONAL SHORT ON J26 BETWEEN NETS BT_TX0 AND BT_RX
23. DO NOT PROVIDE SOLDERMASK CLEARANCES FOR THE SPECIFIC LOCATIONS LEAVE SOLDERMASK AS THEY APPEAR ON CAD.



DRILL CHART: TOP to BOTTOM ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
8 ◦	10.0	+0.0/-10.0	PLATED	428
8 ◦	12.0	+0.0/-12.0	PLATED	24
◎	35.0	+2.0/-2.0	PLATED	98
✥	35.0	+2.0/-2.0	PLATED	6
⊛	40.0	+3.0/-3.0	PLATED	107
⊡	41.0	+3.0/-3.0	PLATED	6
⊛	28.0	+2.0/-2.0	NON-PLATED	2

FINISHED Cu WEIGHT



LAYER 1 L1_PS 1 oz.
LAYER 2 L2_SS 1 oz.

DETAIL A
LAYER STACKUP
SCALE: NONE

___ PUBI (PUBLIC INFORMATION)
_X_ FIUO (FREESCALE INTERNAL USE ONLY)
___ FCP (FREESCALE CONFIDENTIAL PROPRIETARY)

THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO FREESCALE AND SHALL NOT BE USED FOR ENGINEERING DESIGN PROCUREMENT OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF FREESCALE.

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:
DECIMALS ANGLES
.XX .01 ∠0-30'
.XXX .005
√ RMS ALL MACHINED SURFACES
BREAK ALL SHARP EDGES AND CORNERS. REMOVE BURRS.
UNDERLINED DIM. NOT TO SCALE.
THIRD ANGLE ORTHOGRAPHIC PROJECTION IS USED.

PART NO. 170-28727

APPROVALS	DATE
DRAWN VANIPRIYA [LnT]	04-15-15
CHECKED ROSALIA G.	04-15-15
DESIGN ENGINEER SENDHIL [LnT]	04-15-15

FREESCALE SEMICONDUCTOR
6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 USA

TITLE: PRINTED WIRING BOARD
FRDM-FXS-MULT2-B

SIZE	CAD FILE NAME	DWG. NO.	REV
D	LAY-28727	FAB-28727	A1

SCALE 1/1 DO NOT SCALE DRAWING SHEET 1 OF 1