


Table of Contents	
1	TITLE, TOC & REV
2	NOTES
3	BA13 & BA13e
4	MCU-SBC
5	Connectors

Revisions			
Rev	Description	Date	Approved
X1	Original Release	07-NOV-14	J.GODILLON
A	Original Release	12-NOV-14	J.GODILLON

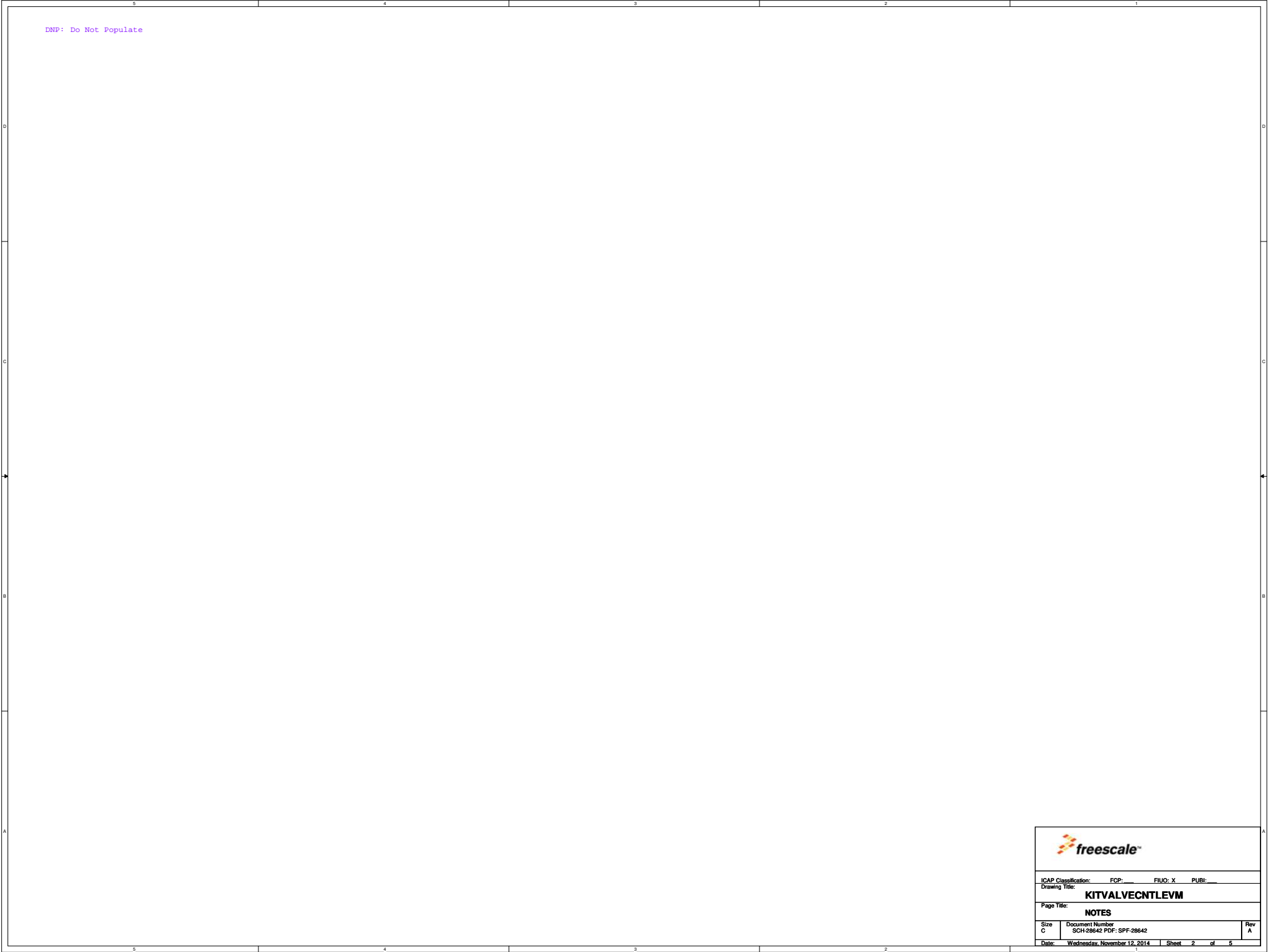
KITVALVECNTLEVM



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Designer: J.GODILLON	Drawing Title: KITVALVECNTLEVM		
Drawn by: J.GODILLON	Page Title: TITLE, TOC & REV		
Approved: J.GODILLON	Size C	Document Number SCH-28642 PDF: SPF-28642	Rev A
Date: Wednesday, November 12, 2014 Sheet 1 of 5			



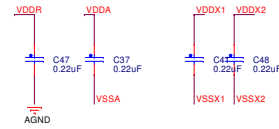
Voltage Source

VAPS_VPWR << VAPS_VPWR
VCCS << VCCS
AGND << AGND
AGND << AGND
DOSV << DOSV

RESET

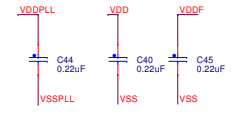
SB0800_RESET << SB0800_RESET
SB0410_RESET << SB0410_RESET

External supply

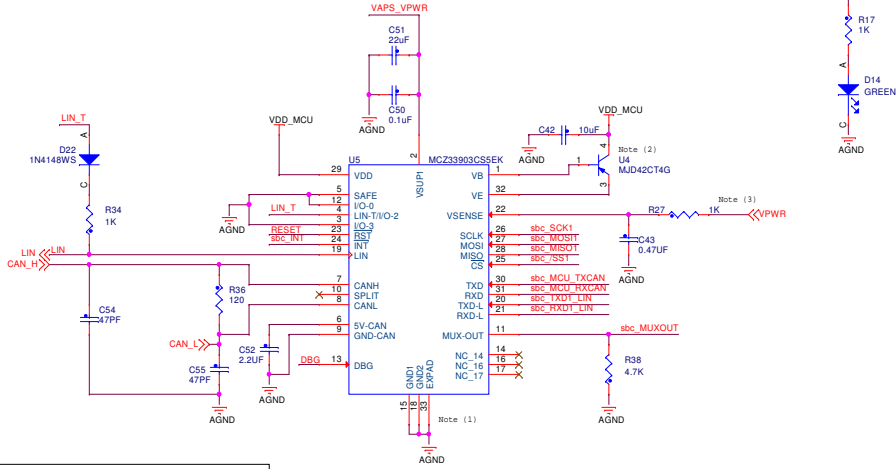


VDDR: Supply the internal Reg.
VDDX: Supply for I/O pins
VDDA: Supply for A/D pins

Internal supply



VDD: Internal Supply core.
VDDF: Supply NVM (EEPROM)
VDDPLL: Supply PLL oscillator



Select Mode for SBC

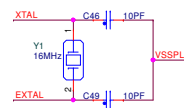
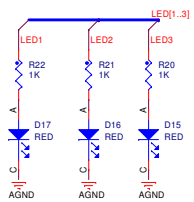
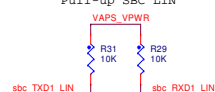


Pin	Debug
1 & 2	Debug Mode
2 & 3	Normal Mode

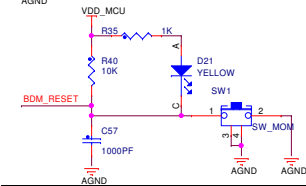
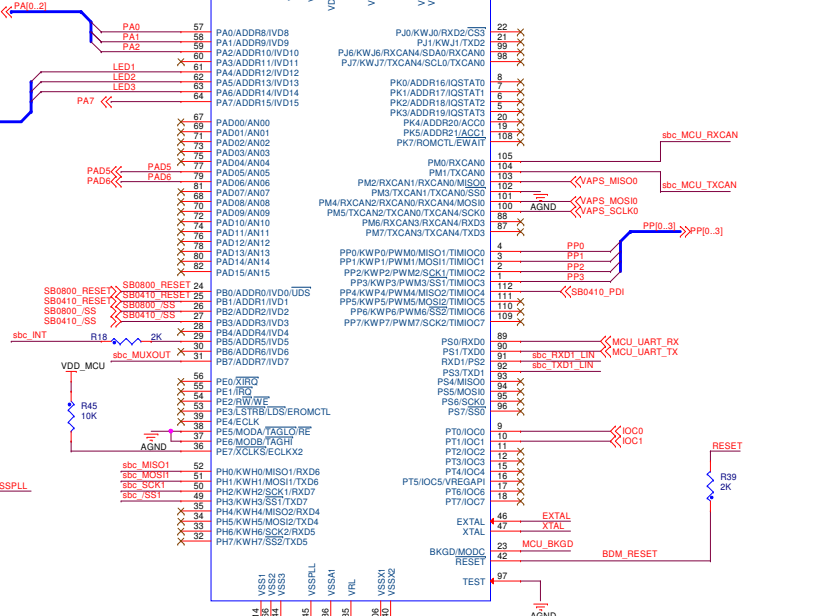
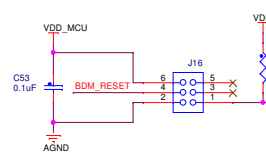
Note (1): Expad should be short to GND analog
Note (2): Share the Power dissipation between SBC and PNP
Note (3): VSENCE protected by internal circuitry against Inverted polarity
Note (4): Capacitors must be connected short to VSSPLL

Comments:
- PNP U5 used to drive more current for MCU > 150mA
- LIN: D26 + R70 => EMC performance
- CAN: R75 & R76 respect ISO 11898 rules => 1/4W

Pull-up SBC LIN



Interface to program MCU (BDM)



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Page Title: **MCU-SBC**
Size C Document Number SCH-28642 PDF: SPF-28642
Date: Wednesday, November 12, 2014 Sheet 4 of 5

