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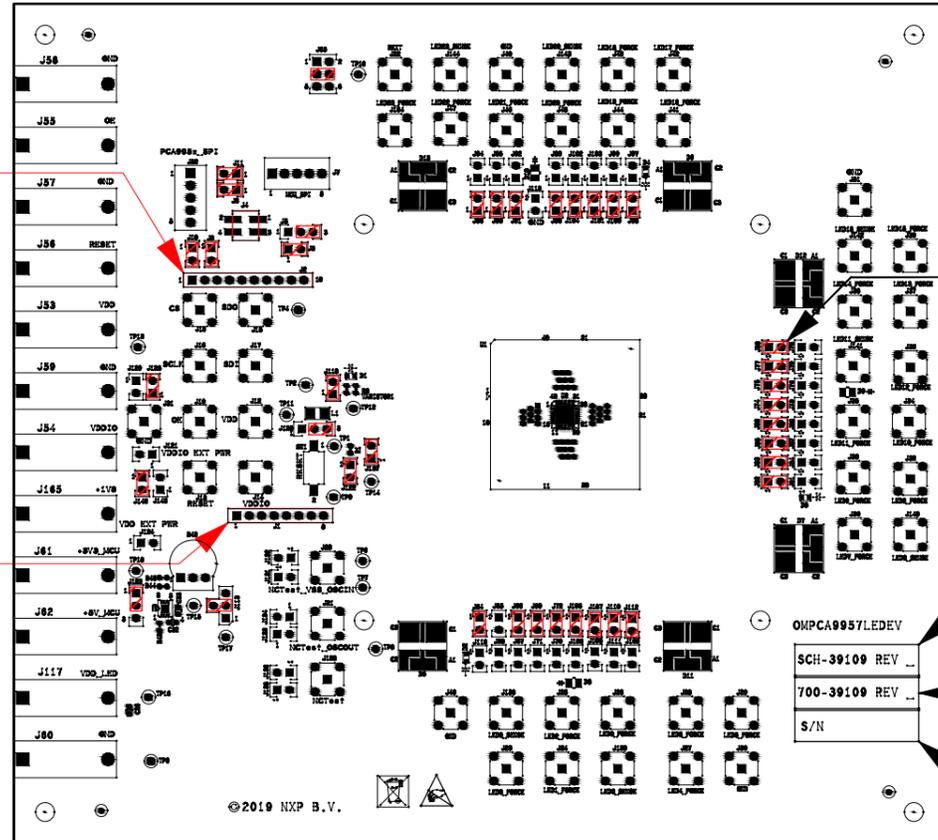
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NOTES : TRIANGLE SYMBOLS IDENTIFY NOTES ON THIS DRAWING.

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LABEL TEXT
ORIENTATION
DO NOT COVER IC MPN

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	A	PRODUCTION RELEASE	2/12/20	BRIAN WANG



PRIMARY SIDE

NOTES:

- ALL COMPONENTS MUST BE ABLE TO WITHSTAND THE Pb-FREE ASSEMBLY PROFILES DESCRIBED IN IPC/JEDEC J-STD-020C (LATEST VERSION)
- WORKMANSHIP SHALL CONFORM TO IPC-A-610 (LATEST REVISION) - CLASS 2 STANDARD.
- PART SUBSTITUTION CRITERIA :-
ALL COMPONENT SUBSTITUTIONS MUST BE APPROVED AND DOCUMENTED BY NXP.
- MAXIMUM LEAD PROTRUSION ON PRIMARY AND SECONDARY SIDE OF PRINTED WIRING BOARD IS .070".
- IN CASES OF CONFLICT, ALL PLACEMENT LOCATIONS ARE TO BE GOVERNED BY THE X-Y CENTROID FILE, UNLESS EXPLICITLY STATED IN THIS ASSEMBLY DRAWING. SILKSCREEN REFERENCE DESIGNATORS ARE INCLUDED FOR CONVENIENCE ONLY
- PLACE PRINTED SERIAL NUMBER LABEL (805-75116) WHERE SHOWN.
- PLACE PRINTED SCHEMATIC/REVISION LABEL (805-75918) WHERE SHOWN, WHICH MUST BE PRINTED "SCH-39109 REV" AND REVERSION REFER TO IT IN AGILE SYSTEM BUT NOT THE REVISION OF THIS DRAWING.
- PLACE PRINTED ASSEMBLY/REVISION LABEL (805-75115) WHERE SHOWN, WHICH MUST BE PRINTED "700-39109 REV" AND REVERSION REFER TO IT IN AGILE SYSTEM, BUT NOT THE REVISION OF THIS DRAWING.
- DO NOT TRIM LEADS OF MATING CONNECTORS: (J1, J2, J3)**

RoHS : ALL PARTS, MATERIALS AND FINISHED ASSEMBLY SHALL MEET THE RoHS COMMISSION DELEGATED DIRECTIVE (EU) 2015 / 863 OF MARCH 31 2015 AMENDING ANNEX II TO DIRECTIVE 2011 / 65 / EU. A CERTIFICATE OF COMPLIANCE IS REQUIRED UPON REQUEST.

REACH : ALL PARTS, MATERIALS AND FINISHED ASSEMBLY SHALL NOT CONTAIN ANY OF THE SUBSTANCES OF VERY HIGH CONCERN (SVHC) ABOVE THE THRESHOLD VALUE PER THE CURRENT ECHA LIST OF SVHC'S, AND WITH THE ANNEX XIV OF REACH DIRECTIVE 76 / 769 / EEC.. A CERTIFICATE OF COMPLIANCE IS REQUIRED UPON REQUEST.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES.
TOLERANCES ARE:
DECIMALS
.XX ±.020
.XXX ±.010
✓ RMS ALL MACHINED SURFACES
BREAK ALL SHARP EDGES AND CORNERS,
REMOVE BURRS.
UNDERLINED DIM. NOT TO SCALE.
THIRD ANGLE ORTHOGRAPHIC PROJECTION IS USED.

PART NO.
700-39109

THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO NXP AND SHALL NOT BE USED FOR ENGINEERING DESIGN, PROCUREMENT OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF FREESCALE.

--- PUBI (PUBLIC INFORMATION)
X FUD (NXP INTERNAL USE ONLY)
--- FCP (NXP CONFIDENTIAL PROPRIETARY)

MATERIAL

APPROVALS

DATE

FINISH

DRAWN
EVAN HE

3/3/20

HEAT TREAT

CHECKED
BRIAN WANG

3/3/20

DESIGN ENGINEER
HELEN XU

3/3/20



NXP semiconductor

6501 WILLIAM CANNON DRIVE
AUSTIN, TEXAS 78735-8598 USA

TITLE
X-OMPCA9957LEDEV
PWA ASSEMBLY

SIZE	FILE NAME	DWG. NO.	REV
B	ASY-39109.DXF	ASY-39109	X2

SCALE ----- DO NOT SCALE DRAWING SHEET 1 OF 2

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DWG. NO. ASY-39109
REV X2

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