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4	USB & J1F Socket
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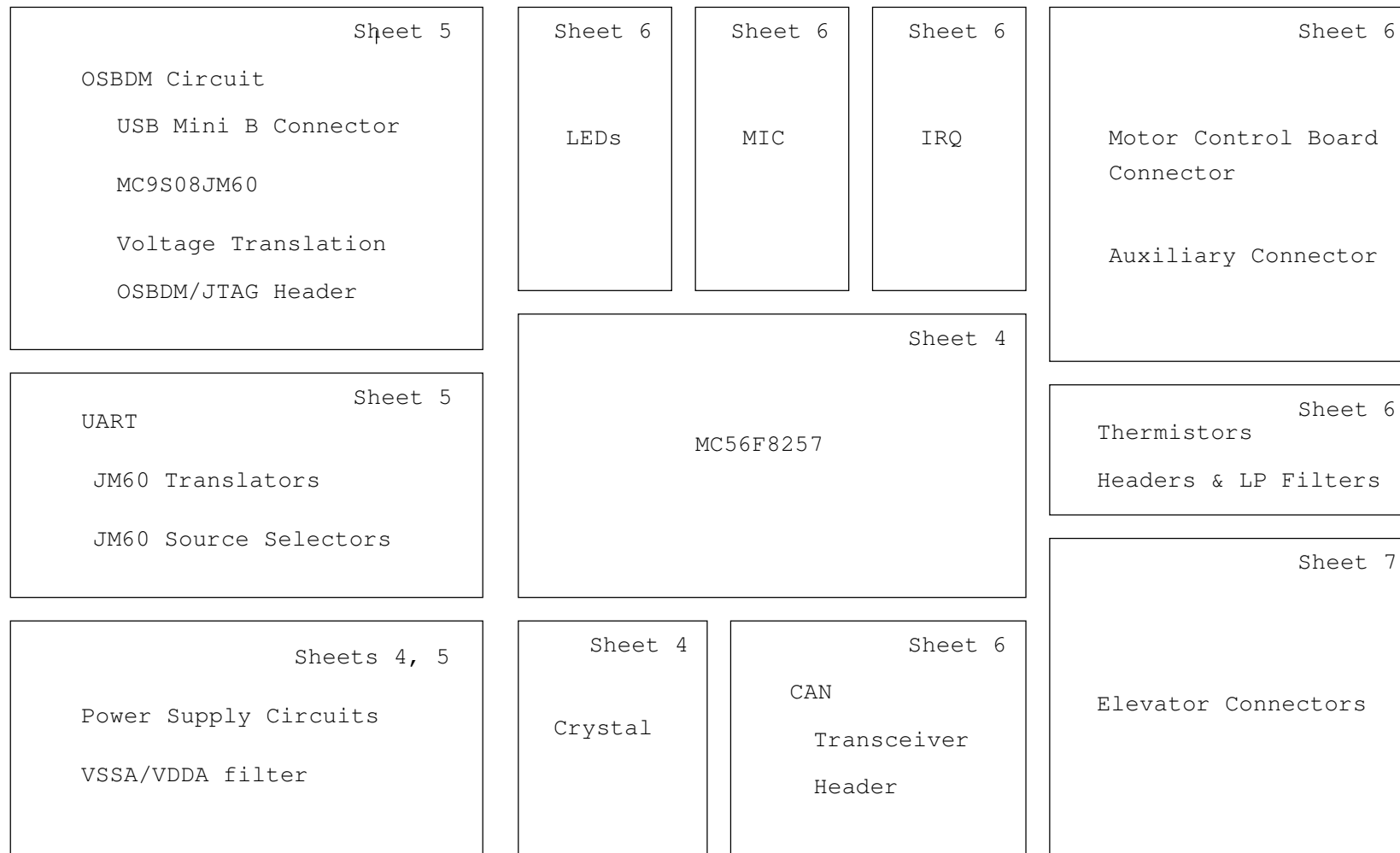
Revisions			
Rev	Description	Date	Approved
A	Prototype Release	16Jun09	J.H.
B	Pilot Update	12Oct10	J.H.

		Microcontroller Solutions Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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Designer: Jay Hartvgsen		Drawing Title: TWR-MC56F8257	
Drawn by: Jay Hartvgsen		Page Title: Table of Contents/Revisions	
Approved: Jay Hartvgsen	Size: C	Document Number: SCH-26034 PDF: SPF-26034	Rev: B
Date: Tuesday, October 12, 2010		Sheet 1 of 7	

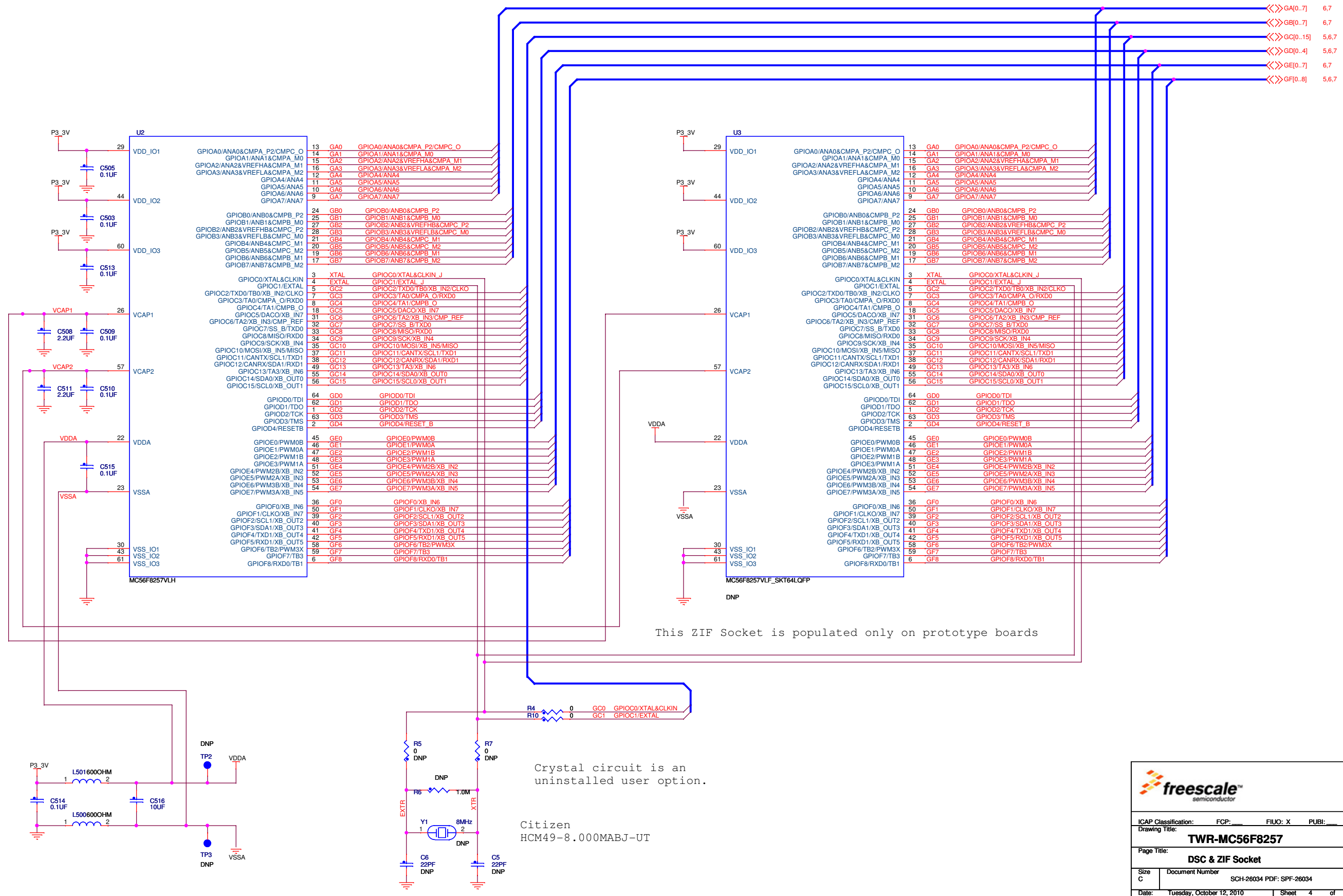
All capacitors are in uF
All voltages are DC
All polarized capacitors are aluminum electrolytic

- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes - Active-Low Signal
 - <> or [] Denotes - Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

		
ICAP Classification: FCP: FILU: X PUBI:		
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The DSC footprint and the ZIF socket are concentric on the board. Boards are built with a surface mounted DCS or with the DSC in the socket - not both.



This ZIF Socket is populated only on prototype boards

Crystal circuit is an uninstalled user option.

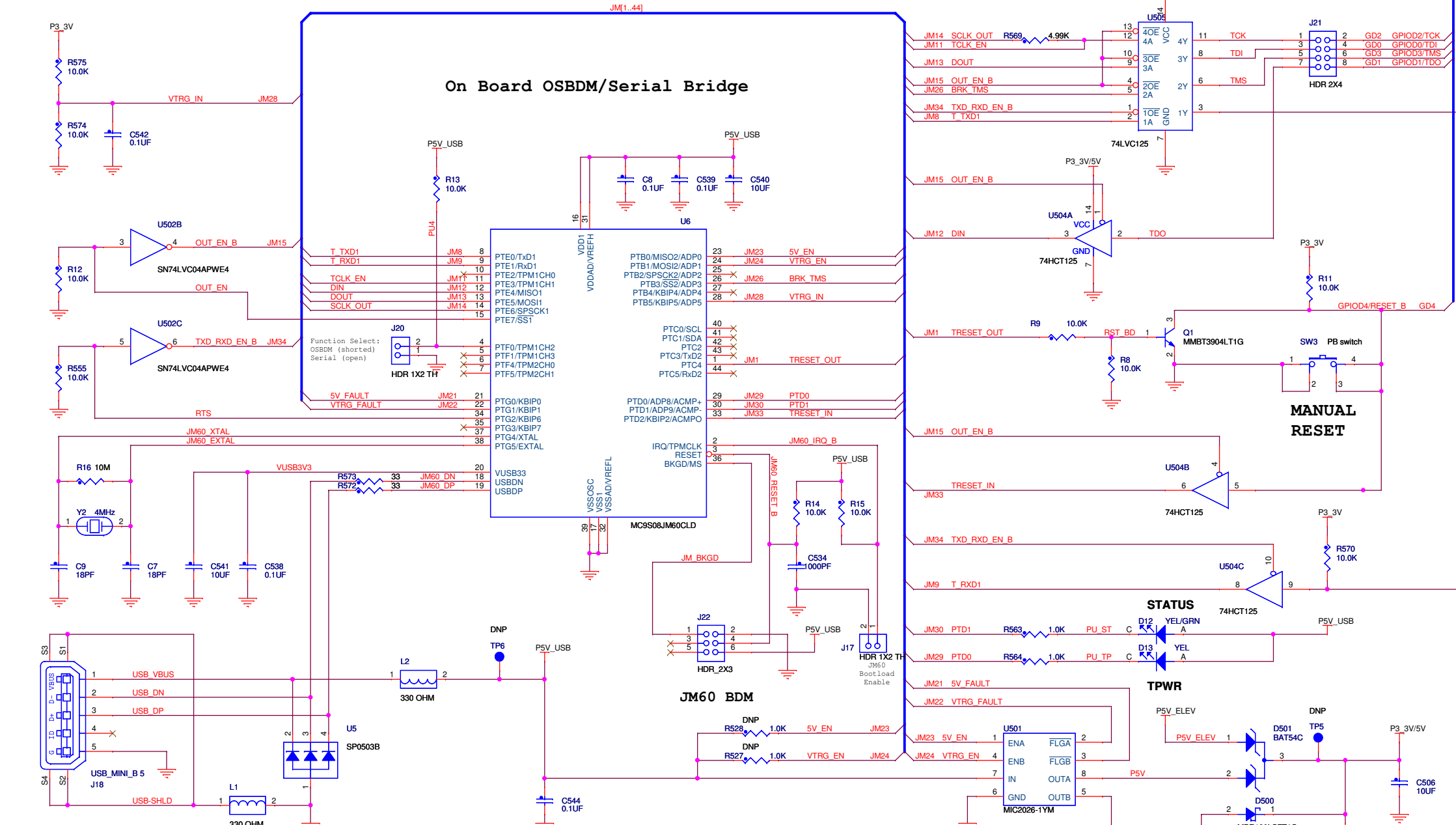
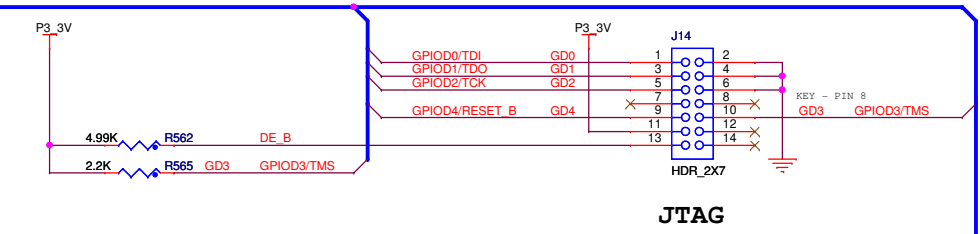
Citizen HCM49-8.000MABJ-UT

ICAP Classification: FCP: FILUO: X PUBI:
 Drawing Title: **TWR-MC56F8257**
 Page Title: **DSC & ZIF Socket**

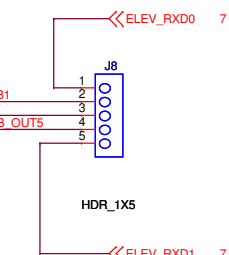
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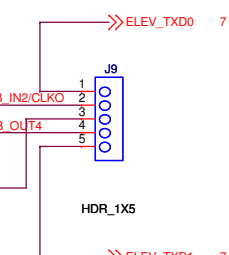
4,6,7 GF[0..8] <<>
4,6,7 GC[0..15] <<>



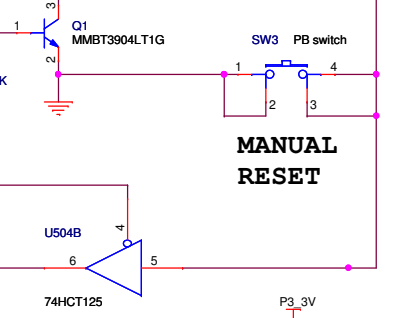
RXD Source Select



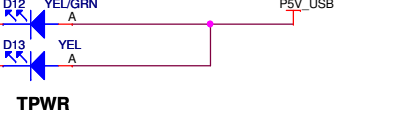
TXD Source Select



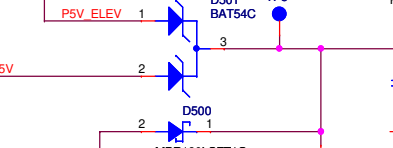
MANUAL RESET



STATUS

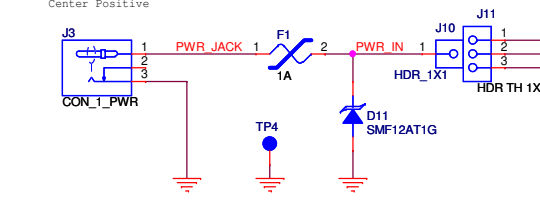


TPWR

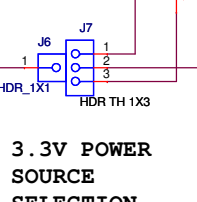


Decoupling on Buffers and Inverters

5V POWER SOURCE SELECTION



3.3V POWER SOURCE SELECTION



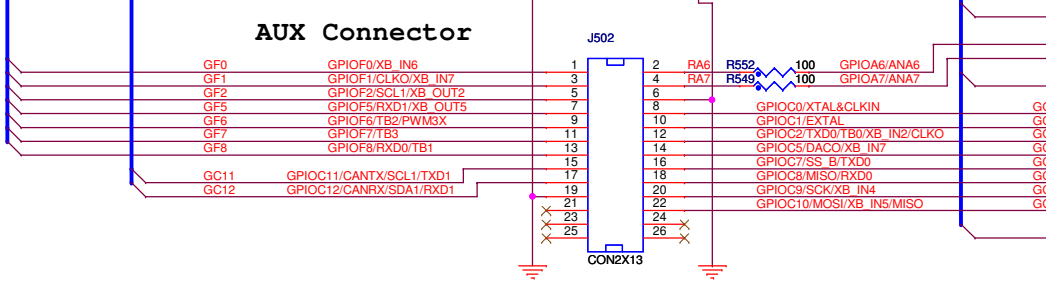
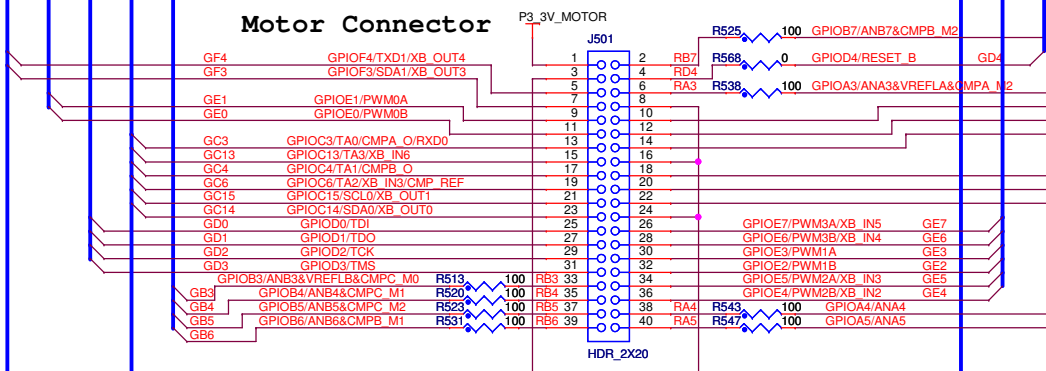
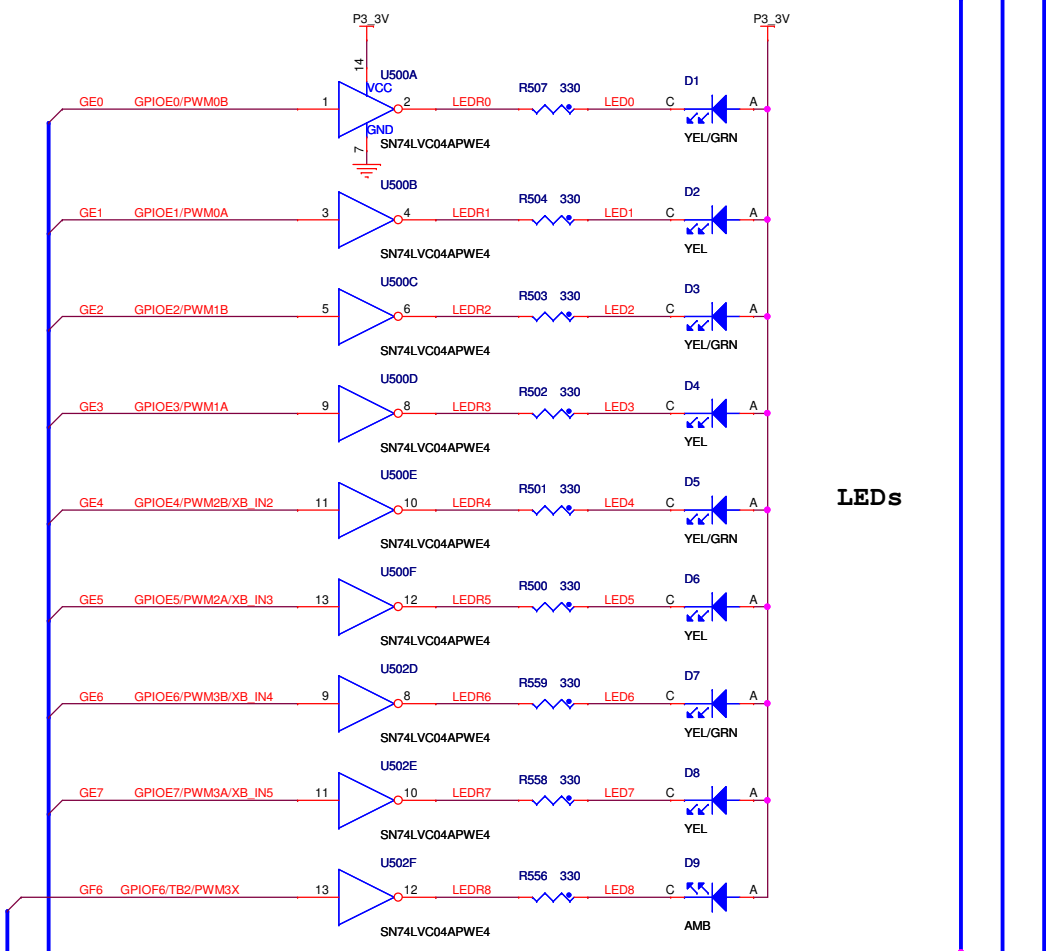
ICAP Classification: FCP: _____ FIUO: X PUBI: _____		Drawing Title: TWR-MC56F8257	
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SPARES

Power On



4.5,7 GC[0..15] <<>
 4.5,7 GD[0..4] <<>
 4.7 GE[0..7] <<>
 4.5,7 GF[0..8] <<>



90C to -20C

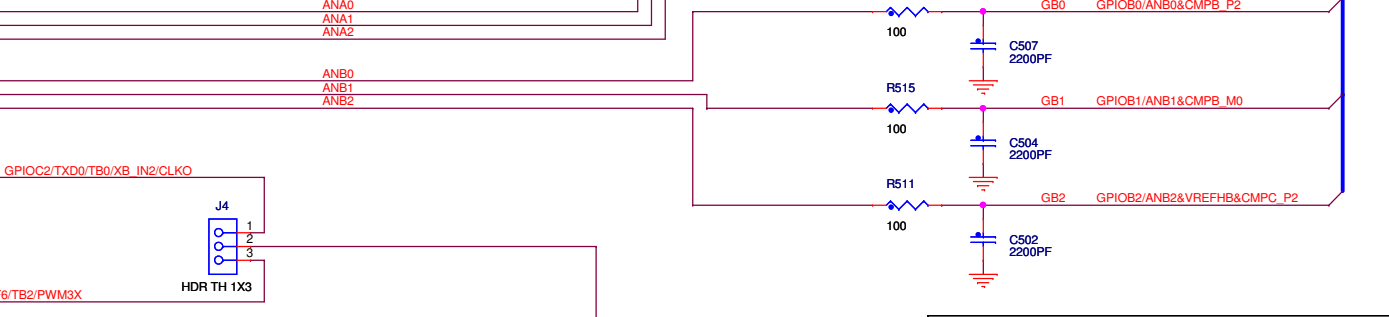
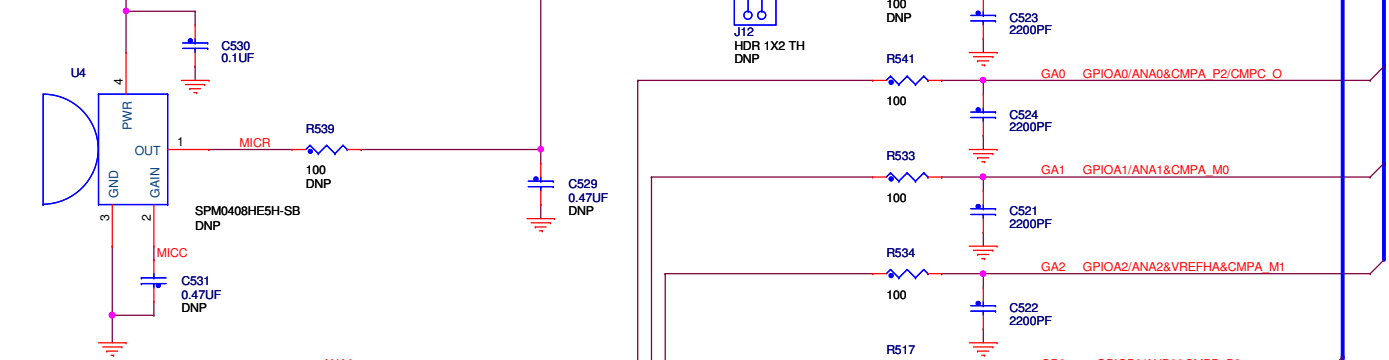
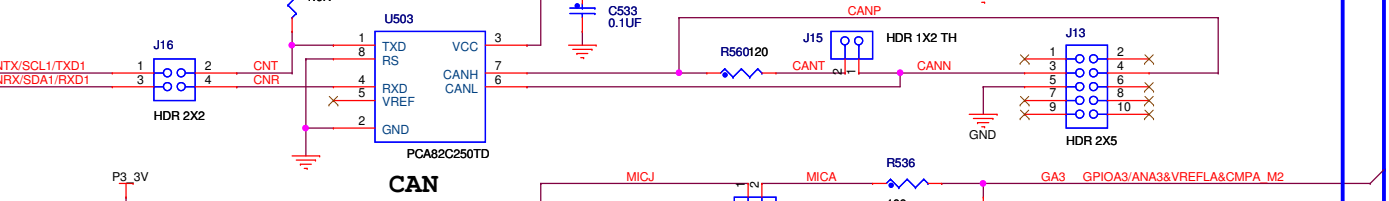
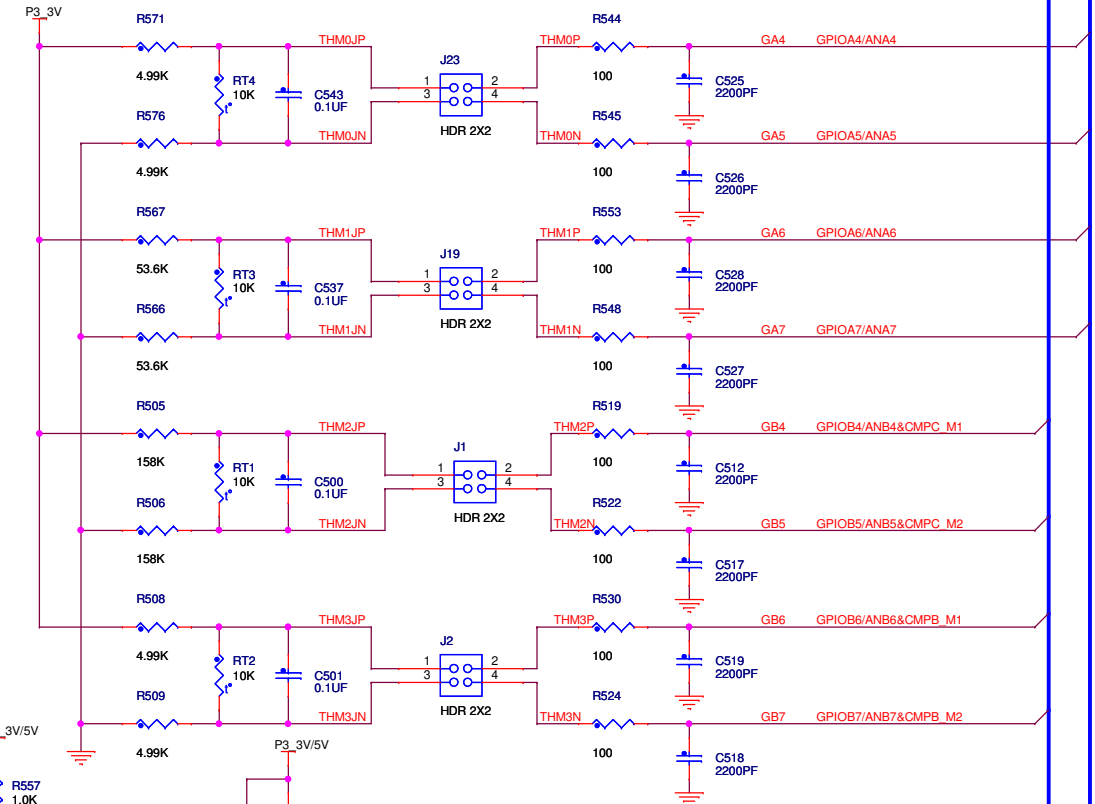
Vdiff ~ 0.305V
to 3.001V
(Ta=25C 1.650V)
Use Gain = 1

Vdiff ~ 0.031V
to 1.539V
(Ta=25C 0.282V)
Use Gain <= 2

Vdiff ~ 10.4mV
to 793.3mV
(Ta=25C 101.2mV)
Use Gain <= 4

Vdiff ~ 0.305V
to 3.001V
(Ta=25C 1.650V)
Use Gain = 1

Thermistors



freescale semiconductor

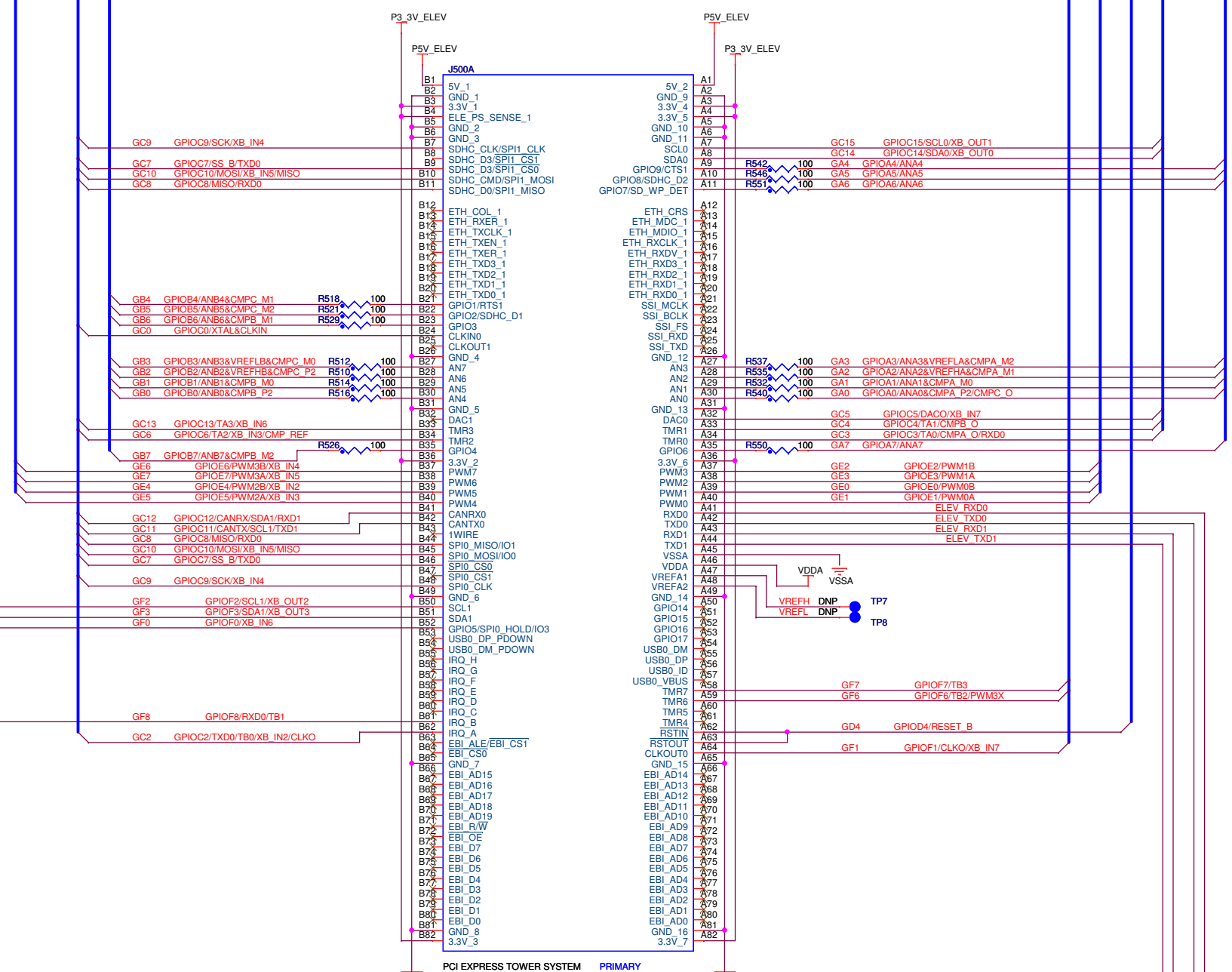
ICAP Classification: FCP: FILU: X PUBI:
 Drawing Title: **TWR-MC56F8257**
 Page Title: **Peripherals & Motor Connect**

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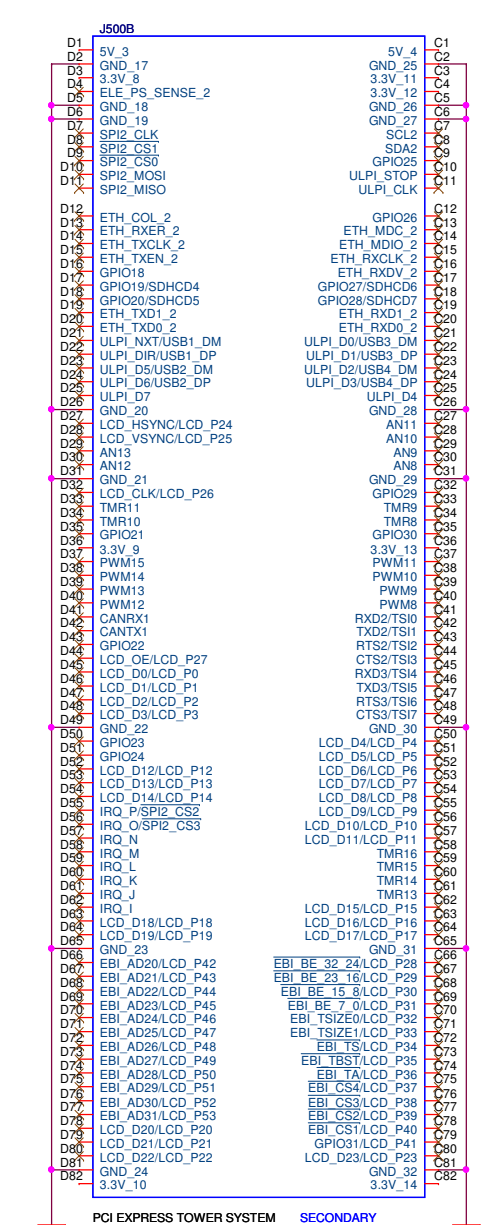
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4,6 GB[0..7] <<>
 4,5,6 GC[0..15] <<>
 4,5,6 GD[0..4] <<>
 4,6 GE[0..7] <<>
 4,5,6 GF[0..8] <<>



PCI EXPRESS TOWER SYSTEM PRIMARY



PCI EXPRESS TOWER SYSTEM SECONDARY

5 ELEV_RXD0 <<>
 5 ELEV_TXD0 <<>
 5 ELEV_RXD1 <<>
 5 ELEV_TXD1 <<>

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ICAP Classification: FCP: _____ FIUO: X PUBI: _____
 Drawing Title: **TWR-MC56F8257**
 Page Title: **Tower Elevator Connectors**

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