

U?

D3	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5		
D4	PTA1/JTAG_TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6	VDD1	C5
H6	PTA12/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA	VDD2	G7
G6	PTA13/LLWU_P4/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB	VDD3	E4
H8	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0		
G8	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	VDDA	F4
E5	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7		
D5	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0	VREFH	G4
G5	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1		
F5	PTA5/FTM0_CH2/I2S0_TX_BCLK/JTAG_TRST	VREFL	G3
F7	PTB0/LLWU_P5/ADC0_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	VBAT	H5
F6	PTB1/ADC0_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB		
E6	PTB16/TSIO_CH9/UART0_RX/EWM_IN		
D7	PTB17/TSIO_CH10/UART0_TX/EWM_OUT		
D6	PTB18/TSIO_CH11/I2S0_TX_BCLK	VREF_OUT/CMP1_IN5/CMP0_IN5	H1
C7	PTB19/TSIO_CH12/I2S0_TX_FS		
E7	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3		
E8	PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0	ADC0_DP0 ADC0_DM0	G1 F1
D8	PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG		
C6	PTC1/LLWU_P6/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0		
B4	PTC10/I2S0_RX_FS	ADC0_DP3 ADC0_DM3	G2 F2
A5	PTC11/LLWU_P11		
B7	PTC2/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS		
C8	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK		
B8	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT		
A8	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT	EXTAL32	H4
A7	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK		
B6	PTC7/CMP0_IN1/SPI0_SIN/I2S0_RX_FS		
A6	PTC8/CMP0_IN2/I2S0_MCLK		
B5	PTC9/CMP0_IN3/I2S0_RX_BCLK	XTAL32	H3
C3	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS		
A4	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS		
C2	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX	CMP1_IN3/ADC0_SE23	H2
B3	PTD3/SPI0_SIN/UART2_TX		
A3	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN		
C1	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT	RESET	F8
B2	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0		
A2	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1		
A1	PTE0/UART1_TX/RTC_CLKOUT	VSS1	C4
B1	PTE1/LLWU_P0/UART1_RX	VSS2	H7
E1	PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3	VSS3	E3
D1	PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3		
E2	PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA	VSSA	F3
D2	PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL		

<Value>