



E5
E6
E7
E8
F8
L6
H5
VDD1
VDD2
VDD3
VDD4
VDD5
VDD6
VBAT
VDDA

J5	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FTM3_CH0/FB_ALE/FB_CS1/FB_TS/I2S1_RXD1	A5
J6	PTA1/JTAG_TDI/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FTM3_CH1/FB_CS0/I2S1_RXD0	D4
K6	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FTM3_CH2/FB_AD4/I2S1_RX_FS	B4
K7	PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0	PTD3/SPI0_SIN/UART2_TX/FTM3_CH3/FB_AD3/I2S1_RX_BCLK	C4
L7	PTA4/LLWU_P3/NMI/EZP_CS/TSI0_CH5/FTM0_CH1	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/NFC_DATA1/EWM_IN	A4
M8	PTA5/USB_CLKIN/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/NFC_DATA0/EWM_OUT	A3
J7	PTA6/ADC3_SE6A/ULPI_CLK/FTM0_CH3/I2S1_RXD0/CLKOUT/TRACE_CLKOUT	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	A2
J8	PTA7/ADC0_SE10/ULPI_DIR/FTM0_CH4/I2S1_RX_BCLK/TRACE_D3	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	A1
K8	PTA8/ADC0_SE11/ULPI_NXT/FTM1_CH0/I2S1_RX_FS/FTM1_QD_PHA/TRACE_D2	PTD8/I2C0_SCL/UART5_RX/FB_A16/NFC_CLE	C9
L8	PTA9/ADC3_SE5A/ULPI_STP/FTM1_CH1/FTM1_QD_PHB/TRACE_D1	PTD9/I2C0_SDA/UART5_TX/FB_A17/NFC_ALE	B9
M9	PTA10/ADC3_SE4A/ULPI_DATA0/FTM2_CH0/FTM2_QD_PHA/TRACE_D0	PTD10/UART5_RTS/FB_A18/NFC_RE	B3
L9	PTA11/ADC3_SE15/ULPI_DATA1/FTM2_CH1/FTM2_QD_PHB	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	B2
K9	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA	PTD12/SPI2_SCK/FTM3_FLT0/SDHC0_D4/FB_A20	B1
J9	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	C3
L10	PTA14/CMP3_IN0/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1	PTD14/SPI2_SIN/SDHC0_D6/FB_A22	C2
L11	PTA15/CMP3_IN1/SPI0_SCK/UART0_RX/I2S0_RXD0	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	C1
K10	PTA16/CMP3_IN2/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1		
K11	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK		
M12	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA/RTC_CLKOUT	D3
M11	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL/SPI1_SIN	D2
K12	PTA24/CMP3_IN4/ULPI_DATA2/FB_A29	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK	D1
J12	PTA25/CMP3_IN5/ULPI_DATA3/FB_A28	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD/SPI1_SOUT	E4
J11	PTA26/ADC2_SE15/ULPI_DATA4/FB_A27	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3	E3
J10	PTA27/ADC2_SE14/ULPI_DATA5/FB_A26	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2/FTM3_CH0	E2
H12	PTA28/ADC2_SE13/ULPI_DATA6/FB_A25	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/FTM3_CH1/USB_SOF_OUT	F4
H11	PTA29/ADC2_SE12/ULPI_DATA7/FB_A24	PTE7/UART3_RTS/I2S0_RXD0/FTM3_CH2	F3
		PTE8/ADC2_SE16/I2S0_RXD1/UART5_TX/I2S0_RX_FS/FTM3_CH3	F2
H10	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/ADC2_SE8/ADC3_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	PTE9/ADC2_SE17/I2S0_TXD1/UART5_RX/I2S0_RX_BCLK/FTM3_CH4	F1
H9	PTB1/ADC0_SE9/ADC1_SE9/ADC2_SE9/ADC3_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	PTE10/UART5_CTS/I2S0_TXD0/FTM3_CH5	G4
G12	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3	PTE11/ADC3_SE16/UART5_RTS/I2S0_TX_FS/FTM3_CH6	G3
G11	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0	PTE12/ADC3_SE17/I2S0_TX_BCLK/FTM3_CH7	M4
G10	PTB4/ADC1_SE10/FTM1_FLT0	PTD24/ADC0_SE17/EXTAL1/CAN1_TX/UART4_TX/I2S1_TX_FS/EWM_OUT/I2S1_RXD1	K5
G9	PTB5/ADC1_SE11/FTM2_FLT0	PTD25/ADC0_SE18/XTAL1/CAN1_RX/UART4_RX/I2S1_TX_BCLK/EWM_IN/I2S1_TXD1	K4
F12	PTB6/ADC1_SE12/FB_AD23	PTD26/ADC3_SE5B/UART4_CTS/I2S1_TXD0/RTC_CLKOUT/USB_CLKIN	J4
F11	PTB7/ADC1_SE13/FB_AD22	PTE27/ADC3_SE4B/UART4_RTS/I2S1_MCLK	H4
F10	PTB8/UART3_RTS/FB_AD21	PTE28/ADC3_SE7A	
F9	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20		
E12	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/I2S1_TX_BCLK/FB_AD19/FTM0_FLT1	PGA0_DM/ADC0_DM0/ADC1_DM3	L2
E11	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/I2S1_TX_FS/FB_AD18/FTM0_FLT2	PGA0_DP/ADC0_DP0/ADC1_DP3	L1
E10	PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/I2S1_TXD0/FB_AD17/EWM_IN		
E9	PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/I2S1_TXD1/FB_AD16/EWM_OUT	PGA1_DM/ADC1_DM0/ADC0_DM3	M2
D12	PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	PGA1_DP/ADC1_DP0/ADC0_DP3	M1
D11	PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB		
D10	PTB20/ADC2_SE4A/SPI2_PCS0/FB_AD31/NFC_DATA15/CMP0_OUT		
D9	PTB21/ADC2_SE5A/SPI2_SCK/FB_AD30/NFC_DATA14/CMP1_OUT	USB0_DM	H2
C12	PTB22/SPI2_SOUT/FB_AD29/NFC_DATA13/CMP2_OUT	USB0_DP	H1
C11	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28/NFC_DATA12/CMP3_OUT		
		PGA2_DM/ADC2_DM0/ADC3_DM3/ADC0_DM1	J2
		PGA2_DP/ADC2_DP0/ADC3_DP3/ADC0_DP1	J1
B12	PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/NFC_DATA11/I2S0_TXD1		
B11	PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/NFC_DATA10/I2S0_TXD0		
A12	PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/NFC_DATA9/I2S0_TX_FS	PGA3_DM/ADC3_DM0/ADC2_DM3/ADC1_DM1	K2
A11	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK	PGA3_DP/ADC3_DP0/ADC2_DP3/ADC1_DP1	K1
A9	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/NFC_DATA8/CMP1_OUT/I2S1_TX_BCLK		
D8	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/NFC_DATA7/CMP0_OUT/I2S1_TX_FS		
C8	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/NFC_DATA6/I2S0_MCLK	ADC0_SE16/CMP1_IN2/ADC0_SE21	K3
B8	PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8/NFC_DATA5	ADC1_SE16/CMP2_IN2/ADC0_SE22	
A8	PTC8/ADC1_SE4B/CMP0_IN2/FTM3_CH4/I2S0_MCLK/FB_AD7/NFC_DATA4		
D7	PTC9/ADC1_SE5B/CMP0_IN3/FTM3_CH5/I2S0_RX_BCLK/FB_AD6/NFC_DATA3/FTM2_FLT0		
C7	PTC10/ADC1_SE6B/I2C1_SCL/FTM3_CH6/I2S0_RX_FS/FB_AD5/NFC_DATA2/I2S1_MCLK	DAC0_OUT/CMP1_IN3/ADC0_SE23	L3
B7	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/FTM3_CH7/I2S0_RXD1/FB_RW/NFC_WE	DAC1_OUT/CMP0_IN4/CMP2_IN3/ADC1_SE23	L4
A7	PTC12/UART4_RTS/FB_AD27/FTM3_FLT0		
D6	PTC13/UART4_CTS/FB_AD26		
C6	PTC14/UART4_RX/FB_AD25	RESET	L12
B6	PTC15/UART4_TX/FB_AD24		
A6	PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16/NFC_RB	EXTAL32	M6
D5	PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24/NFC_CE0	XTAL32	M7
C5	PTC18/UART3_RTS/FB_TBST/FB_CS2/FB_BE15_8/NFC_CE1		
B5	PTC19/UART3_CTS/FB_CS3/FB_BE7_0/FB_TA	VOUT33	G1
		VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	M3
		RTC_WAKEUP	L5
G5	VREFH		
G6	VREFL	NC_A10	A10
G2	VREGIN	NC_B10	B10
		NC_C10	C10
		NC_M5	M5

F6
F7
G7
G8
H3
H7
H8
M10
VSS1
VSS2
VSS3
VSS4
VSS5
VSS6
VSS7
VSS8
H6
VSSA

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