



	PTA0/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK		
28	TSIO_CH3/PTA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI	VREFH	18
29	TSIO_CH3/PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	VREFL	19
30	TSIO_CH4/PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO		
31	TSIO_CH5/PTA4/FTM0_CH1/NMI/EZP_CS		25
32	PTA5/FTM0_CH2/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	VBAT	
33	CMP2_IN0/PTA12/CAN0_TX/FTM1_CH0/I2S0_TXD/FTM1_QD_PHA		
34	CMP2_IN1/PTA13/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB	VDD1	7
35	PTA14/SPI0_PCS0/UART0_TX/I2S0_TX_BCLK	VDD2	38
36	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD		
37	PTA16/SPI0_SOUT/UART0_CTS/I2S0_RX_FS	VDDA	17
40	ADC1_SE17/PTA17/SPI0_SIN/UART0_RTS/I2S0_MCLK/I2S0_CLKIN		
41	EXTAL/PTA18/FTM0_FLT2/FTM_CLKIN0		
	XTAL/PTA19/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1		
43	LCD_P0/ADC0_SE8/ADC1_SE8/TSIO_CH0/PTB0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	VLL1	62
44	LCD_P1/ADC0_SE9/ADC1_SE9/TSIO_CH6/PTB1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	VLL2	61
45	LCD_P2/ADC0_SE12/TSIO_CH7/PTB2/I2C0_SCL/UART0_RTS/FTM0_FLT3	VLL3	60
46	LCD_P3/ADC0_SE13/TSIO_CH8/PTB3/I2C0_SDA/UART0_CTS/FTM0_FLT0		
47	LCD_P8/PTB8/UART3_RTS		
48	LCD_P9/PTB9/SPI1_PCS1/UART3_CTS		
49	LCD_P10/ADC1_SE14/PTB10/SPI1_PCS0/UART3_RX/FTM0_FLT1		
50	LCD_P11/ADC1_SE15/PTB11/SPI1_SCK/UART3_TX/FTM0_FLT2		
51	LCD_P12/TSIO_CH9/PTB16/SPI1_SOUT/UART0_RX/EWM_IN	VCAP1	64
52	LCD_P13/TSIO_CH10/PTB17/SPI1_SIN/UART0_TX/EWM_OUT	VCAP2	63
53	LCD_P14/TSIO_CH11/PTB18/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA		
54	LCD_P15/TSIO_CH12/PTB19/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB		
55	LCD_P20/ADC0_SE14/TSIO_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD		
56	LCD_P21/ADC0_SE15/TSIO_CH14/PTC1/SPI0_PCS3/UART1_RTS/FTM0_CH0		
57	LCD_P22/ADC0_SE4B/CMP1_IN0/TSIO_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1		
58	LCD_P23/CMP1_IN1/PTC3/SPI0_PCS1/UART1_RX/FTM0_CH2		
65	LCD_P24/PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT		
66	LCD_P25/PTC5/SPI0_SCK/LPT0_ALT2/CMP0_OUT		
67	LCD_P26/CMP0_IN0/PTC6/SPI0_SOUT/PDB0_EXTRG	PGA1_DM/ADC1_DM0/ADC0_DM3	16
68	LCD_P27/CMP0_IN1/PTC7/SPI0_SIN	PGA1_DP/ADC1_DP0/ADC0_DP3	15
69	LCD_P28/ADC1_SE4B/CMP0_IN2/PTC8/I2S0_MCLK/I2S0_CLKIN		
70	LCD_P29/ADC1_SE5B/CMP0_IN3/PTC9/I2S0_RX_BCLK/FTM2_FLT0	PGA0_DM/ADC0_DM0/ADC1_DM3	14
71	LCD_P30/ADC1_SE6B/CMP0_IN4/PTC10/I2C1_SCL/I2S0_RX_FS	PGA0_DP/ADC0_DP0/ADC1_DP3	13
72	LCD_P31/ADC1_SE7B/PTC11/I2C1_SDA/I2S0_RXD		
73	LCD_P40/PTD0/SPI0_PCS0/UART2_RTS		22
74	LCD_P41/ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS	DAC0_OUT/CMP1_IN3/ADC0_SE23	21
75	LCD_P42/PTD2/SPI0_SOUT/UART2_RX	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	
76	LCD_P43/PTD3/SPI0_SIN/UART2_TX		
77	LCD_P44/PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN	EXTAL32	24
78	LCD_P45/ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/FTM0_CH5/EWM_OUT	XTAL32	23
79	LCD_P46/ADC0_SE7B/PTD6/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0		
80	LCD_P47/PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1		
1	ADC1_SE4A/PTE0/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA	RESET	42
2	ADC1_SE5A/PTE1/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL		
3	ADC1_SE6A/PTE2/SPI1_SCK/UART1_CTS/SDHC0_DCLK		
4	ADC1_SE7A/PTE3/SPI1_SIN/UART1_RTS/SDHC0_CMD		
5	PTE4/SPI1_PCS0/UART3_TX/SDHC0_D3	VSSA	20
6	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2		
9	ADC0_SE4A/PTE16/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3	VSS1	8
10	ADC0_SE5A/PTE17/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPT0_ALT3	VSS2	39
11	ADC0_SE6A/PTE18/SPI0_SOUT/UART2_CTS/I2C0_SDA	VSS3	59
12	ADC0_SE7A/PTE19/SPI0_SIN/UART2_RTS/I2C0_SCL		

<Value>