



	TA0/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK		
	TA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI		
	TA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO		
I10	TSI0_CH4/PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO		
J8	TSI0_CH5/PTA4/FTM0_CH1/NMI/EZP_CS		
K7	PTA5/FTM0_CH2/RMII0_RXER/MII0_RXER/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	VDD1	E7
J9	PTA10/FTM2_CH0/MII0_RXD2/FTM2_QD_PHA/TRACE_D0	VDD2	E5
J4	PTA11/FTM2_CH1/MII0_RXCLK/FTM2_QD_PHB	VDD3	L10
K8	CMP2_IN0/PTA12/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD/FTM1_QD_PHA	VDD4	E6
L8	CMP2_IN1/PTA13/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB		
K9	PTA14/SPI0_PCS0/UART0_TX/RMII0 CRS_DV/MII0_RXDV/I2S0_TX_BCLK		
L9	PTA15/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD		
J10	PTA16/SPI0_SOUT/UART0_CTS/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS	VDDA	F5
H10	ADC1_SE17/PTA17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK/I2S0_CLKIN		
L11	EXTAL/PTA18/FTM0_FLT2/FTM_CLKIN0	VBAT	K6
K11	XTAL/PTA19/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1		
H11	PTA29/MII0_COL/FB_A24	VREFH	G5
G11		VREFL	G6
G10	ADC0_SE8/ADC1_SE8/TSI0_CH0/PTB0/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA		
G9	ADC0_SE9/ADC1_SE9/TSI0_CH6/PTB1/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB		
G8	ADC0_SE12/TSI0_CH7/PTB2/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3		
F11	ADC0_SE13/TSI0_CH8/PTB3/I2C0_SDA/UART0_CTS/ENET0_1588_TMR1/FTM0_FLT0		
E11	ADC1_SE12/PTB6/FB_AD23		
D11	ADC1_SE13/PTB7/FB_AD22		
E10	PTB8/UART3_RTS/FB_AD21		
D10	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20		
C10	ADC1_SE14/PTB10/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1	ADC0_DM1	H2
B10	ADC1_SE15/PTB11/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2	ADC0_DP1	H1
E9	TSI0_CH9/PTB16/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN		
D9	TSI0_CH10/PTB17/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT	ADC1_DM1	J2
C9	TSI0_CH11/PTB18/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	ADC1_DP1	J1
F10	TSI0_CH12/PTB19/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB		
F9	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT	PGA1_DM/ADC1_DM0/ADC0_DM3	L2
F8	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT	PGA1_DP/ADC1_DP0/ADC0_DP3	L1
E8	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT	PGA0_DM/ADC0_DM0/ADC1_DM3	K2
B9	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28	PGA0_DP/ADC0_DP0/ADC1_DP3	K1
D8	ADC0_SE14/TSI0_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14		
C8	ADC0_SE15/TSI0_CH14/PTC1/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13		
B8	ADC0_SE4B/CMP1_IN0/TSI0_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12		
A8	CMP1_IN1/PTC3/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT	USB0_DM	F2
D7	PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	USB0_DP	F1
C7	PTC5/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT		
B7	CMP0_IN0/PTC6/SPI0_SOUT/PDB0_EXTRG/FB_AD9		
A7	CMP0_IN1/PTC7/SPI0_SIN/FB_AD8	ADC0_SE16/CMP1_IN2/ADC0_SE21	H3
D6	ADC1_SE4B/CMP0_IN2/PTC8/I2S0_MCLK/I2S0_CLKIN/FB_AD7	ADC1_SE16/CMP2_IN2/ADC0_SE22	J3
C6	ADC1_SE5B/CMP0_IN3/PTC9/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0		
C5	ADC1_SE6B/CMP0_IN4/PTC10/I2C1_SCL/I2S0_RX_FS/FB_AD5	DAC0_OUT/CMP1_IN3/ADC0_SE23	K5
B6	ADC1_SE7B/PTC11/I2C1_SDA/I2S0_RXD/FB_RW	DAC1_OUT/CMP2_IN3/ADC1_SE23	K4
A6	PTC12/UART4_RTS/FB_AD27		
A5	PTC13/UART4_CTS/FB_AD26	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	L3
B5	PTC14/UART4_RX/FB_AD25		
D5	PTC15/UART4_TX/FB_AD24		
C4	PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSIZ1/FB_BE23_16_BLS15_8		
B4	PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSIZ0/FB_BE31_24_BLS7_0	EXTAL32	L5
A4	PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8_BLS23_16	XTAL32	L4
	PTC19/UART3_CTS/ENET0_1588_TMR3/FB_CS3/FB_BE7_0_BLS31_24/FB_TA		
D4	PTD0/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS		
D3	ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS/FB_CS0		
C3	PTD2/SPI0_SOUT/UART2_RX/FB_AD4	RESET	J11
B3	PTD3/SPI0_SIN/UART2_TX/FB_AD3		
A3	PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN		
A2	ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT	VREGIN	G2
B2	ADC0_SE7B/PTD6/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0		
A1	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	VOUT33	G1
A10	PTD8/I2C0_SCL/UART5_RX/FB_A16		
A9	PTD9/I2C0_SDA/UART5_TX/FB_A17		
B1	PTD10/UART5_RTS/FB_A18		
C2	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19		
C1	PTD12/SPI2_SCK/SDHC0_D4/FB_A20	VSSA	F6
D2	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21		
D1	PTD14/SPI2_SIN/SDHC0_D6/FB_A22	VSS1	F7
E1	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	VSS2	G3
		VSS3	K10
E4	ADC1_SE4A/PTE0/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA	VSS4	G7
E3	ADC1_SE5A/PTE1/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL	VSS5	L6
E2	ADC1_SE6A/PTE2/SPI1_SCK/UART1_CTS/SDHC0_DCLK		
F4	ADC1_SE7A/PTE3/SPI1_SIN/UART1_RTS/SDHC0_CMD		
H7	PTE4/SPI1_PCS0/UART3_TX/SDHC0_D3	NC_A11	A11
G4	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	NC_B11	B11
F3	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/I2S0_CLKIN	NC_C11	C11
H5	ADC0_SE17/PTE24/CAN1_TX/UART4_TX/EWM_OUT	NC_K3	K3
J5	ADC0_SE18/PTE25/CAN1_RX/UART4_RX/EWM_IN	NC_H4	H4
H6	PTE26/UART4_CTS/ENET_1588_CLKIN/RTC_CLKOUT/USB_CLKIN		
		RESERVED	L7