



E5
E6
E7
E8
E9
F5
F6
F7
F8
F9
H5

U7

J5	PTA0/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS	A5
J6	PTA1/TSI0_CH2/UART0_RX/FTM0_CH6/JTAG_TD/EZP_DI	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0	D4
K6	PTA2/TSI0_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4	C4
K7	PTA3/TSI0_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO	PTD3/SPI0_SIN/UART2_TX/FB_AD3	B4
L7	PTA4/LLWU_P3/TSI0_CH5/FTM0_CH1/NMI/EZP_CS	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	A4
M8	PTA5/USB_CLKIN/FTM0_CH2/RMII0_RXER/MII0_RXER/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/EWM_OUT	A3
J7	PTA6/FTM0_CH3/TRACE_CLKOUT	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	A2
J8	PTA7/ADC0_SE10/FTM0_CH4/TRACE_D3	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	A1
K8	PTA8/ADC0_SE11/FTM1_CH0/FTM1_QD_PHA/TRACE_D2	PTD8/I2C0_SCL/UART5_RX/FB_A16	C9
L8	PTA9/FTM1_CH1/MII0_RXD3/FTM1_QD_PHB/TRACE_D1	PTD9/I2C0_SDA/UART5_TX/FB_A17	B9
M9	PTA10/FTM2_CH0/MII0_RXD2/FTM2_QD_PHA/TRACE_D0	PTD10/UART5_RTS/FB_A18	B3
L9	PTA11/FTM2_CH1/MII0_RXCLK/FTM2_QD_PHB	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	B2
K9	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD0/FTM1_QD_PHA	PTD12/SPI2_SCK/SDHC0_D4/FB_A20	B1
J9	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	C3
L10	PTA14/SPI0_PCS0/UART0_TX/RMII0_CRS_DV/MII0_RXDV/I2S0_RX_BCLK/I2S0_TXD1	PTD14/SPI2_SIN/SDHC0_D6/FB_A22	C2
K10	PTA15/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD0	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	C1
K11	PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS/I2S0_RXD1	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA/RTC_CLKOUT	D3
M12	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL/SPI1_SIN	D2
M11	PTA18/EXTALO/FTM0_FLT2/FTM_CLKIN0	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK	D1
K12	PTA19/XTALO/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD/SPI1_SOUT	E4
J12	PTA24/MII0_TXD2/FB_A29	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3	E3
J11	PTA25/MII0_TXCLK/FB_A28	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	E2
J10	PTA26/MII0_TXD3/FB_A27	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/USB_SOF_OUT	E1
H12	PTA27/MII0_CRS/FB_A26	PTE7/UART3_RTS/I2S0_RXD0	F4
H11	PTA28/MII0_TXER/FB_A25	PTE8/I2S0_RXD1/UART5_TX/I2S0_RX_FS	F3
	PTA29/MII0_COL/FB_A24	PTE9/I2S0_TXD1/UART5_RX/I2S0_RX_BCLK	F2
H10	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA	PTE10/UART5_CTS/I2S0_TX_FS	F1
H9	PTB1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB	PTE11/UART5_RTS/I2S0_TX_FS	G4
G12	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3	PTE12/I2S0_TX_BCLK	G3
G11	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/ENET0_1588_TMR1/FTM0_FLT0	PTE24/ADC0_SE17/CAN1_TX/UART4_TX/EWM_OUT	M4
G10	PTB4/ADC1_SE10/ENET0_1588_TMR2/FTM1_FLT0	PTE25/ADC0_SE18/CAN1_RX/UART4_RX/EWM_IN	K5
G9	PTB5/ADC1_SE11/ENET0_1588_TMR3/FTM2_FLT0	PTE26/ENET_1588_CLKIN/UART4_CTS/RTC_CLKOUT/USB_CLKIN	K4
F12	PTB6/ADC1_SE12/FB_AD23	PTE27/UART4_RTS	J4
F11	PTB7/ADC1_SE13/FB_AD22	PTE28	H4
F10	PTB8/UART3_RTS/FB_AD21		
F9	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20	USB0_DM	H2
E12	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1	USB0_DP	H1
E11	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2		
E10	PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN	DAC0_OUT/CMP1_IN3/ADC0_SE23	L3
E9	PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT	DAC1_OUT/CMP0_IN4/CMP2_IN3/ADC1_SE23	L4
D12	PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	ADC0_SE16/CMP1_IN2/ADC0_SE21	J3
D11	PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	ADC1_SE16/CMP2_IN2/ADC0_SE22	K3
D10	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT	PGA0_DM/ADC0_DM0/ADC1_DM3	L2
D9	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT	PGA0_DP/ADC0_DP0/ADC1_DP3	L1
C12	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT	PGA1_DM/ADC1_DM0/ADC0_DM3	M2
C11	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28	PGA1_DP/ADC1_DP0/ADC0_DP3	M1
B12	PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD1		
B11	PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/I2S0_TXD0	ADC0_DM1	J2
A12	PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/I2S0_TX_FS	ADC0_DP1	J1
A11	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK		
A9	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	ADC1_DM1	K2
D8	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/CMP0_OUT	ADC1_DP1	K1
C8	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/I2S0_MCLK		
B8	PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	M3
A8	PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/FB_AD7	VOUT33	G1
D7	PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0	VREGIN	G2
C7	PTC10/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS/FB_AD5		
B7	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD1/FB_RW	VREFH	G5
A7	PTC12/UART4_RTS/FB_AD27	VREFL	G6
D6	PTC13/UART4_CTS/FB_AD26		
C6	PTC14/UART4_RX/FB_AD25	VBAT	L6
B6	PTC15/UART4_TX/FB_AD24		
A6	PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSIZ1/FB_BE23_16	EXTAL32	M6
D5	PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSIZ0/FB_BE31_24	XTAL32	M7
C5	PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8		
B5	PTC19/UART3_CTS/ENET0_1588_TMR3/FB_CS3/FB_BE7_0/FB_TA	RESET	L12

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F7
H3
G3
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H7
H8
M10
VSS1
VSS2
VSS3
VSS4
VSS5
VSS6
VSS7
VSS8
VSSA
M5
NC_M5
A10
B10
C10
NC_A10
NC_B10
NC_C10
L5
RTC_WAKEUP

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