



UP

N6 M6	TAMPER5	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FTM3_CH0/FB_ALE/FB_CS1/FB_TS/I2S1_RXD1	L8
	TAMPER6	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FTM3_CH1/FB_CS0/I2S1_RXD0	F8
	TAMPER7	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FTM3_CH2/FB_AD4/I2S1_RX_FS	K6
M2 M1	USB0_DM	PTD3/SPI0_SIN/UART2_TX/FTM3_CH3/FB_AD3/I2S1_RX_BCLK	J6
	USB0_DP	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/NFC_DATA1/EWM_IN	K5
		PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/NFC_DATA0/EWM_OUT	J5
R16 T6 T5	RESET	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	K4
	XTAL32	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	E7
	EXTAL32	PTD8/I2C0_SCL/UART5_RX/FB_A16/NFC_CLE	J4
N3 P3	ADC0_SE16/CMP1_IN2/ADC0_SE21	PTD9/I2C0_SDA/UART5_TX/FB_A17/NFC_ALE	F7
	ADC1_SE16/CMP2_IN2/ADC0_SE22	PTD10/UART5_RTS/FB_A18/NFC_RE	E6
		PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	G5
R3 R4	DAC0_OUT/CMP1_IN3/ADC0_SE23	PTD12/SPI2_SCK/FTM3_FLT0/SDHC0_D4/FB_A20	F5
	DAC1_OUT/CMP0_IN4/CMP2_IN3/ADC1_SE23	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	F4
		PTD14/SPI2_SIN/SDHC0_D6/FB_A22	E5
R2 R1 T2 T1	PGA0_DM/ADC0_DM0/ADC1_DM3	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	E4
	PGA1_DM/ADC1_DM0/ADC0_DM3		
	PGA1_DP/ADC1_DP0/ADC0_DP3		
N2 N1 P2 P1	PGA2_DM/ADC2_DM0/ADC3_DM3/ADC0_DM1		
	PGA2_DP/ADC2_DP0/ADC3_DP3/ADC0_DP1		
	PGA3_DM/ADC3_DM0/ADC2_DM3/ADC1_DM1		
T7 N8 T8	PGA3_DP/ADC3_DP0/ADC2_DP3/ADC1_DP1		
P8 P8 T12 R12	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5		
	PTA1/JTAG_TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6		
	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/UART0_TX/FTM0_CH7/TSIO_CH3		
N12 P12 N12 T13	PTA3/TSIO_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO		
	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1		
	PTA5/USB_CLKIN/FTM0_CH2/RMII0_RXER/MII0_RXER/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST		
M10 N10 R11 P11	PTA6/ADC3_SE6A/ULPI_CLK/FTM0_CH3/I2S1_RXD0/TRACE_CLKOUT		
	PTA7/ADC0_SE10/ULPI_DIR/FTM0_CH4/I2S1_RX_BCLK/TRACE_D3		
	PTA8/ADC0_SE11/ULPI_NXT/FTM1_CH0/I2S1_RX_FS/FTM1_QD_PHA/TRACE_D2		
N11 T11 T15 T16	PTA9/ADC3_SE5A/ULPI_STP/FTM1_CH1/MII0_RXD3/FTM1_QD_PHB/TRACE_D1		
	PTA10/ADC3_SE4A/ULPI_DATA0/FTM2_CH0/MII0_RXD2/FTM2_QD_PHA/TRACE_D0		
	PTA11/ADC3_SE15/ULPI_DATA1/FTM2_CH1/MII0_RXCLK/FTM2_QD_PHB		
N13 R14 M13 R15 P14 N14	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD0/FTM1_QD_PHA		
	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB		
	PTA14/CMP3_IN0/SPI0_PCS0/UART0_TX/RMII0_CRS_DV/MII0_RXDV/I2S0_RX_BCLK/I2S0_TXD1		
M12 M11 P15 M14 N15 M15 L16 K14 K15 J13 J14 J15 H13 H14 H15 G13 G14 G15 H16	PTA15/CMP3_IN1/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD0		
	PTA16/CMP3_IN2/SPI0_SOUT/UART0_CTS/UART0_COL/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS/I2S0_RXD1		
	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTA18/EXTAL0/FTM1_FLT0/FTM_CLKIN0		
	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1		
	PTA24/CMP3_IN4/ULPI_DATA2/MII0_TXD2/FB_A29		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTA25/CMP3_IN5/ULPI_DATA3/MII0_TXCLK/FB_A28		
	PTA26/ADC2_SE15/ULPI_DATA4/MII0_TXD3/FB_A27		
	PTA27/ADC2_SE14/ULPI_DATA5/MII0_CRS/FB_A26		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTA28/ADC2_SE13/ULPI_DATA6/MII0_TXER/FB_A25		
	PTA29/ADC2_SE12/ULPI_DATA7/MII0_COL/FB_A24		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/ADC2_SE8/ADC3_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA		
	PTB1/ADC0_SE9/ADC1_SE9/ADC2_SE9/ADC3_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB		
	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/UART0_COL/ENET0_1588_TMR1/FTM0_FLT0		
	PTB4/ADC1_SE10/ENET0_1588_TMR2/FTM1_FLT0		
	PTB5/ADC1_SE11/ENET0_1588_TMR3/FTM2_FLT0		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTB6/ADC1_SE12/FB_AD23		
	PTB7/ADC1_SE13/FB_AD22		
	PTB8/UART3_RTS/FB_AD21		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20		
	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/I2S1_TX_BCLK/FB_AD19/FTM0_FLT1		
	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/I2S1_TX_FS/FB_AD18/FTM0_FLT2		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTB16/TSIO_CH9/SPI1_SOUT/UART0_RX/I2S1_TXD0/FB_AD17/EWM_IN		
	PTB17/TSIO_CH10/SPI1_SIN/UART0_TX/I2S1_TXD1/FB_AD16/EWM_OUT		
	PTB18/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTB19/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB		
	PTB20/ADC2_SE4A/SPI2_PCS0/FB_AD31/NFC_DATA15/CMP0_OUT		
	PTB21/ADC2_SE5A/SPI2_SCK/FB_AD30/NFC_DATA14/CMP1_OUT		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTB22/SPI2_SOUT/FB_AD29/NFC_DATA13/CMP2_OUT		
	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28/NFC_DATA12/CMP3_OUT		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/NFC_DATA11/I2S0_TXD1		
	PTC1/LLWU_P6/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/NFC_DATA10/I2S0_TXD0		
	PTC2/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/NFC_DATA9/I2S0_TX_FS		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK		
	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/NFC_DATA8/CMP1_OUT/I2S1_TX_BCLK		
	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/NFC_DATA7/CMP0_OUT/I2S1_TX_FS		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/NFC_DATA6/I2S0_MCLK		
	PTC7/CMP0_IN1/SPI0_SOF/USB_SOF_OUT/I2S0_RX_FS/FB_AD8/NFC_DATA5		
	PTC8/ADC1_SE4B/CMP0_IN2/FTM3_CH4/I2S0_MCLK/FB_AD7/NFC_DATA4		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTC9/ADC1_SE5B/CMP0_IN3/FTM3_CH5/I2S0_RX_BCLK/FB_AD6/NFC_DATA3/FTM2_FLT0		
	PTC10/ADC1_SE6B/I2C1_SCL/FTM3_CH6/I2S0_RX_FS/FB_AD5/NFC_DATA2/I2S1_MCLK		
	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/FTM3_CH7/I2S0_RXD1/FB_RW/NFC_WE		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTC12/UART4_RTS/FB_AD27/FTM3_FLT0		
	PTC13/UART4_CTS/FB_AD26		
	PTC14/UART4_RX/FB_AD25		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTC15/UART4_TX/FB_AD24		
	PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSIZ1/FB_BE23_T6/NFC_RB		
	PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSIZ0/FB_BE31_T4/NFC_CE0		
G16 F13 F14 E14 E15 F12 G12 H12 F11 G11 H11 J12 K13 J11 F10 F9 E9 M9 M8	PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8/NFC_CE1		
	PTC19/UART3_CTS/ENET0_1588_TMR3/FB_CS3/FB_BE7_0/FB_TA		

<Value>

		DDR_DQ0	C5
		DDR_DQ1	B4
		DDR_DQ2	A4
C12	DDR_A2	DDR_DQ3	B6
C10	DDR_A3	DDR_DQ4	D6
A13	DDR_A4	DDR_DQ5	A6
A14	DDR_A5	DDR_DQ6	B7
D11	DDR_A6	DDR_DQ7	A8
A15	DDR_A7	DDR_DQ8	D1
B15	DDR_A8	DDR_DQ9	C1
B13	DDR_A9	DDR_DQ10	C2
B14	DDR_A10	DDR_DQ11	B2
C15	DDR_A11	DDR_DQ12	C3
D16	DDR_A12	DDR_DQ13	C4
D15	DDR_A13	DDR_DQ14	B3
		DDR_DQ15	A2
A9	DDR_BA0	DDR_DQS0	C8
B10	DDR_BA1		D3
B9	DDR_BA2	DDR_DQS1	D9
A11	DDR_CK	DDR_DM0	
A10	DDR_CK	DDR_DM1	A3
B16	DDR_CKE	DDR_ODT	A7
D14	DDR_CS		
C13	DDR_CAS	VSS1	H8
E16	DDR_RAS		H9
D13	DDR_WE	VSS2	L2
		VSS3	J8
G7	VDD1	VSS4	J9
G8	VDD2	VSS5	T14
G9	VDD3	VSS6	K8
G10	VDD4	VSS7	K9
J7	VDD5	VSS8	L9
J10	VDD6	VSS9	N7
K10	VDD7	VSS10	T4
L10	VDD8	VSS11	N4
N5	VDDA	VSSA	
B1	DDR_VDD1	DDR_VSS1	A1
B5	DDR_VDD2	DDR_VSS2	A5
B8	DDR_VDD3	DDR_VSS3	D5
B12	DDR_VDD4	DDR_VSS4	A12
C16	DDR_VDD5	DDR_VSS5	E8
D2	DDR_VDD6	DDR_VSS6	A16
D4	DDR_VDD7	DDR_VSS7	C7
D7	DDR_VDD8	DDR_VSS8	E11
E10	DDR_VDD9	DDR_VSS9	C9
E12	DDR_VDD10	DDR_VSS10	C14
		DDR_VSS11	D8
P5	VBAT	DDR_VSS12	D12
H7	VDDINT1	DDR_VSS13	E3
H10	VDDINT2	DDR_VREF	C6
L1	VOUT33		M4
		VREFL	P4
		VREFH	L3
		VREGIN	T3
		VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	

<Value>