

		16	1	22	9	10			
		VBAT	VDD1	VDD2	VDDA	VREFH			
17	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5								
18	PTA1/JTAG_TDI/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6							PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG	33
19	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7							PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0	34
20	PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0							PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS	35
21	PTA4/LLWU_P3/NMI/EZP_CS/TSI0_CH5/FTM0_CH1							PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK	36
24	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0							PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT	37
25	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1							PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT	38
							PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK	39	
							PTC7/CMP0_IN1/SPI0_SIN/I2S0_RX_FS	40	
27	PTB0/LLWU_P5/ADC0_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA								
28	PTB1/ADC0_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB							PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS	41
29	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3							PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS	42
30	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0							PTD2/LLWU_P13/SPI0_SOUT/UART2_RX	43
31	PTB16/TSI0_CH9/UART0_RX/EWM_IN							PTD3/SPI0_SIN/UART2_TX	44
32	PTB17/TSI0_CH10/UART0_TX/EWM_OUT							PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN	45
							PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT	46	
							PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0	47	
							PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	48	
7	ADC0_DP0								
8	ADC0_DM0								
26	RESET							PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3	3
							PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3	4	
							PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA	5	
							PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL	6	
13	VREF_OUT/CMP1_IN5/CMP0_IN5								
							EXTAL32	15	
							XTAL32	14	
		VSS1	VSS2	VSSA	VREFL	EX_PAD			
		2	23	12	11	49	<Value>		