



U?		21	3	30	48	13	14	
		VBAT	VDD1	VDD2	VDD3	VDDA	VREFH	
17	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5						22
		PTA1/JTAG_TDI/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6						23
		PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7						24
		PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0						25
		PTA4/LLWU_P3/NMI/EZP_CS/TSI0_CH5/FTM0_CH1						26
		PTA5/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST						27
		PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA						28
		PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB						29
		PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0						32
		PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1						33
9	PGA0_DP/ADC0_DP0/ADC1_DP3	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA						35
10	PGA0_DM/ADC0_DM0/ADC1_DM3	PTB1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB						36
11	PGA1_DP/ADC1_DP0/ADC0_DP3	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3						37
		PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0						38
		PTB16/TSI0_CH9/UART0_RX/FB_AD17/EWM_IN						39
		PTB17/TSI0_CH10/UART0_TX/FB_AD16/EWM_OUT						40
		PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA						41
		PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB						42
		PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD1						43
		PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/I2S0_TXD0						44
		PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/I2S0_TX_FS						45
		PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK						46
34	RESET	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT						49
		PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/CMP0_OUT						50
		PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/I2S0_MCLK						51
		PTC7/CMP0_IN1/SPI0_SIN/I2S0_RX_FS/FB_AD8						52
		PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/FB_AD7						53
		PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0						54
		PTC10/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS/FB_AD5						55
		PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD1/FB_RW						56
		PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS						57
		PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0						58
18	DAC0_OUT/CMP1_IN3/ADC0_SE23	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4						59
		PTD3/SPI0_SIN/UART2_TX/FB_AD3						60
		PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN						61
		PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/EWM_OUT						62
		PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0						63
		PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1						64
		PTE0/ADC1_SE4A/UART1_TX/I2C1_SDA/RTC_CLKOUT						1
		PTE1/LLWU_P0/ADC1_SE5A/UART1_RX/I2C1_SCL						2
		PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3						5
		PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3						6
20	EXTAL32	PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA						7
		PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL						8
19	XTAL32							