



		ADC0_DM0/ADC1_DM3	VBAT	25
		ADC0_DP0/ADC1_DP3		7
16			VDD1	38
15	PGA1_DM/ADC1_DM0/ADC0_DM3		VDD2	50
	PGA1_DP/ADC1_DP0/ADC0_DP3		VDD3	60
26			VDD4	70
27	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5		VDD5	
28	PTA1/JTAG_TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6			
29	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7			
30	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0			
31	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1	VDDA	17	
32	PTA5/USB_CLKIN/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST	VREFH	18	
33	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA			
34	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB			
35	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1	VREGIN	12	
36	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0	VOUT33	11	
37	PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1			
40	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK			
41	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0	USB0_DM	10	
	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	USB0_DP	9	
43				
44	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA			
45	PTB1/ADC0_SE9/ADC1_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB			
46	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3	DAC0_OUT/CMP1_IN3/ADC0_SE23	22	
47	PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0			
48	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1			
51	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	21	
52	PTB16/TSIO_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN			
53	PTB17/TSIO_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT			
54	PTB18/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA			
	PTB19/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB			
55		RESET	42	
56	PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD1			
57	PTC1/LLWU_P6/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/I2S0_TXD0			
58	PTC2/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/I2S0_TX_FS			
61	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK			
62	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT			
63	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/CMP0_OUT			
64	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/I2S0_MCLK	XTAL32	23	
65	PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8	EXTAL32	24	
66	PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/FB_AD7			
67	PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0			
68	PTC10/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS/FB_AD5			
71	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD1/FB_RW			
72	PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16			
	PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24			
73				
74	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS			
75	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0			
76	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4			
77	PTD3/SPI0_SIN/UART2_TX/FB_AD3			
78	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	VSS1	8	
79	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/EWM_OUT	VSS2	39	
	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	VSS3	49	
	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	VSS4	59	
		VSS5	69	
1				
2	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA/RTC_CLKOUT			
3	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL/SPI1_SIN			
4	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK	VSSA	20	
5	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD/SPI1_SOUT	VREFL	19	
6	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3			
	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2			

<Value>