



L6
E5
E6
E7
E8
E9
F5
F6
H5
G5

VBAT
VDD1
VDD2
VDD3
VDD4
VDD5
VDD6
VDDA
VREFH

J5	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5	PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD1	B12
J6	PTA1/JTAG_TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6	PTC1/LLWU_P6/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/I2S0_TXD0	B11
K6	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7	PTC2/ADC0_SE4b/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/I2S0_TX_FS	A12
K7	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK	A11
L7	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	A9
M8	PTA5/USB_CLKIN/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/CMP0_OUT	D8
J7	PTA6/FTM0_CH3/TRACE_CLKOUT	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/I2S0_MCLK	C8
J8	PTA7/ADC0_SE10/FTM0_CH4/TRACE_D3	PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8	B8
K8	PTA8/ADC0_SE11/FTM1_CH0/FTM1_QD_PHA/TRACE_D2	PTC8/ADC1_SE4b/CMP0_IN2/I2S0_MCLK/FB_AD7	A8
L8	PTA9/FTM1_CH1/FTM1_QD_PHB/TRACE_D1	PTC9/ADC1_SE5b/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0	D7
M9	PTA10/FTM2_CH0/FTM2_QD_PHA/TRACE_D0	PTC10/ADC1_SE6b/I2C1_SCL/I2S0_RX_FS/FB_AD5	C7
L9	PTA11/FTM2_CH1/FTM2_QD_PHB	PTC11/LLWU_P11/ADC1_SE7b/I2C1_SDA/I2S0_RXD1/FB_RW	B7
K9	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA	PTC12/UART4_RTS/FB_AD27	A7
J9	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB	PTC13/UART4_CTS/FB_AD26	D6
L10	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1	PTC14/UART4_RX/FB_AD25	C6
L11	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0	PTC15/UART4_TX/FB_AD24	B6
K10	PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1	PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSI21/FB_BE23_16	A6
K11	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK	PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSI20/FB_BE31_24	D5
M12	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0	PTC18/UART3_RTS/FB_TBST/FB_CS2/FB_BE15_8	C5
M11	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	PTC19/UART3_CTS/FB_CS3/FB_BE7_0/FB_TA	B5
K12	PTA24/FB_A29		
J12	PTA25/FB_A28		
J11	PTA26/FB_A27	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS	A5
J10	PTA27/FB_A26	PTD1/ADC0_SE5b/SPI0_SCK/UART2_CTS/FB_CS0	D4
H12	PTA28/FB_A25	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4	C4
H11	PTA29/FB_A24	PTD3/SPI0_SIN/UART2_TX/FB_AD3	B4
		PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	A4
H10	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	PTD5/ADC0_SE6b/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/EWM_OUT	A3
H9	PTB1/ADC0_SE9/ADC1_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	PTD6/LLWU_P15/ADC0_SE7b/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	A2
G12	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	A1
G11	PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0	PTD8/I2C0_SCL/UART5_RX/FB_A16	C9
G10	PTB4/ADC1_SE10/FTM1_FLT0	PTD9/I2C0_SDA/UART5_TX/FB_A17	B9
G9	PTB5/ADC1_SE11/FTM2_FLT0	PTD10/UART5_RTS/FB_A18	B3
F12	PTB6/ADC1_SE12/FB_AD23	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	B2
F11	PTB7/ADC1_SE13/FB_AD22	PTD12/SPI2_SCK/SDHC0_D4/FB_A20	B1
F10	PTB8/UART3_RTS/FB_AD21	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	C3
F9	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20	PTD14/SPI2_SIN/SDHC0_D6/FB_A22	C2
E12	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	C1
E11	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2		
E10	PTB16/TSIO_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN		
E9	PTB17/TSIO_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT	PTE0/ADC1_SE4a/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA/RTC_CLKOUT	D3
D12	PTB18/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	PTE1/LLWU_P0/ADC1_SE5a/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL/SPI1_SIN	D2
D11	PTB19/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	PTE2/LLWU_P1/ADC1_SE6a/SPI1_SCK/UART1_CTS/SDHC0_DCLK	D1
D10	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT	PTE3/ADC1_SE7a/SPI1_SIN/UART1_RTS/SDHC0_CMD/SPI1_SOUT	E4
D9	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3	E3
C12	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	E2
C11	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/USB_SOF_OUT	E1
		PTE7/UART3_RTS/I2S0_RXD0	F4
L5	RTC_WAKEUP	PTE8/I2S0_RXD1/UART5_TX/I2S0_RX_FS	F3
L2	PGA0_DM/ADC0_DM0/ADC1_DM3	PTE9/I2S0_TXD1/UART5_RX/I2S0_RX_BCLK	F2
L1	PGA0_DP/ADC0_DP0/ADC1_DP3	PTE10/UART5_CTS/I2S0_TXD0	F1
M2	PGA1_DM/ADC1_DM0/ADC0_DM3	PTE11/UART5_RTS/I2S0_TX_FS	G4
M1	PGA1_DP/ADC1_DP0/ADC0_DP3	PTE12/I2S0_TX_BCLK	G3
		PTE24/ADC0_SE17/CAN1_TX/UART4_TX/EWM_OUT	M4
J2	ADC0_DM1	PTE25/ADC0_SE18/CAN1_RX/UART4_RX/EWM_IN	K5
J1	ADC0_DP1	PTE26/UART4_CTS/RTC_CLKOUT/USB_CLKIN	K4
		PTE27/UART4_RTS	J4
K2	ADC1_DM1	PTE28	H4
K1	ADC1_DP1	USB0_DM	H2
		USB0_DP	H1
J3	ADC0_SE16/CMP1_IN2/ADC0_SE21		
K3	ADC1_SE16/CMP2_IN2/ADC0_SE22	VOUT33	G1
		VREGIN	G2
L3	DAC0_OUT/CMP1_IN3/ADC0_SE23		
L4	DAC1_OUT/CMP0_IN4/CMP2_IN3/ADC1_SE23	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	M3
L12	RESET	NC_M5	M5
		NC_C10	C10
		NC_B10	B10
		NC_A10	A10

<Value>

VSS8
H8
VSS7
H7
VSS6
G8
VSS5
G7
VSS4
H3
VSS3
F7
VSS2
F6
VSS1
H6
VSSA
G6
VREFL