

NXP		U?	3	30	48	14	15	21	13		
			VDD1	VDD2	VDD3	VREFH	VREFL	VBAT	VDDA		
22	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5										
23	PTA1/JTAG_TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6										43
24	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7									PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD1	44
25	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0									PTC1/LLWU_P6/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/I2S0_TXD0	45
26	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1									PTC2/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/I2S0_TX_FS	46
27	PTA5/USB_CLKIN/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST									PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK	49
28	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA									PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	50
29	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB									PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/CMP0_OUT	51
32	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0									PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/I2S0_MCLK	52
33	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1									PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8	53
										PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/FB_AD7	54
										PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0	55
										PTC10/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS/FB_AD5	56
										PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD1/FB_RW	
35	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA										
36	PTB1/ADC0_SE9/ADC1_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB										
37	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3										
38	PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0										
39	PTB16/TSIO_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN										
40	PTB17/TSIO_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT										
41	PTB18/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA									PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS	57
42	PTB19/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB									PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0	58
										PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4	59
										PTD3/SPI0_SIN/UART2_TX/FB_AD3	60
8	VREGIN									PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	61
7	VOUT33									PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/EWM_OUT	62
5	USB0_DP									PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	63
6	USB0_DM									PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	64
9	PGA0_DP/ADC0_DP0/ADC1_DP3										
10	PGA0_DM/ADC0_DM0/ADC1_DM3									PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/I2C1_SDA/RTC_CLKOUT	1
11	PGA1_DP/ADC1_DP0/ADC0_DP3									PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/I2C1_SCL/SPI1_SIN	2
12	PGA1_DM/ADC1_DM0/ADC0_DM3										
19	XTAL32									DAC0_OUT/CMP1_IN3/ADC0_SE23	18
20	EXTAL32									VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	17
34	RESET										
			VSS1	VSS2	VSS3				VSSA		
			4	31	47				16	<Value>	