

K10 G3 F7 G7 VSS4 L6	VSS1	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/FTM0_CH5	J6
	VSS2	PTA1/JTAG_TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6	H8
	VSS3	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7	J7
	VSS4	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0	H9
	VSS5	PTA4/NMI/EZP_CS/TSIO_CH5/FTM0_CH1	J8
K6	VBAT	PTA5/FTM0_CH2/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	K7
G2	VREGIN	PTA10/FTM2_CH0/FTM2_QD_PHA/TRACE_D0	J9
		PTA11/FTM2_CH1/FTM2_QD_PHB	J4
		PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD/FTM1_QD_PHA	K8
		PTA13/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB	L8
		PTA14/SPI0_PCS0/UART0_TX/I2S0_TX_BCLK	K9
G1	VOUT33	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD	L9
		PTA16/SPI0_SOUT/UART0_CTS/I2S0_RX_FS	J10
		PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK/I2S0_CLKIN	H10
		PTA18/XTAL/FTM0_FLT2/FTM_CLKIN0	L11
		PTA19/XTAL/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1	K11
F5	VDDA		
G5	VREFH	PTB0/ADC0_SE8/ADC1_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	G11
		PTB1/ADC0_SE9/ADC1_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	G10
		PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3	G9
		PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/FTM0_FLT0	G8
G6	VREFL		
F6	VSSA	PTB6/ADC1_SE12/FB_AD23	F11
		PTB7/ADC1_SE13/FB_AD22	E11
		PTB8/UART3_RTS/FB_AD21	D11
		PTB9/SPI1_PCS1/UART3_CTS/FB_AD20	E10
		PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1	D10
L3	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2	C10
K1 K2	PGA0_DP/ADC0_DP0/ADC1_DP3		B10
	PGA0_DM/ADC0_DM0/ADC1_DM3	PTB16/TSIO_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN	E9
		PTB17/TSIO_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT	D9
		PTB18/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	C9
		PTB19/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	F10
L1 L2	PGA1_DP/ADC1_DP0/ADC0_DP3	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT	F9
	PGA1_DM/ADC1_DM0/ADC0_DM3	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT	F8
		PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT	E8
		PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28	
H1 H2	ADC0_DP1		
	ADC0_DM1		
J1 J2	ADC1_DP1	PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14	B9
	ADC1_DM1	PTC1/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13	D8
		PTC2/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12	C8
		PTC3/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT	B8
		PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	A8
H3 J3	ADC0_SE16/CMP1_IN2/ADC0_SE21	PTC5/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT	D7
	ADC1_SE16/CMP2_IN2/ADC0_SE22	PTC6/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/FB_AD9	C7
		PTC7/CMP0_IN1/SPI0_SIN/FB_AD8	B7
		PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/I2S0_CLKIN/FB_AD7	A7
		PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0	D6
K5 K4	DAC0_OUT/CMP1_IN3/ADC0_SE23	PTC10/ADC1_SE6B/CMP0_IN4/I2C1_SCL/I2S0_RX_FS/FB_AD5	C6
	DAC1_OUT/CMP2_IN3/ADC1_SE23	PTC11/ADC1_SE7B/I2C1_SDA/I2S0_RXD/FB_RW	C5
		PTC12/UART4_RTS/FB_AD27	B6
		PTC13/UART4_CTS/FB_AD26	A6
		PTC14/UART4_RX/FB_AD25	A5
L4	XTAL32	PTC15/UART4_TX/FB_AD24	B5
		PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSI21/FB_BE23_16_BLS15_8	D5
		PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSI20/FB_BE31_24_BLS7_0	C4
		PTC18/UART3_RTS/FB_TBST1/FB_CS2/FB_BE15_8_BLS23_16	B4
		PTC19/UART3_CTS/FB_CS3/FB_BE7_0_BLS31_24/FB_TA	A4
L5	EXTAL32		
J11	RESET	PTD0/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS	D4
		PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0	D3
		PTD2/SPI0_SOUT/UART2_RX/FB_AD4	C3
		PTD3/SPI0_SIN/UART2_TX/FB_AD3	B3
		PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	A3
F1 F2	USB0_DP	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT	B2
	USB0_DM	PTD6/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	A1
		PTD7/CMT_IRQ/UART0_TX/FTM0_CH7/FTM0_FLT1	A10
		PTD8/I2C0_SCL/UART5_RX/FB_A16	A9
		PTD9/I2C0_SDA/UART5_TX/FB_A17	B1
A11 B11 C11 K3 H4	NC_A11	PTD10/UART5_RTS/FB_A18	C2
	NC_B11	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	C1
	NC_C11	PTD12/SPI2_SCK/SDHC0_D4/FB_A20	D2
	NC_K3	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	D1
	NC_H4	PTD14/SPI2_SIN/SDHC0_D6/FB_A22	E1
L7	RESERVED	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	
A11 B11 C11 K3 H4	NC_A11	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA	E4
	NC_B11	PTE1/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL	E3
	NC_C11	PTE2/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK	E2
	NC_K3	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD	F4
	NC_H4	PTE4/SPI1_PCS0/UART3_TX/SDHC0_D3	H7
L7	RESERVED	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	G4
		PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/I2S0_CLKIN	F3
L7	RESERVED	PTE24/ADC0_SE17/CAN1_TX/UART4_TX/EWM_OUT	H5
		PTE25/ADC0_SE18/CAN1_RX/UART4_RX/EWM_IN	J5
		PTE26/UART4_CTS/RTC_CLKOUT/USB_CLKIN	H6