

|    | VBAT                                                                      | VDD1<br>VDD2<br>VDD3<br>VDDA |                                                               |    |
|----|---------------------------------------------------------------------------|------------------------------|---------------------------------------------------------------|----|
| 22 |                                                                           |                              |                                                               | 7  |
| 23 | PTA0/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK      |                              | VOUT33                                                        |    |
| 24 | PTA1/TSI0_CH2/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI                           |                              |                                                               |    |
| 25 | PTA2/TSI0_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO                 |                              |                                                               | 17 |
| 26 | PTA3/TSI0_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO                         |                              | VREF_OUT/CMP1_IN5/CMP0_IN5                                    | 14 |
| 27 | PTA4/LLWU_P3/TSI0_CH5/FTM0_CH1/NMI/EZP_CS                                 |                              | VREFH                                                         | 15 |
| 28 | PTA5/USB_CLKIN/FTM0_CH2/I2S0_TX_BCLK/JTAG_TRST                            |                              | VREFL                                                         | 8  |
| 29 | PTA12/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA                                      |                              | VREGIN                                                        |    |
| 32 | PTA13/LLWU_P4/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB                             |                              |                                                               |    |
| 33 | PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0                                         |                              |                                                               |    |
|    | PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1                              |                              |                                                               | 20 |
| 35 |                                                                           |                              | EXTAL32                                                       | 18 |
| 36 | PTB0/LLWU_P5/ADC0_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA              |                              | CMP1_IN3/ADC0_SE23                                            | 11 |
| 37 | PTB1/ADC0_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB                      |                              | ADC0_DP3                                                      | 9  |
| 38 | PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3                      |                              | ADC0_DP0                                                      | 12 |
| 39 | PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0            |                              | ADC0_DM3                                                      | 10 |
| 40 | PTB16/TSI0_CH9/UART0_RX/EWM_IN                                            |                              | ADC0_DM0                                                      |    |
| 41 | PTB17/TSI0_CH10/UART0_TX/EWM_OUT                                          |                              |                                                               |    |
| 42 | PTB18/TSI0_CH11/I2S0_TX_BCLK                                              |                              |                                                               |    |
|    | PTB19/TSI0_CH12/I2S0_TX_FS                                                |                              |                                                               | 34 |
| 43 |                                                                           |                              | RESET                                                         | 6  |
| 44 | PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG                             |                              | USB0_DM                                                       | 5  |
| 45 | PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0   |                              | USB0_DP                                                       | 19 |
| 46 | PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS |                              | XTAL32                                                        |    |
| 49 | PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK            |                              |                                                               |    |
| 50 | PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT                         |                              |                                                               |    |
| 51 | PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT                      |                              |                                                               |    |
| 52 | PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK        |                              |                                                               |    |
| 53 | PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS                             |                              |                                                               | 57 |
| 54 | PTC8/CMP0_IN2/I2S0_MCLK                                                   |                              | PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS                             | 58 |
| 55 | PTC9/CMP0_IN3/I2S0_RX_BCLK                                                |                              | PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS                             | 59 |
| 56 | PTC10/I2S0_RX_FS                                                          |                              | PTD2/LLWU_P13/SPI0_SOUT/UART2_RX                              | 60 |
|    | PTC11/LLWU_P11                                                            |                              | PTD3/SPI0_SIN/UART2_TX                                        | 61 |
|    |                                                                           |                              | PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN             | 62 |
|    |                                                                           |                              | PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT | 63 |
|    |                                                                           |                              | PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0 | 64 |
|    |                                                                           |                              | PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1                      | 1  |
|    |                                                                           |                              | PTE0/UART1_TX/RTC_CLKOUT                                      | 2  |
|    |                                                                           |                              | PTE1/LLWU_P0/UART1_RX                                         |    |
|    |                                                                           | VSS1<br>VSS2<br>VSS3<br>VSSA |                                                               |    |

<Value>