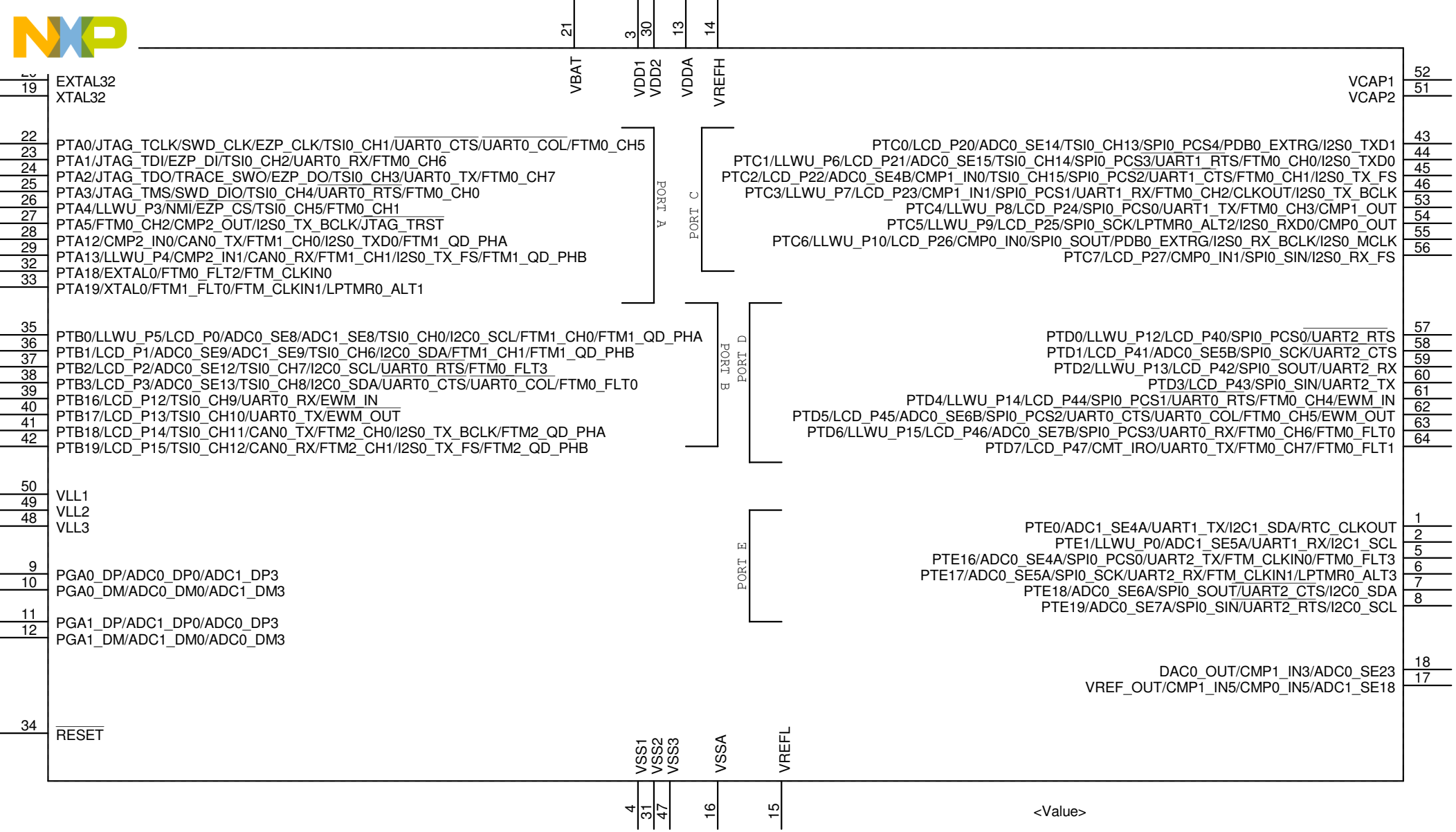
21 VBAT
3 VDD1
30 VDD2
13 VDDA
14 VREFH
20 EXTAL32
19 XTAL32
22 PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5
23 PTA1/JTAG_TDI/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6
24 PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7
25 PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0
26 PTA4/LLWU_P3/NMI/EZP_CS/TSI0_CH5/FTM0_CH1
27 PTA5/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST
28 PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA
29 PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB
32 PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0
33 PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1
PORT A
35 PTB0/LLWU_P5/LCD_P0/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA
36 PTB1/LCD_P1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB
37 PTB2/LCD_P2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3
38 PTB3/LCD_P3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0
39 PTB16/LCD_P12/TSI0_CH9/UART0_RX/EWM_IN
40 PTB17/LCD_P13/TSI0_CH10/UART0_TX/EWM_OUT
41 PTB18/LCD_P14/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA
42 PTB19/LCD_P15/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB
PORT B
50 VLL1
49 VLL2
48 VLL3
9 PGA0_DP/ADC0_DP0/ADC1_DP3
10 PGA0_DM/ADC0_DM0/ADC1_DM3
11 PGA1_DP/ADC1_DP0/ADC0_DP3
12 PGA1_DM/ADC1_DM0/ADC0_DM3
34 RESET
4 VSS1
31 VSS2
47 VSS3
16 VSSA
15 VREFL
PORT C
PTC0/LCD_P20/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD1 43
PTC1/LLWU_P6/LCD_P21/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0 44
PTC2/LCD_P22/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS 45
PTC3/LLWU_P7/LCD_P23/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK 46
PTC4/LLWU_P8/LCD_P24/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT 53
PTC5/LLWU_P9/LCD_P25/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT 54
PTC6/LLWU_P10/LCD_P26/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK 55
PTC7/LCD_P27/CMP0_IN1/SPI0_SIN/I2S0_RX_FS 56
PORT D
PTD0/LLWU_P12/LCD_P40/SPI0_PCS0/UART2_RTS 57
PTD1/LCD_P41/ADC0_SE5B/SPI0_SCK/UART2_CTS 58
PTD2/LLWU_P13/LCD_P42/SPI0_SOUT/UART2_RX 59
PTD3/LCD_P43/SPI0_SIN/UART2_TX 60
PTD4/LLWU_P14/LCD_P44/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN 61
PTD5/LCD_P45/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT 62
PTD6/LLWU_P15/LCD_P46/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0 63
PTD7/LCD_P47/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1 64
PORT E
PTE0/ADC1_SE4A/UART1_TX/I2C1_SDA/RTC_CLKOUT 1
PTE1/LLWU_P0/ADC1_SE5A/UART1_RX/I2C1_SCL 2
PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3 5
PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3 6
PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA 7
PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL 8
DAC0_OUT/CMP1_IN3/ADC0_SE23 18
VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18 17
VCAP1 52
VCAP2 51
<Value>