



	PTA0/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK	VDD1	E5
J/	TSIO_CH3/PTA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI	VDD2	E6
H9	TSIO_CH3/PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	VDD3	E7
J8	TSIO_CH4/PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO	VDD4	L10
K7	TSIO_CH5/PTA4/LLWU_P3/FTM0_CH1/NMI/EZP_CS	VDDA	F5
J9	PTA5/FTM0_CH2/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST		
K8	PTA10/FTM2_CH0/FB_AD15/FTM2_QD_PHA/TRACE_D0	VBAT	K6
L8	CMP2_IN0/PTA12/FTM1_CH0/FB_CS5/FB_TSI21/FB_BE23_16_BLS15_8/I2S0_TXD/FTM1_QD_PHA		
K9	CMP2_IN1/PTA13/LLWU_P4/FTM1_CH1/FB_CS4/FB_TSI20/FB_BE31_24_BLS7_0/I2S0_TX_FS/FTM1_QD_PHB	VREFH	G5
L9	PTA14/SPI0_PCS0/UART0_TX/FB_AD31/I2S0_TX_BCLK		
J10	PTA15/SPI0_SCK/UART0_RX/FB_AD30/I2S0_RXD	VREFL	G6
H10	PTA16/SPI0_SOUT/UART0_CTS/FB_AD29/I2S0_RX_FS		
L11	ADC1_SE17/PTA17/SPI0_SIN/UART0_RTS/FB_AD28/I2S0_MCLK/I2S0_CLKIN	VREGIN	G2
K11	EXTAL/PTA18/FTM0_FLT2/FTM_CLKIN0		
H11	XTAL/PTA19/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1	VOUT33	G1
	PTA29/FB_AD19		
G11	LCD_P0/ADC0_SE8/ADC1_SE8/TSIO_CH0/PTB0/LLWU_P5/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	VLL1	A9
G10	LCD_P1/ADC0_SE9/ADC1_SE9/TSIO_CH6/PTB1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	VLL2	A10
G9	LCD_P2/ADC0_SE12/TSIO_CH7/PTB2/I2C0_SCL/UART0_RTS/FTM0_FLT3	VLL3	A11
G8	LCD_P3/ADC0_SE13/TSIO_CH8/PTB3/I2C0_SDA/UART0_CTS/FTM0_FLT0		
F11	LCD_P6/ADC1_SE12/PTB6	VCAP1	C11
E11	LCD_P7/ADC1_SE13/PTB7	VCAP2	B11
D11	LCD_P8/PTB8/UART3_RTS		
E10	LCD_P9/PTB9/SPI1_PCS1/UART3_CTS		
D10	LCD_P10/ADC1_SE14/PTB10/SPI1_PCS0/UART3_RX/FTM0_FLT1	PGA0_DP/ADC0_DP0/ADC1_DP3	K1
C10	LCD_P11/ADC1_SE15/PTB11/SPI1_SCK/UART3_TX/FTM0_FLT2	PGA0_DM/ADC0_DM0/ADC1_DM3	K2
B10	LCD_P12/TSIO_CH9/PTB16/SPI1_SOUT/UART0_RX/EWM_IN	PGA1_DP/ADC1_DP0/ADC0_DP3	L1
E9	LCD_P13/TSIO_CH10/PTB17/SPI1_SIN/UART0_TX/EWM_OUT	PGA1_DM/ADC1_DM0/ADC0_DM3	L2
D9	LCD_P14/TSIO_CH11/PTB18/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA		
C9	LCD_P15/TSIO_CH12/PTB19/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB	ADC0_DP1/OP0_DP0	H1
F10	LCD_P16/PTB20/SPI2_PCS0/CMP0_OUT	ADC0_DM1/OP0_DM0	H2
F9	LCD_P17/PTB21/SPI2_SCK/CMP1_OUT	ADC1_DP1/OP1_DP0/OP1_DM1	J1
F8	LCD_P18/PTB22/SPI2_SOUT/CMP2_OUT	ADC1_DM1/OP1_DM0	J2
E8	LCD_P19/PTB23/SPI2_SIN/SPI0_PCS5		
B9	LCD_P20/ADC0_SE14/TSIO_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD	USB0_DP	F1
D8	LCD_P21/ADC0_SE15/TSIO_CH14/PTC1/LLWU_P6/SPI0_PCS3/UART1_RTS/FTM0_CH0	USB0_DM	F2
C8	LCD_P22/ADC0_SE4B/CMP1_IN0/TSIO_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1		
B8	LCD_P23/CMP1_IN1/PTC3/LLWU_P7/SPI0_PCS1/UART1_RX/FTM0_CH2		
A8	LCD_P24/PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT	TRI1_DP	J5
D7	LCD_P25/PTC5/LLWU_P9/SPI0_SCK/LPT0_ALT2/CMP0_OUT	TRI1_DM	H5
C7	LCD_P26/CMP0_IN0/PTC6/LLWU_P10/SPI0_SOUT/PDB0_EXTRG		
B7	LCD_P27/CMP0_IN1/PTC7/SPI0_SIN		
A7	LCD_P28/ADC1_SE4B/CMP0_IN2/PTC8/I2S0_MCLK/I2S0_CLKIN	TRIO_DP	J4
D6	LCD_P29/ADC1_SE5B/CMP0_IN3/PTC9/I2S0_RX_BCLK/FTM2_FLT0	TRIO_DM	H4
C6	LCD_P30/ADC1_SE6B/CMP0_IN4/PTC10/I2C1_SCL/I2S0_RX_FS	TRIO_OUT/OP1_DM2	K3
C5	LCD_P31/ADC1_SE7b/PTC11/LLWU_P11/I2C1_SDA/I2S0_RXD	TRI1_OUT/CMP2_IN5/ADC1_SE22	H6
B6	LCD_P32/PTC12/UART4_RTS		
A6	LCD_P33/PTC13/UART4_CTS		
A5	LCD_P34/PTC14/UART4_RX	DAC1_OUT/CMP2_IN3/ADC1_SE23/OP0_DP5/OP1_DP5	K4
B5	LCD_P35/PTC15/UART4_TX	DAC0_OUT/CMP1_IN3/ADC0_SE23/OP0_DP4/OP1_DP4	K5
D5	LCD_P36/PTC16/UART3_RX		
C4	LCD_P37/PTC17/UART3_TX	ADC0_SE16/OP0_OUT/CMP1_IN2/ADC0_SE21/OP0_DP1/OP1_DP1	H3
B4	LCD_P38/PTC18/UART3_RTS	ADC1_SE16/OP1_OUT/CMP2_IN2/ADC0_SE22/OP0_DP2/OP1_DP2	J3
A4	LCD_P39/PTC19/UART3_CTS	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	L3
D4	LCD_P40/PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS		
D3	LCD_P41/ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS		
C3	LCD_P42/PTD2/LLWU_P13/SPI0_SOUT/UART2_RX	EXTAL32	L5
B3	LCD_P43/PTD3/SPI0_SIN/UART2_TX	XTAL32	L4
A3	LCD_P44/PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN		
A2	LCD_P45/ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/FTM0_CH5/EWM_OUT		
B2	LCD_P46/ADC0_SE7b/PTD6/LLWU_P15/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0		
A1	LCD_P47/PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	RESET	J11
B1	PTD10/UART5_RTS/FB_AD9		
C2	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_AD8		
C1	PTD12/SPI2_SCK/SDHC0_D4/FB_AD7		
D2	PTD13/SPI2_SOUT/SDHC0_D5/FB_AD6	RESERVED	L7
D1	PTD14/SPI2_SIN/SDHC0_D6/FB_AD5		
E1	PTD15/SPI2_PCS1/SDHC0_D7/FB_RW		
E4	ADC1_SE4A/PTE0/SPI1_PCS1/UART1_TX/SDHC0_D1/FB_AD27/I2C1_SDA	VSSA	F6
E3	ADC1_SE5a/PTE1/LLWU_P0/SPI1_SOUT/UART1_RX/SDHC0_D0/FB_AD26/I2C1_SCL		
E2	ADC1_SE6a/PTE2/LLWU_P1/SPI1_SCK/UART1_CTS/SDHC0_DCLK/FB_AD25	VSS1	F7
F4	ADC1_SE7A/PTE3/SPI1_SIN/UART1_RTS/SDHC0_CMD/FB_AD24	VSS2	K10
H7	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3/FB_CS3/FB_BE7_0_BLS31_24/FB_TA	VSS3	G3
G4	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2/FB_TBST/FB_CS2/FB_BE15_8_BLS23_16	VSS4	G7
F3	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/FB_ALE/FB_CS1/FB_TS/I2S0_CLKIN	VSS5	L6