

U?			
12	ADC0_DM0/ADC1_DM3/PGA0_DM		
11	ADC0_DP0/ADC1_DP3/PGA0_DP		
8	ADC0_DM1/OP0_DM0	VDD1	1
7	ADC0_DP1/OP0_DP0	VDD2	38
		VDDA	15
20	ADC0_SE16/OP0_OUT/CMP1_IN2/ADC0_SE21/OP0_DP1/OP1_DP1	VREFH	16
19	ADC1_SE16/OP1_OUT/CMP2_IN2/ADC0_SE22/OP0_DP2/OP1_DP2		
28	ADC0_SE23/DAC0_OUT/CMP1_IN3/OP0_DP4/OP1_DP4	VBAT1	32
29	ADC1_SE23/CMP0_IN4/CMP2_IN3/OP0_DP5/OP1_DP5		
14	ADC1_DM0/ADC0_DM3/PGA1_DM	VCAP1	64
13	ADC1_DP0/ADC0_DP3/PGA1_DP	VCAP2	63
9	ADC1_DP1/OP1_DP0/OP1_DM1		
10	ADC1_DM1/OP1_DM0	VLL1	62
		VLL2	61
23	TRI0_DM	VLL3	60
24	TRI0_DP		
27	ADC1_SE22/CMP2_IN5		
21	ADC1_SE18/VREF_OUT/CMP1_IN5/CMP0_IN5	VOUT33	5
22	TRI0_OUT/OP1_DM2	VREGIN	6
33	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5		
34	PTA1/JTAG_TD/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6	USB0_DP	3
35	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7	USB0_DM	4
36	PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0		
37	PTA4/LLWU_P3/NMI/EZP_CS/TSI0_CH5/FTM0_CH1		
40	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0		
41	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1		
43	PTB0/LLWU_P5/LCD_P0/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	EXTAL32	31
44	PTB1/LCD_P1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	XTAL32	30
45	PTB2/LCD_P2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3		
46	PTB3/LCD_P3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0		
47	PTB8/LCD_P8/UART3_RTS		
48	PTB9/LCD_P9/SPI1_PCS1/UART3_CTS	RESET	42
49	PTB10/LCD_P10/ADC1_SE14/SPI1_PCS0/UART3_RX/FTM0_FLT1		
50	PTB11/LCD_P11/ADC1_SE15/SPI1_SCK/UART3_TX/FTM0_FLT2		
51	PTB16/LCD_P12/TSI0_CH9/SPI1_SOUT/UART0_RX/EWM_IN		
52	PTB17/LCD_P13/TSI0_CH10/SPI1_SIN/UART0_TX/EWM_OUT		
53	PTB18/LCD_P14/TSI0_CH11/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA	NC_25	25
54	PTB19/LCD_P15/TSI0_CH12/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB	NC_26	26
55	PTC0/LCD_P20/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD1		
56	PTC1/LLWU_P6/LCD_P21/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0		
57	PTC2/LCD_P22/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS		
58	PTC3/LLWU_P7/LCD_P23/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK		
65	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT/LCD_P24	VSS1	2
66	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT/LCD_P25	VSS2	39
67	PTC6/LLWU_P10/LCD_P26/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK	VSS3	59
68	PTC7/LCD_P27/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS		
69	PTC8/LCD_P28/ADC1_SE4B/CMP0_IN2/I2S0_MCLK		
70	PTC9/LCD_P29/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FTM2_FLT0		
71	PTC10/LCD_P30/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS		
72	PTC11/LLWU_P11/LCD_P31/ADC1_SE7B/I2C1_SDA/I2S0_RXD1		
73	PTD0/LLWU_P12/LCD_P40/SPI0_PCS0/UART2_RTS	VSSA	18
74	PTD1/LCD_P41/ADC0_SE5B/SPI0_SCK/UART2_CTS	VREFL	17
75	PTD2/LLWU_P13/LCD_P42/SPI0_SOUT/UART2_RX		
76	PTD3/LCD_P43/SPI0_SIN/UART2_TX		
77	PTD4/LLWU_P14/LCD_P44/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN		
78	PTD5/LCD_P45/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT		
79	PTD6/LLWU_P15/LCD_P46/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0		
80	PTD7/LCD_P47/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1		

<Value>