



E7	VDD3
E8	VDD4
F5	VDD5
F8	VDD6
H5	VDDA
L6	VBAT
G5	VREFH
M3	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18
J3	ADC0_SE16/CMP1_IN2/ADC0_SE21
K3	ADC1_SE16/CMP2_IN2/ADC0_SE22
L3	DAC0_OUT/CMP1_IN3/ADC0_SE23
L4	DAC1_OUT/CMP0_IN4/CMP2_IN3/ADC1_SE23

PORT A

M6	EXTAL32
M7	XTAL32
L2	PGA0_DM/ADC0_DM0/ADC1_DM3
L1	PGA0_DP/ADC0_DP0/ADC1_DP3
M2	PGA1_DM/ADC1_DM0/ADC0_DM3
M1	PGA1_DP/ADC1_DP0/ADC0_DP3
J2	PGA2_DM/ADC2_DM0/ADC3_DM3/ADC0_DM1
J1	PGA2_DP/ADC2_DP0/ADC3_DP3/ADC0_DP1
K2	PGA3_DM/ADC3_DM0/ADC2_DM3/ADC1_DM1
K1	PGA3_DP/ADC3_DP0/ADC2_DP3/ADC1_DP1

PORT B

G2	VREGIN
G1	VOUT33
H2	USB0_DM
H1	USB0_DP
L12	RESET
L5	TAMPER0/RTC_WAKEUP
K5	TAMPER1
K4	TAMPER2
J4	TAMPER3
H4	TAMPER4
M4	TAMPER5

PORT C

A10	NC_A10
B10	NC_B10
C10	NC_C10
M5	NC_M5
H6	VSSA
G6	VREFL

PORT D

F6	VSS1
F7	VSS2
G7	VSS4
G8	VSS5
H3	VSS3
H7	VSS6
H8	VSS7
M10	VSS8

PORT E

<Value>

PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5	J5
PTA1/JTAG_TD/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6	J6
PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7	K6
PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0	K7
PTA4/LLWU_P3/NMI/EZP_CS/TSI0_CH5/FTM0_CH1	L7
PTA5/USB_CLKIN/FTM0_CH2/RMII0_RXER/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST	M8
PTA6/ADC3_SE6A/ULPI_CLK/FTM0_CH3/I2S1_RXD0/TRACE_CLKOUT	J7
PTA7/ADC0_SE10/ULPI_DIR/FTM0_CH4/I2S1_RX_BCLK/TRACE_D3	J8
PTA8/ADC0_SE11/ULPI_NXT/FTM1_CH0/I2S1_RX_FS/FTM1_QD_PHA/TRACE_D2	K8
PTA9/ADC3_SE5A/ULPI_STP/FTM1_CH1/MII0_RXD3/FTM1_QD_PHB/TRACE_D1	L8
PTA10/ADC3_SE4A/ULPI_DATA0/FTM2_CH0/MII0_RXD2/FTM2_QD_PHA/TRACE_D0	M9
PTA11/ADC3_SE15/ULPI_DATA1/FTM2_CH1/MII0_RXCLK/FTM2_QD_PHB	L9
PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD0/FTM1_QD_PHA	K9
PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB	J9
PTA14/CMP3_IN0/SPI0_PCS0/UART0_TX/RMII0_CRS_DV/MII0_RXDV/I2S0_RX_BCLK/I2S0_TXD1	L10
PTA15/CMP3_IN1/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD0	L11
PTA16/CMP3_IN2/SPI0_SOUT/UART0_CTS/UART0_COL/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS/I2S0_RXD1	J10
PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK	K11
PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0	M12
PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	M11
PTA24/CMP3_IN4/ULPI_DATA2/MII0_TXD2/FB_A29	K12
PTA25/CMP3_IN5/ULPI_DATA3/MII0_TXCLK/FB_A28	J12
PTA26/ADC2_SE15/ULPI_DATA4/MII0_TXD3/FB_A27	J11
PTA27/ADC2_SE14/ULPI_DATA5/MII0_CRS/FB_A26	J10
PTA28/ADC2_SE13/ULPI_DATA6/MII0_TXER/FB_A25	H12
PTA29/ADC2_SE12/ULPI_DATA7/MII0_COL/FB_A24	H11
PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/ADC2_SE8/ADC3_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA	H10
PTB1/ADC0_SE9/ADC1_SE9/ADC2_SE9/ADC3_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB	H9
PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3	G12
PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/ENET0_1588_TMR1/FTM0_FLT0	G11
PTB4/ADC1_SE10/ENET0_1588_TMR2/FTM1_FLT0	G10
PTB5/ADC1_SE11/ENET0_1588_TMR3/FTM2_FLT0	G9
PTB6/ADC1_SE12/FB_AD23	F12
PTB7/ADC1_SE13/FB_AD22	F11
PTB8/UART3_RTS/FB_AD21	F10
PTB9/SPI1_PCS1/UART3_CTS/FB_AD20	F9
PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/I2S1_TX_BCLK/FB_AD19/FTM0_FLT1	E12
PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/I2S1_TX_FS/FB_AD18/FTM0_FLT2	E11
PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/I2S1_TXD0/FB_AD17/EWM_IN	E10
PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/I2S1_TXD1/FB_AD16/EWM_OUT	E9
PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	D12
PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	D11
PTB20/ADC2_SE4A/SPI2_PCS0/FB_AD31/NFC_DATA15/CMP0_OUT	D10
PTB21/ADC2_SE5A/SPI2_SCK/FB_AD30/NFC_DATA14/CMP1_OUT	D9
PTB22/SPI2_SOUT/FB_AD29/NFC_DATA13/CMP2_OUT	C12
PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28/NFC_DATA12/CMP3_OUT	C11
PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/NFC_DATA11/I2S0_TXD1	B12
PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/NFC_DATA10/I2S0_TXD0	B11
PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/NFC_DATA9/I2S0_TX_FS	A12
PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK	A11
PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/NFC_DATA8/CMP1_OUT/I2S1_TX_BCLK	A9
PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/NFC_DATA7/CMP0_OUT/I2S1_TX_FS	D8
PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/NFC_DATA6/I2S0_MCLK	C8
PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8/NFC_DATA5	B8
PTC8/ADC1_SE4B/CMP0_IN2/FTM3_CH4/I2S0_MCLK/FB_AD7/NFC_DATA4	A8
PTC9/ADC1_SE5B/CMP0_IN3/FTM3_CH5/I2S0_RX_BCLK/FB_AD6/NFC_DATA3/FTM2_FLT0	D7
PTC10/ADC1_SE6B/I2C1_SCL/FTM3_CH6/I2S0_RX_FS/FB_AD5/NFC_DATA2/I2S1_MCLK	C7
PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/FTM3_CH7/I2S0_RXD1/FB_RW/NFC_WE	B7
PTC12/UART4_RTS/FB_AD27/FTM3_FLT0	A7
PTC13/UART4_CTS/FB_AD26	D6
PTC14/UART4_RX/FB_AD25	C6
PTC15/UART4_TX/FB_AD24	B6
PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSI21/FB_BE23_16/NFC_RB	A6
PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSI20/FB_BE31_24/NFC_CE0	D5
PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8/NFC_CE1	C5
PTC19/UART3_CTS/ENET0_1588_TMR3/FB_CS3/FB_BE7_0/FB_TA	B5
PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FTM3_CH0/FB_ALE/FB_CS1/FB_TS/I2S1_RXD1	A5
PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FTM3_CH1/FB_CS0/I2S1_RXD0	D4
PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FTM3_CH2/FB_AD4/I2S1_RX_FS	C4
PTD3/SPI0_SIN/UART2_TX/FTM3_CH3/FB_AD3/I2S1_RX_BCLK	B4
PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/NFC_DATA1/EWM_IN	A4
PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/NFC_DATA0/EWM_OUT	A3
PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	A2
PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	A1
PTD8/I2C0_SCL/UART5_RX/FB_A16/NFC_CLE	C9
PTD9/I2C0_SDA/UART5_TX/FB_A17/NFC_ALE	B9
PTD10/UART5_RTS/FB_A18/NFC_RE	B3
PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	B2
PTD12/SPI2_SCK/FTM3_FLT0/SDHC0_D4/FB_A20	B1
PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	C3
PTD14/SPI2_SIN/SDHC0_D6/FB_A22	C2
PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	C1
PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA/RTC_CLKOUT	D3
PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL/SPI1_SIN	D2
PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK	D1
PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD/SPI1_SOUT	E4
PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3	E3
PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2/FTM3_CH0	E2
PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/FTM3_CH1/USB_SOF_OUT	E1
PTE7/UART3_RTS/I2S0_RXD0/FTM3_CH2	F4
PTE8/ADC2_SE16/I2S0_RXD1/UART5_TX/I2S0_RX_FS/FTM3_CH3	F3
PTE9/ADC2_SE17/I2S0_TXD1/UART5_RX/I2S0_RX_BCLK/FTM3_CH4	F2
PTE10/UART5_CTS/I2S0_TXD0/FTM3_CH5	F1
PTE11/ADC3_SE16/UART5_RTS/I2S0_TX_FS/FTM3_CH6	G4
PTE12/ADC3_SE17/I2S0_TX_BCLK/FTM3_CH7	G3