

NXP

		3	4		
		VDD	VREFH		
30	PTA0/IRQ_0/LLWU_P7/SWD_CLK/ADC0_SE12/CMP0_IN2/TPM1_CH0		PTB0/IRQ_8/LLWU_P4/ADC0_SE6/EXTRG_IN/SPI0_SCK		16
31	PTA1/IRQ_1/LPTMR0_ALT1/RESET/TPM_CLKIN0		PTB1/IRQ_9/ADC0_SE5/CMP0_IN3/UART0_TX/UART0_RX		17
32	PTA2/SWD_DIO/CMP0_OUT		PTB2/IRQ_10/LLWU_P5/ADC0_SE4/UART0_RX/UART0_TX		18
7	PTA3/EXTAL0/I2C0_SCL/I2C0_SDA		PTB3/IRQ_14/I2C0_SCL/UART0_TX		23
8	PTA4/LLWU_P0/XTAL0/I2C0_SDA/I2C0_SCL		PTB4/IRQ_15/LLWU_P6/I2C0_SDA/UART0_RX		24
9	PTA5/LLWU_P1/RTC_CLK_IN/TPM0_CH5/SPI0_SS		PTB5/IRQ_16/NMI/ADC0_SE1/CMP0_IN1/TPM1_CH1		25
10	PTA6/LLWU_P2/TPM0_CH4/SPI0_MISO		PTB6/IRQ_2/LPTMR0_ALT3/TPM0_CH3/TPM_CLKIN1		1
15	PTA7/IRQ_7/LLWU_P3/ADC0_SE7/SPI0_MISO/SPI0_MOSI		PTB7/IRQ_3/TPM0_CH2		2
19	PTA8/ADC0_SE3		PTB8/ADC0_SE11/TPM0_CH3		11
20	PTA9/ADC0_SE2		PTB9/ADC0_SE10/TPM0_CH2		12
21	PTA10/IRQ_12		PTB10/ADC0_SE9/TPM0_CH1		13
22	PTA11/IRQ_13		PTB11/ADC0_SE8/TPM0_CH0		14
26	PTA12/IRQ_17/LPTMR0_ALT2/ADC0_SE0/CMP0_IN0/TPM1_CH0/TPM_CLKIN0		PTB12		28
27	PTA13		PTB13/ADC0_SE13/TPM1_CH1/RTC_CLKOUT		29
		VSS	VREFL		
		6	5		<Value>