

		3	30	48	14	13		
		VDD1	VDD2	VDD3	VREFH	VDDA		
22	PTA0/SWD_CLK/TSIO_CH1/TPM0_CH5							57
23	PTA1/TSIO_CH2/UART0_RX/TPM2_CH0							58
24	PTA2/TSIO_CH3/UART0_TX/TPM2_CH1							59
25	PTA3/SWD_DIO/TSIO_CH4/I2C1_SCL/TPM0_CH0							60
26	PTA4/NMI/TSIO_CH5/I2C1_SDA/TPM0_CH1							61
27	PTA5/TPM0_CH2							62
28	PTA12/TPM1_CH0							63
29	PTA13/TPM1_CH1							64
32	PTA18/EXTAL0/UART1_RX/TPM_CLKIN0							
33	PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1							
34	PTA20/RESET							
35	PTB0/LLWU_P5/ADC0_SE8/TSIO_CH0/I2C0_SCL/TPM1_CH0							
36	PTB1/ADC0_SE9/TSIO_CH6/I2C0_SDA/TPM1_CH1							
37	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/TPM2_CH0							1
38	PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/TPM2_CH1							2
39	PTB16/TSIO_CH9/SPI1_MOSI/UART0_RX/TPM_CLKIN0/SPI1_MISO							5
40	PTB17/TSIO_CH10/SPI1_MISO/UART0_TX/TPM_CLKIN1/SPI1_MOSI							6
41	PTB18/TSIO_CH11/TPM2_CH0							7
42	PTB19/TSIO_CH12/TPM2_CH1							8
								9
								10
								11
43	PTC0/ADC0_SE14/TSIO_CH13/EXTRG_IN/CMP0_OUT							12
44	PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/TSIO_CH14/I2C1_SCL/TPM0_CH0							20
45	PTC2/ADC0_SE11/TSIO_CH15/I2C1_SDA/TPM0_CH1							21
46	PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT							17
49	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3							18
50	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT							19
51	PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO							
52	PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI							
53	PTC8/CMP0_IN2/I2C0_SCL/TPM0_CH4							
54	PTC9/CMP0_IN3/I2C0_SDA/TPM0_CH5							
55	PTC10/I2C1_SCL							
56	PTC11/I2C1_SDA							
		VSS1	VSS2	VSS3	VREFL	VSSA		
		4	31	47	15	16	<Value>	