

U?	7	38	60	17	18	
	VDD1	VDD2	VDD3	VDDA	VREFH	
26	PTA0/SWD_CLK/TSI0_CH1/TPM0_CH5					PTD0/SPI0_PCS0/TPM0_CH073
27	PTA1/TSI0_CH2/UART0_RX/TPM2_CH0					PTD1/ADC0_SE5B/SPI0_SCK/TPM0_CH174
28	PTA2/TSI0_CH3/UART0_TX/TPM2_CH1					PTD2/SPI0_MOSI/UART2_RX/TPM0_CH2/SPI0_MISO75
29	PTA3/SWD_DIO/TSI0_CH4/I2C1_SCL/TPM0_CH0					PTD3/SPI0_MISO/UART2_TX/TPM0_CH3/SPI0_MOSI76
30	PTA4/NMI/TSI0_CH5/I2C1_SDA/TPM0_CH1					PTD4/LLWU_P14/SPI1_PCS0/UART2_RX/TPM0_CH477
31	PTA5/TPM0_CH2					PTD5/ADC0_SE6B/SPI1_SCK/UART2_TX/TPM0_CH578
32	PTA12/TPM1_CH0					PTD6/LLWU_P15/ADC0_SE7B/SPI1_MOSI/UART0_RX/SPI1_MISO79
33	PTA13/TPM1_CH1					PTD7/SPI1_MISO/UART0_TX/SPI1_MOSI80
34	PTA14/SPI0_PCS0/UART0_TX					
35	PTA15/SPI0_SCK/UART0_RX					
36	PTA16/SPI0_MOSI/SPI0_MISO					
37	PTA17/SPI0_MISO/SPI0_MOSI					PTE0/UART1_TX/RTC_CLKOUT/CMP0_OUT/I2C1_SDA1
40	PTA18/EXTAL0/UART1_RX/TPM_CLKIN0					PTE1/SPI1_MOSI/UART1_RX/SPI1_MISO/I2C1_SCL2
41	PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1					PTE2/SPI1_SCK3
42	PTA20/RESET					PTE3/SPI1_MISO/SPI1_MOSI4
						PTE4/SPI1_PCS05
43	PTB0/LLWU_P5/ADC0_SE8/TSI0_CH0/I2C0_SCL/TPM1_CH0					PTE59
44	PTB1/ADC0_SE9/TSI0_CH6/I2C0_SDA/TPM1_CH1					PTE16/ADC0_DP1/ADC0_SE1/SPI0_PCS0/UART2_TX/TPM_CLKIN010
45	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/TPM2_CH0					PTE17/ADC0_DM1/ADC0_SE5A/SPI0_SCK/UART2_RX/TPM_CLKIN1/LPTMR0_ALT311
46	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/TPM2_CH1					PTE18/ADC0_DP2/ADC0_SE2/SPI0_MOSI/I2C0_SDA/SPI0_MISO12
47	PTB8/EXTRG_IN					PTE19/ADC0_DM2/ADC0_SE6A/SPI0_MISO/I2C0_SCL/SPI0_MOSI13
48	PTB9					PTE20/ADC0_DP0/ADC0_SE0/TPM1_CH0/UART0_TX14
49	PTB10/SPI1_PCS0					PTE21/ADC0_DM0/ADC0_SE4A/TPM1_CH1/UART0_RX15
50	PTB11/SPI1_SCK					PTE22/ADC0_DP3/ADC0_SE3/TPM2_CH0/UART2_TX16
51	PTB16/TSI0_CH9/SPI1_MOSI/UART0_RX/TPM_CLKIN0/SPI1_MISO					PTE23/ADC0_DM3/ADC0_SE7A/TPM2_CH1/UART2_RX24
52	PTB17/TSI0_CH10/SPI1_MISO/UART0_TX/TPM_CLKIN1/SPI1_MOSI					PTE24/TPM0_CH0/I2C0_SCL25
53	PTB18/TSI0_CH11/TPM2_CH0					PTE25/TPM0_CH1/I2C0_SDA21
54	PTB19/TSI0_CH12/TPM2_CH1					PTE29/CMP0_IN5/ADC0_SE4B/TPM0_CH2/TPM_CLKIN022
						PTE30/DAC0_OUT/ADC0_SE23/CMP0_IN4/TPM0_CH3/TPM_CLKIN123
						PTE31/TPM0_CH4
55	PTC0/ADC0_SE14/TSI0_CH13/EXTRG_IN/CMP0_OUT					
56	PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/TSI0_CH14/I2C1_SCL/TPM0_CH0					
57	PTC2/ADC0_SE11/TSI0_CH15/I2C1_SDA/TPM0_CH1					
58	PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT					
61	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3					
62	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT					
63	PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO					
64	PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI					
65	PTC8/CMP0_IN2/I2C0_SCL/TPM0_CH4					
66	PTC9/CMP0_IN3/I2C0_SDA/TPM0_CH5					
67	PTC10/I2C1_SCL					
68	PTC11/I2C1_SDA					
69	PTC12/TPM_CLKIN0					
70	PTC13/TPM_CLKIN1					
71	PTC16					
72	PTC17					
	VSS3	VSS2	VSS1	VSSA	VREFL	
	59	39	8	20	19	<Value>