



		15	7		
		VDD	VDDA		
10	PTA0/SWD_CLK/TPM0_CH5				
11	PTA1/UART0_RX/TPM2_CH0				
12	PTA2/UART0_TX/TPM2_CH1			PTD4/LLWU_P14/SPI1_PCS0/UART2_RX/TPM0_CH4	29
13	PTA3/SWD_DIO/I2C1_SCL/TPM0_CH0			PTD5/ADC0_SE6B/SPI1_SCK/UART2_TX/TPM0_CH5	30
14	PTA4/NMI/I2C1_SDA/TPM0_CH1			PTD6/LLWU_P15/ADC0_SE7B/SPI1_MOSI/UART0_RX/SPI1_MISO	31
17	PTA18/EXTAL0/UART1_RX/TPM_CLKIN0			PTD7/SPI1_MISO/UART0_TX/SPI1_MOSI	32
18	PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1				
19	PTA20/RESET				
20	PTB0/LLWU_P5/ADC0_SE8/I2C0_SCL/TPM1_CH0			PTE0/UART1_TX/RTC_CLKOUT/CMP0_OUT/I2C1_SDA	1
21	PTB1/ADC0_SE9/I2C0_SDA/TPM1_CH1			PTE30/ADC0_SE23/CMP0_IN4/TPM0_CH3/TPM_CLKIN1	9
22	PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/I2C1_SCL/TPM0_CH0				
23	PTC2/ADC0_SE11/I2C1_SDA/TPM0_CH1				
24	PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT			USB0_DP	3
25	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3			USB0_DM	4
26	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT				
27	PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO				
28	PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI			VOUT33	5
				VREGIN	6
			VSS1		
			VSS2		
			VSSA		
			EX_PAD		
<Value>		2	16	8	33