

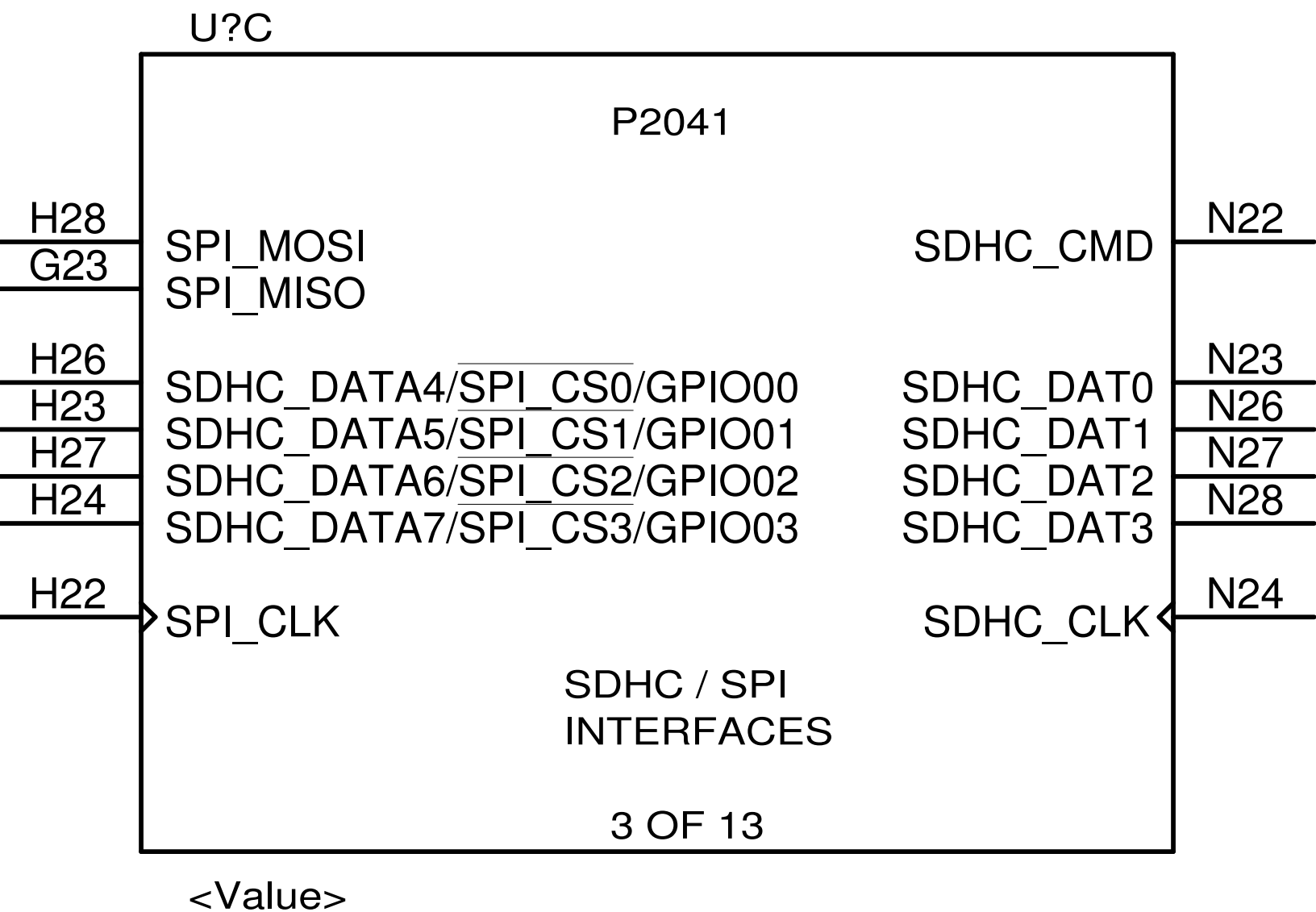


U?A			MVREF	W7
AD14	MCK0	P2041		AA2
AE14	MCK0		MDQS0	AA1
AE13	MCK1		MDQS0	AD3
AD13	MCK1		MDQS1	AD4
AG13	MCK2		MDQS1	AE9
AH13	MCK2		MDQS2	AD9
AG14	MCK3		MDQS2	AD1
AH14	MCK3		MDQS3	AD2
			MDQS3	AD18
			MDQS4	AD17
		DDR_SDRAM	MDQS4	AG24
			MDQS5	AH24
Y4			MDQS5	AE23
Y5	MDQ00		MDQS6	AE22
AB1	MDQ01		MDQS6	AE28
AB3	MDQ02		MDQS7	AF28
Y1	MDQ03		MDQS7	AH3
Y3	MDQ04		MDQS8	AG3
Y3	MDQ05		MDQS8	
AA3	MDQ06			AH2
AA4	MDQ07		MECC0	AF3
AB5	MDQ08		MECC1	AG5
AB6	MDQ09		MECC2	AH5
AE4	MDQ10		MECC3	AG1
AE6	MDQ11		MECC4	AG2
AA6	MDQ12		MECC5	AH4
AB4	MDQ13		MECC6	AF5
AC6	MDQ14		MECC7	
AD6	MDQ15			Y2
AF7	MDQ16		MDM0	AC7
AF8	MDQ17		MDM1	AD8
AD11	MDQ18		MDM2	AD5
AF11	MDQ19		MDM3	AE17
AE7	MDQ20		MDM4	AH25
AD7	MDQ21		MDM5	AF22
AD10	MDQ22		MDM6	AE26
AE10	MDQ23		MDM7	AF4
AC1	MDQ24		MDM8	
AC2	MDQ25			AC16
AF1	MDQ26		MBA0	AC15
AF2	MDQ27		MBA1	AC8
AC3	MDQ28		MBA2	
AC4	MDQ29			AG16
AE1	MDQ30		MA00	AF12
AE3	MDQ31		MA01	AC12
AE16	MDQ32		MA02	AH11
AD16	MDQ33		MA03	AG11
AE19	MDQ34		MA04	AH10
AD19	MDQ35		MA05	AC11
AF15	MDQ36		MA06	AC10
AF16	MDQ37		MA07	AF10
AF18	MDQ38		MA08	AH9
AF19	MDQ39		MA09	AH16
AH23	MDQ40		MA10	AG9
AG23	MDQ41		MA11	AC9
AH27	MDQ42		MA12	AH20
AG27	MDQ43		MA13	AG8
AG21	MDQ44		MA14	AH7
AH22	MDQ45		MA15	
AH26	MDQ46			AH18
AG26	MDQ47		MWE	
AF21	MDQ48			AH17
AD21	MDQ49		MRAS	AH19
AF24	MDQ50		MCAS	
AD24	MDQ51			AC18
AE20	MDQ52		MCS0	AC21
AD20	MDQ53		MCS1	AG17
AD23	MDQ54		MCS2	AG20
AE25	MDQ55		MCS3	
AF26	MDQ56			AB8
AF27	MDQ57		MCKE0	AB7
AD25	MDQ58		MCKE1	AH6
AD26	MDQ59		MCKE2	AG6
AG28	MDQ60		MCKE3	
AF25	MDQ61			AC19
AD27	MDQ62		MODT0	AD22
AD28	MDQ63		MODT1	AG18
			MODT2	AH21
			MODT3	
AG12	MDIC0	MAPAR_OUT		
AE12	MDIC1			
AH8				AG15
	MAPAR_ERR			



U?B

P2041			
G1			G5
G2	SD_RX02	SD_TX02	G6
E1	SD_RX02	SD_TX02	F3
E2	SD_RX03	SD_TX03	F4
A2	SD_RX03	SD_TX03	B4
A3	SD_RX04	SD_TX04	B5
B7	SD_RX04	SD_TX04	C8
A7	SD_RX05	SD_TX05	D8
B9	SD_RX05	SD_TX05	C10
A9	SD_RX06	SD_TX06	D10
B11	SD_RX06	SD_TX06	C12
A11	SD_RX07	SD_TX07	D12
	SD_RX07	SD_TX07	
A13			
B13	SD_RX10		C14
B15	SD_RX10	SD_TX10	D14
A15	SD_RX11	SD_TX10	D16
B19	SD_RX11	SD_TX11	C16
A19	SD_RX12	SD_TX11	C18
B21	SD_RX12	SD_TX12	D18
A21	SD_RX13	SD_TX12	C20
	SD_RX13	SD_TX13	D20
		SD_TX13	
SERDES			
		SD_REF_CLK1	D3
		SD_REF_CLK1	D4
E23			
F24	EMI2_MDIO		E17
	EMI2_MDC	SD_REF_CLK2	F17
		SD_REF_CLK2	
C2		RESERVE1	E5
B17	AGND_SRDS1	RESERVE2	E6
	AGND_SRDS2		
		RESERVE3	F14
		RESERVE4	F15
		SD_IMP_CAL_TX	E21
		SD_IMP_CAL_RX	F7
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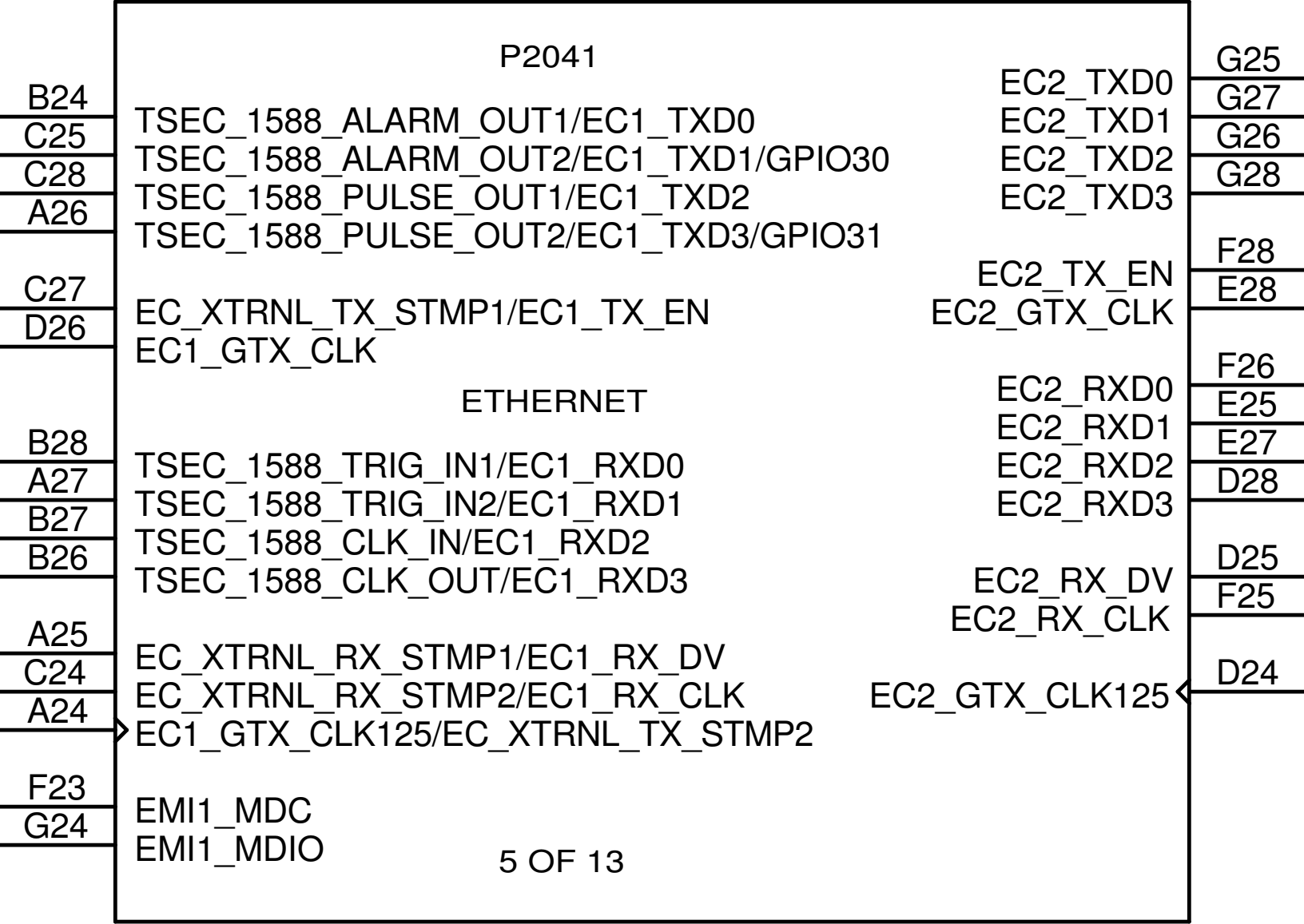


U?D

P2041			
J6	LAD00	LCS0	R5
J4	LAD01	LCS1	P7
K2	LAD02	LCS2	U4
K4	LAD03	LCS3	R1
L1	LAD04		
J5	LAD05		
N5	LAD06		
N2	LAD07		
N3	LAD08		
N1	LAD09	LWE0	M6
P4	LAD10	LWE1	M3
R7	LAD11		
T4	LAD12		
U2	LAD13		
T6	LAD14		
T7	LAD15	LBCTL	P1
K6	LA16		
K3	LA17	LALE	N6
P6	LA18		
P3	LA19	LGPL0/LFCLE	L5
P2	LA20	LGPL1/LFALE	K1
R3	LA21	LGPL2/LOE/LFRE	L6
T1	LA22	LGPL3/LFWP	J3
U1	LA23	LGPL4/LGTA/LUPWAIT/LPBSE	L2
R6	LA24	LGPL5	J1
T5	LA25		
T3	LA26		
T2	LA27	LCLK0	M4
U5	LA28	LCLK1	N7
U3	LA29		
V1	LA30		
V3	LA31		
L3	LDP0	LOCAL BUS INTERFACE	
M1	LDP1		



U?E



<Value>

U?F

		P2041		
V28	SCAN_MODE			
T28	TEST_SEL			
Y25	IRQ00			
AB27	IRQ01			V5
AB25	IRQ02		TEMP_ANODE	U6
AA26	DMA2_DREQ0/IRQ03/GPIO21		TEMP_CATHODE	
V25	DMA2_DACK0/IRQ04/GPIO22			
AA22	DMA2_DDONE0/IRQ05/GPIO23			
Y26	USB1_DRVVBUS/GPIO24/IRQ06			
AA23	USB1_PWRFAULT/GPIO25/IRQ07			
AC22	USB2_DRVVBUS/GPIO26/IRQ08			
AC27	USB2_PWRFAULT/GPIO27/IRQ09			V26
AB24	IRQ10/GPIO28/EVT7	SYSTEM LOGIC	EVT0	U27
AC24	IRQ11/GPIO29/EVT8		EVT1	U26
			EVT2	W24
Y24	IRQ_OUT/EVT9		EVT3	U24
L21	LP_TMP_DETECT		EVT4	AC25
M21	NC M21		IIC1_SCL	AC28
T24	TMP_DETECT		IIC1_SDA	W25
AB28	IO_VSEL0		IIC2_SCL	AA25
U23	IO_VSEL1	DMA1_DACK0/IIC3_SCL/GPIO16/SDHC_CD/M1DVAL/LB_DVAL	IIC2_SDA	AB23
AB21	IO_VSEL2	DMA1_DDONE0/IIC3_SDA/GPIO17/M1SRCID0/LB_SRCID0/SDHC_WP		AB26
Y23	IO_VSEL3	DMA1_DREQ0/IIC4_SCL/EVT5/M1SRCID1/LB_SRCID1/GPIO18/LB_MSRCID1		AC23
Y21	IO_VSEL4	IIC4_SDA/EVT6/M1SRCID2/LB_SRCID2/GPIO19		V24
T25	CKSTP_OUT			
			UART1_SIN/GPIO10	R26
T23	HRESET		UART1_SOUT/GPIO8	R23
U28	RESET_REQ		UART1_CTS/UART3_SIN/GPIO14	R25
T22	PORESET		UART1_RTS/UART3_SOUT/GPIO12	P24
			UART2_SIN/GPIO11	P27
			UART2_SOUT/GPIO9	P26
			UART2_CTS/UART4_SIN/GPIO15	P23
			UART2_RTS/UART4_SOUT/GPIO13	P25
W28	TDI		RESERVE5	AB19
AA28	TDO			
Y28	TCK		RESERVE6	AB17
W27	TMS			
Y27	TRST			
R28	SYSCLK		CLK_OUT	T27
P28	RTC		ASLEEP	R22

<Value>



AF23	GVDD01	P2041	OVDD01	W26
AF20	GVDD02		OVDD02	T26
AF17	GVDD03		OVDD03	T20
AF9	GVDD04		OVDD04	R20
AF6	GVDD05		OVDD05	P20
AC14	GVDD06		OVDD06	N20
AC13	GVDD07			
AB14	GVDD08	I/O MISC		
AB13	GVDD09		BVDD01	R4
AA15	GVDD10	POWER	BVDD02	N4
AA14	GVDD11		BVDD03	M7
AA13	GVDD12		BVDD04	L7
AA12	GVDD13		BVDD05	L4
AA11	GVDD14		BVDD06	K7
AA10	GVDD15		BVDD07	J7
AA9	GVDD16			
AA8	GVDD17			
			CVDD1	M20
			CVDD2	K20
			LVDD01	J20
			LVDD02	H20
			LVDD03	G20
			LVDD04	E26
			LVDD05	C26
			POVDD	U21
		RESERVE7		AB20
		RESERVE8		AB18



U?H			
AA21	VDD_CA_PL01	VDD_CB01	AA20
Y15	VDD_CA_PL02	VDD_CB02	AA18
Y13	VDD_CA_PL03	VDD_CB03	Y20
Y11	VDD_CA_PL04	VDD_CB04	Y18
Y9	VDD_CA_PL05	VDD_CB05	W20
W15	VDD_CA_PL06	VDD_CB06	W18
W13	VDD_CA_PL07	VDD_CB07	W16
W11	VDD_CA_PL08	VDD_CB08	V20
W9	VDD_CA_PL09	VDD_CB09	V18
V15	VDD_CA_PL10	VDD_CB10	U18
V13	VDD_CA_PL11	VDD_CB11	U16
V11	VDD_CA_PL12		
V9	VDD_CA_PL13		
U20	VDD_CA_PL14	PLATFORM	
U15	VDD_CA_PL15		
U13	VDD_CA_PL16	PLL/SENSE	
U11	VDD_CA_PL17		
U9	VDD_CA_PL18	POWER	
T19	VDD_CA_PL19		
T17	VDD_CA_PL20	AVDD_CC1	V7
T15	VDD_CA_PL21	AVDD_CC2	W22
T13	VDD_CA_PL22		
T11	VDD_CA_PL23		
T9	VDD_CA_PL24		
R19	VDD_CA_PL25		
R17	VDD_CA_PL26	AVDD_DDR	W6
R15	VDD_CA_PL27		
R13	VDD_CA_PL28		
R11	VDD_CA_PL29		
R9	VDD_CA_PL30	AVDD_PLAT	V22
P19	VDD_CA_PL31		
P17	VDD_CA_PL32	AVDD_SRDS1	C1
P15	VDD_CA_PL33	AVDD_SRDS2	A17
P13	VDD_CA_PL34		
P11	VDD_CA_PL35		
P9	VDD_CA_PL36		
N19	VDD_CA_PL37	SENSEVDD_CA_PL	H8
N17	VDD_CA_PL38	SENSEVDD_CB	G8
N15	VDD_CA_PL39	SENSEVDD_CB	AB16
N13	VDD_CA_PL40	SENSEVDD_CB	AA16
N11	VDD_CA_PL41		
N9	VDD_CA_PL42		
M19	VDD_CA_PL43		
M17	VDD_CA_PL44	VDD_LP	L20
M15	VDD_CA_PL45		
M13	VDD_CA_PL46		
M11	VDD_CA_PL47	SVDD01	H1
M9	VDD_CA_PL48	SVDD02	F2
L19	VDD_CA_PL49	SVDD03	E4
L17	VDD_CA_PL50	SVDD04	D1
L15	VDD_CA_PL51	SVDD05	C17
L13	VDD_CA_PL52	SVDD06	B20
L11	VDD_CA_PL53	SVDD07	B16
L9	VDD_CA_PL54	SVDD08	B12
K19	VDD_CA_PL55	SVDD09	B8
K17	VDD_CA_PL56	SVDD10	B3
K15	VDD_CA_PL57	SVDD11	B2
K13	VDD_CA_PL58	SVDD12	A22
K11	VDD_CA_PL59	SVDD13	A18
K9	VDD_CA_PL60	SVDD14	A14
J19	VDD_CA_PL61	SVDD15	A10
J17	VDD_CA_PL62	SVDD16	A6
J15	VDD_CA_PL63	SVDD17	A4
J13	VDD_CA_PL64		
J11	VDD_CA_PL65		
J9	VDD_CA_PL66		
H19	VDD_CA_PL67	XVDD01	H6
H17	VDD_CA_PL68	XVDD02	G4
H15	VDD_CA_PL69	XVDD03	F5
H13	VDD_CA_PL70	XVDD04	E22
H11	VDD_CA_PL71	XVDD05	D21
H9	VDD_CA_PL72	XVDD06	D15
G19	VDD_CA_PL73	XVDD07	D11
G17	VDD_CA_PL74	XVDD08	D7
G15	VDD_CA_PL75	XVDD09	C19
G13	VDD_CA_PL76	XVDD10	C13
G11	VDD_CA_PL77	XVDD11	C9
G9	VDD_CA_PL78	XVDD12	C4



AH15		P2041	GND044	Y12
AH12	GND001	GROUND #1	GND045	Y10
	GND002		GND046	Y8
			GND047	Y7
AG25			GND048	Y6
AG22	GND003		GND049	W23
AG19	GND004		GND050	W21
AG10	GND005		GND051	W19
AG7	GND006		GND052	W17
AG4	GND007		GND053	W14
AF14	GND008		GND054	W12
AF13	GND009		GND055	W10
AE27	GND010		GND056	W8
AE24	GND011		GND057	W5
AE21	GND012		GND058	W2
AE18	GND013		GND059	V27
AE15	GND014		GND060	V23
AE11	GND015		GND061	V21
AE8	GND016		GND062	V19
AE5	GND017		GND063	V17
AE2	GND018		GND064	V16
AD15	GND019		GND065	V14
AD12	GND020		GND066	V12
AC26	GND021		GND067	V10
AC20	GND022		GND068	V8
AC17	GND023		GND069	V6
AC5	GND024		GND070	V4
AB22	GND025		GND071	V2
AB15	GND026		GND072	U25
AB12	GND027		GND073	U22
AB11	GND028		GND074	U19
AB10	GND029		GND075	U17
AB9	GND030		GND076	U14
AB2	GND031		GND077	U12
AA27	GND032		GND078	U10
AA24	GND033		GND079	U8
AA19	GND034		GND080	U7
AA17	GND035		GND081	T21
AA7	GND036		GND082	T18
AA5	GND037		GND083	T16
Y22	GND038		GND084	T14
Y19	GND039		GND085	T12
Y17	GND040		GND086	T10
Y16	GND041		GND087	T8
Y14	GND042		GND088	R27
	GND043			



R24		P2041		
R21	GND089			K5
R18	GND090		GND135	J21
R16	GND091		GND136	J18
R14	GND092		GND137	J16
R12	GND093		GND138	J14
R10	GND094		GND139	J12
R8	GND095		GND140	J10
R2	GND096		GND141	J8
P21	GND097		GND142	J2
P18	GND098		GND143	H25
P16	GND099		GND144	H21
P14	GND100		GND145	H18
P12	GND101		GND146	H16
P10	GND102		GND147	H14
P8	GND103		GND148	H12
P5	GND104		GND149	H10
N25	GND105		GND150	H4
N21	GND106		GND151	H3
N18	GND107		GND152	G22
N16	GND108	GROUND #2	GND153	G21
N14	GND109		GND154	G18
N12	GND110		GND155	G16
N10	GND111		GND156	G14
N8	GND112		GND157	G12
M18	GND113		GND158	G10
M16	GND114		GND159	F27
M14	GND115		GND160	F22
M12	GND116		GND161	E24
M10	GND117		GND162	D27
M8	GND118		GND163	D23
M5	GND119		GND164	C23
M2	GND120		GND165	B25
L18	GND121		GND166	B23
L16	GND122		GND167	
L14	GND123			A23
L12	GND124		GND168	
L10	GND125			
L8	GND126			
K21	GND127			
K18	GND128			
K16	GND129			
K14	GND130			
K12	GND131			
K10	GND132			
K8	GND133			
	GND134			



U?K

H2	SGND01	P2041 GROUND #3	XGND01	H5
F1	SGND02		XGND02	G3
E3	SGND03		XGND03	F21
D17	SGND04		XGND04	F6
D2	SGND05		XGND05	D19
C3	SGND06		XGND06	D13
B22	SGND07		XGND07	D9
B18	SGND08		XGND08	C21
B14	SGND09		XGND09	C15
B10	SGND10		XGND10	C11
B6	SGND11		XGND11	C7
B1	SGND12		XGND12	C5
A20	SGND13			
A16	SGND14			
A12	SGND15			
A8	SGND16			
A5	SGND17			
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<Value>



U?L

P2041

W4	NC03
W3	NC04
W1	NC05
H7	NC06
G7	NC07
F20	NC08
F19	NC09
F18	NC10
F16	NC11
F13	NC12
F12	NC13
F11	NC14
F10	NC15
F9	NC16
F8	NC17
E20	NC18
E19	NC19
E18	NC20
E16	NC21
E15	NC22
E14	NC23
E13	NC24
E12	NC25
E11	NC26
E10	NC27
E9	NC28
E8	NC29
E7	NC30
D22	NC31
D6	NC32
D5	NC33
C22	NC34
C6	NC35

MISC/RESERV



U?M

M25	USB1_VBUS_CLMP	USB2_VBUS_CLMP	J25
L28	USB1_UDM	USB2_UDM	L26
K28	USB1_UDP	USB2_UDP	K26
M27	USB1_UID	USB2_UID	J27
P22	USB_CLKIN		
M23	USB1_VDD_3P3	USB2_VDD_3P3	J23
L22	USB1_VDD_1P0	USB2_VDD_1P0	K22
L24	USB1_VDD_1P8_DECAP	USB2_VDD_1P8_DECAP	K24
L23	USB1_IBIAS_REXT	USB2_IBIAS_REXT	K23
M28	USB1_AGND01	USB2_AGND01	M26
M24	USB1_AGND02	USB2_AGND02	L25
M22	USB1_AGND03	USB2_AGND03	K25
L27	USB1_AGND04	USB2_AGND04	J26
K27	USB1_AGND05	USB2_AGND05	J24
J28	USB1_AGND06	USB2_AGND06	J22

USB

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<Value>