



U?

1	VDD1		
7	VDDA		
8	VSSA	JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/PTA0/UART0_CTS/UART0_COL/FTM0_CH5	12
		JTAG_TDI/EZP_DI/TSI0_CH2/PTA1/UART0_RX/FTM0_CH6	13
		JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/PTA2/UART0_TX/FTM0_CH7	14
		JTAG_TMS/SWD_DIO/TSI0_CH4/PTA3/UART0_RTS/FTM0_CH0	15
		NMI/EZP_CS/TSI0_CH5/PTA4/FTM0_CH1/LLWU_P3	16
11	VBAT		
		EXTAL0/PTA18/FTM0_FLT2/FTM_CLKIN0	17
6	VREGIN	XTAL0/PTA19/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	18
5	VOUT33		
4	USB0_DM		
3	USB0_DP		
		ADC0_SE8/TSI0_CH0/PTB0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA/LLWU_P5	20
10	EXTAL32	ADC0_SE9/TSI0_CH6/PTB1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	21
9	XTAL32		
19	RESET		
		ADC0_SE15/TSI0_CH14/PTC1/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0/LLWU_P6	22
2	VSS1	ADC0_SE4B/CMP1_IN0/TSI0_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS	23
		CMP1_IN1/PTC3/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK/LLWU_P7	24
		PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT/LLWU_P8	25
		PTC5/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT/LLWU_P9	26
		CMP0_IN0/PTC6/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK/LLWU_P10	27
		CMP0_IN1/PTC7/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS	28
33	EPAD		
		PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN/LLWU_P14	29
		ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT	30
		ADC0_SE7B/PTD6/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0/LLWU_P15	31
		PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	32